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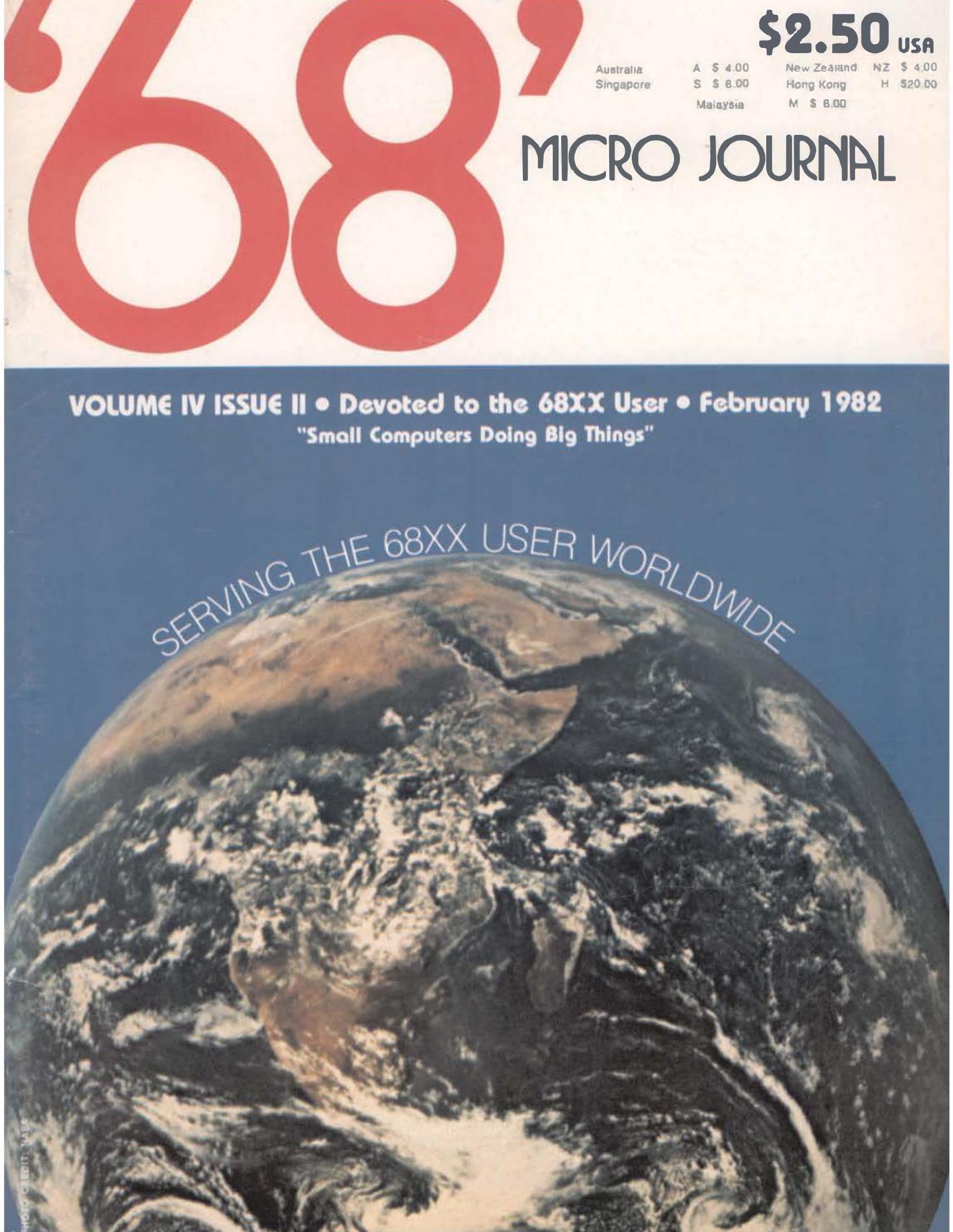
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- Supports most of Jensen and Wirth specification
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- Implements scalar, subrange and structured data types
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- Dynamic storage allocation
- Ability to call other Pascal programs
- FLEX version may call assembly programs
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Pascal on diskette for 5" and 8" 6809 FLEX is available for \$200.00 The 5" version requires two disk drives.

The UniFLEX version is \$300.00 and includes one year of maintenance. All orders should include 3 percent for postage and handling (10 percent on foreign orders).

™FLEX and UniFLEX are trademarks of Technical Systems Consultants, Inc.



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(Letters to the Editor for Publication) All 'letters to the Editor' should be substantiated by facts. Opinions should be indicated as such. All letters must be signed. We are interested in receiving letters that will benefit or alert our readers. Praise as well as gripes is always good subject matter. Your name may be withheld upon request. If you have had a good experience with a 6800 vendor please put it in a letter. If the experience was bad put that in a letter also. Remember, if you tell us who they are then it is only fair that your name 'not' be withheld. This means that all letters published, of a critical nature, cannot have a name withheld. We will attempt to publish 'verbatim' letters that are composed using 'good taste.' We reserve the right to define (for '68' Micro) what constitutes 'good taste.'

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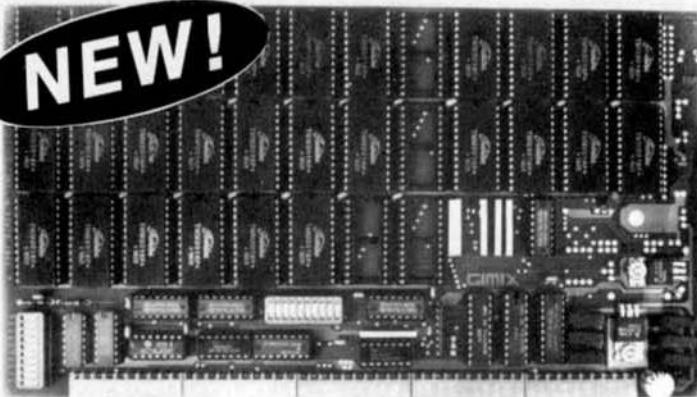
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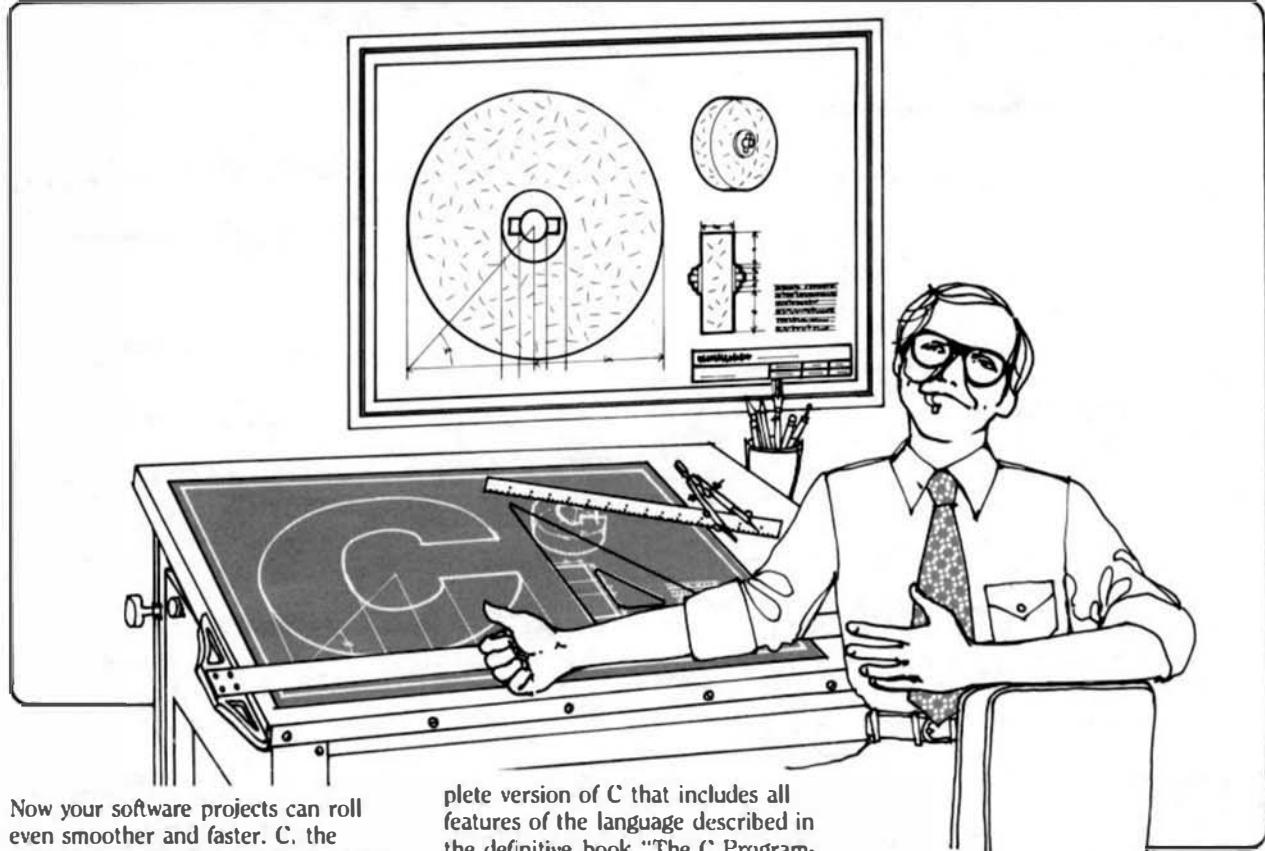
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Few languages can match C's outstanding ability to produce fast, compact native code. In fact, it is one of very few languages that is truly efficient enough to be used to produce operating systems, critical real-time programs, and compilers. Because of the richness and variety of C operators and the way they naturally combine, complex functions require less code. Plus the 6809 architecture makes it a superior C machine.

Complete and standard . . .

Microware's new C compiler is a com-

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The bridge to Unix and the future . . .

Because Microware's C compiler has essentially all features of Unix C, and because the OS-9 operating system is a Unix-type operating system, C programs readily move between OS-9 and Unix. And it is becoming apparent that C will be the preferred programming language for all popular 16-bit microcomputers. As a result, software written in C is inherently protected against processor obsolescence and is assured port-

ability to all latest-generation microprocessors including the 68000.

Plus the OS-9 connection . . .

C is the latest member of the broadest line of 6809 software tools in the industry: Microware's OS-9 family. All OS-9 system functions are directly callable from C programs. The C compiler utilizes the standard OS-9 Text Editor and Assembler, and can process data files used by Basic®9, Pascal, and Cobol.

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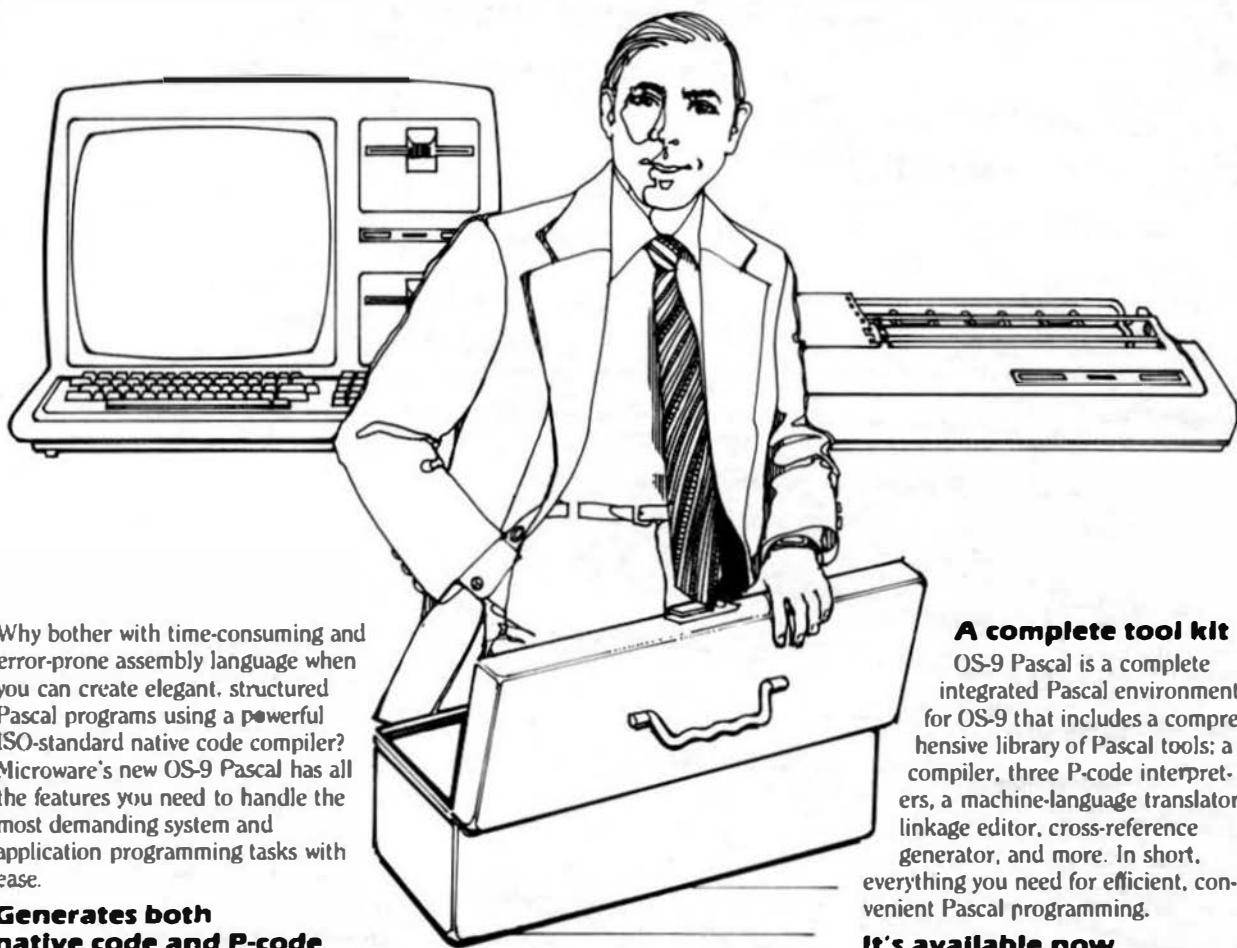


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OS-9 PASCAL™

A New Programming Tool For Experts



Why bother with time-consuming and error-prone assembly language when you can create elegant, structured Pascal programs using a powerful ISO-standard native code compiler? Microware's new OS-9 Pascal has all the features you need to handle the most demanding system and application programming tasks with ease.

Generates both native code and P-code

With OS-9 Pascal you don't have to make that difficult choice between easy-to-use P-code Pascal or fast native-code Pascal. You can compile your Pascal program to pure 6809 assembly language source code. OS-9 Pascal performs extensive local and global code optimization which results in incredibly fast and compact machine language programs. Or if you prefer, OS-9 Pascal can generate P-code for interpretive execution to simplify program debugging and testing. There's also a Virtual Memory P-code Interpreter that can run huge Pascal programs that other microcomputers can't touch. In fact, you can run programs using any combination of P-code, compiled machine language, or handwritten assembly language procedures.

ISO Standard Pascal Plus

OS-9 Pascal conforms to the ISO industry standard for Pascal, so you are assured of portability to or from any other computer that uses standard Pascal. OS-9 Pascal protects your software investment and gives you access to a vast body of existing Pascal software. Beyond the standard, we've added natural extensions to OS-9 Pascal to make it even more versatile, such as: relaxed identifier syntax; separate procedure compilation; random access file and interactive I/O; bitwise logical operators; runtime error handling; and much more. And because it runs under OS-9, it is inherently multiuser and multitasking.

A complete tool kit

OS-9 Pascal is a complete integrated Pascal environment for OS-9 that includes a comprehensive library of Pascal tools: a compiler, three P-code interpreters, a machine-language translator, linkage editor, cross-reference generator, and more. In short, everything you need for efficient, convenient Pascal programming.

It's available now

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Includes Editor and Assembler

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It's easy to create databases and reports with DATAMAN+. Full editing capability has been added to make it a snap. DATAMAN+ is the first truly RANDOM DBM system to allow any size record and any number of fields.

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User configurable to virtually any terminal with at least a 64 character line and 8600 baud capability.
 Available soon for FLEX 9.

PRICE \$89.95

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BY Chuck Ester, Ph.D.
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Full Directory program! DINFO fills the screen with all the information about your disk, such as: Name, Date, # of Files, Largest, Smallest, Free space, Linked filename, Format of the disk plus more.

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Flex User Notes

BY: RONALD W. ANDERSON
3540 STRUBRIDGE COURT
ANN ARBOR, MI 48105

MORE ON PRIME NUMBERS?

Since writing a few columns ago about the Prime number program by Brian Bailey that executes the primes to 10000 in 2 seconds, I've had a couple of other reports and listings appear in my mailbox. Doug Beck sent me a listing of a Sieve method program that finds the primes in 0.51 seconds on a 1 MHz 6809 system. Andrew Wood sent two listings all the way from Australia. The first, he indicates is quite straightforward, and it finds the same primes in 0.256 seconds. The second has been optimized by using single byte arithmetic where the numbers are small enough, etc. It finds the same primes in 0.187 seconds! (These times are all without listing the results of course.) The sieve method requires all the numbers to be in an array in memory, so the results may be printed out after the calculations are done. Andrew indicates that "as a matter of interest, an IBM assembly language version of this same algorithm ran in under 5 milli-seconds on an IBM 3033!" He also indicates that he thinks these times can be bettered somewhat, but not by another factor of 10!

Since receiving the above, I received a letter from Alan Fowler in Australia. He has submitted an article to '68' Micro Journal that will probably be published in this issue (or the last one). He has written a program to find the primes less than 10000 in 146 milliseconds. Apparently my mention of the 2 second program set quite a few people in motion to try to better that time. Since Mr. Fowler's program will be published with his article, I won't give details here.

All these listings, which I have yet to compare in detail, sort of pushed me into a project I had been thinking about for a while. A long time ago, I did a Sieve prime program in BASIC. (Perhaps back far enough that I didn't really optimize things as much as possible.) Last night, I decided to translate the program to Pascal. I needed an "integer square root" and it hardly seemed reasonable to have to include the whole floating point and Scientific function packages of the runtime for one line of code, so I wrote a simple Newton's method square root function that returns the nearest integer to the actual square root (or more correctly, one of the two nearest integers). I compiled the program in Pascal, using OmegaSoft and Lucidata. Remember that this method requires all the calculations to be done before we know which numbers in the array are the primes, and the following times are for the calculation part only. Lucidata: 18 seconds, OmegaSoft: 3.5 seconds, and TSC Pascal: 2 seconds flat! The Pascal listing is included here.

I have to admit that looking at the programs from Andrew Wood showed me that the array need not be initialized with the odd numbers, but simply with a boolean "flag". When the "crossing out" is completed,

one can calculate the numbers from the value of each of the array indices where the contents are "true". I'm not quite certain that this isn't cheating a bit, because the calculation of the numbers from the array indices is really part of the calculation of the prime numbers, yet it is not included in the calculation time, but is part of the "output" time. However, using an array of BOOLEAN, reduces the memory requirement by half, since Boolean values are single bytes. I was able to calculate the primes to a limit of 30,000 by increasing the array dimension to 15,000. The calculation ran 12 seconds with the OmegaSoft complied code.

The arrival of Alan Fowler's letter made me decide to try the primes in Assembler using the sieve algorithm that I used in the Pascal Program mentioned above. A rather direct translation, using FLEX routines wherever possible, after a bit of study and optimization of the main loop, finds the primes in 176 milliseconds. My first try ran 241 milliseconds. After getting the program this far, I looked at Alan Fowler's approach, which is rather similar. I decided that I would have quite a time reducing my time below that 146 millisecond record, so I have quit for a while. In order to get my time down I used direct page addressing for a few variables. I don't think a program that simply indicates that it has done something, is quite complete unless it can output its results, so I added the routines necessary to scan the array of numbers left by the prime finder, and output the numbers in 10 columns per line. I also added a prime count output. I suppose that I will sometime again pick up the problem and make further improvements, but for the time being, I'm satisfied that the rule that Assembler is about 10 times faster than the best compilers, holds pretty well.

FILES WITH LUCIDATA PASCAL

I've received a few letters from some other Pascal purchasers that don't think the Lucidata Manual is the greatest in a few areas (though the feeling is unanimous that the demonstration programs are very helpful). In particular, the area that describes the use of files suffers from the lack of a specific discussion of how a command line specifies files and indicates how those files are linked to the internal filenames of the Pascal program. This will be an attempt to clarify that information somewhat. I note with some interest that the manual produced for version 2 of the compiler contains considerably more information on this subject than the version 3 manual. The discussion that is in the manual is in section 4.2 in both cases. To expand on the discussion a bit, let's start with BASIC. You may recall with most BASIC interpreters, that you open a file by its "directory" file name, and at that point, you give it a file number by which it is referenced thereafter in the program. Pascal does roughly the same thing, except that it uses names rather than numbers as internal references.

Suppose you write a program in Pascal that is to read a file and perform some operation on it and write the result to a

second file. You might decide that you are going to call the files INDATA and OUTDATA respectively in the program. The Pascal Program's first line would then be: PROGRAM OPERATE (INDATA, OUTDATA); . Actually to be compatible with standard Pascal, you would have to declare two other filenames in the parameter list, those of INPUT, and OUTPUT. Lucidata's compiler always provides these two channels to the terminal, and it doesn't require their presence in the parameter list, but it doesn't object to their being there either. The full line for a standard Pascal implementation would then be: PROGRAM OPERATE (INPUT, OUTPUT, INDATA, OUTDATA); . You will have to declare two variables INDATA, OUTDATA : FILE OF CHAR; Somewhere in the program you would have to open these two files: RESET (INDATA); {OPEN FOR READ} REWRITE (OUTDATA); {OPEN FOR WRITE}

How do we ever get the disk filename involved? When you run the program you associate the disk filenames with the internal filenames in the command line. ++RUN OPERATE DATA.TXT NEWDAT.ATXT. Note that the ORDER of specifying the filenames in the command line determines which is associated with INDATA (the first one) and OUTDATA (the second one). To write to the terminal you use WRITE (VARIABLENAME); To write the contents of that variable to the disk file you would use WRITE (OUTDATA, VARIABLENAME);. Reading data from a file proceeds similarly. The only caution is that if you have a file full of integer numbers, they must be read into an integer variable etc.

Perhaps it will help you to understand files if you realize that INPUT and OUTPUT are files of CHAR. They are the channels to the standard I/O device (the terminal). It is easy to output to a printer too. The printer becomes another file of CHAR open for output only. Lucidata has conveniently linked the FLEX printer drivers to Pascal as device #2. In the program first line, you use PROGRAM TESTPRINT (INPUT, OUTPUT, PRINTER); Of course, PRINTER could be whatever you want to use as an internal filename. Just 'P' would do if you wish. Inside the program you must declare the variable PRINTER : FILE OF CHAR; and open the file for write with REWRITE (PRINTER);. The command line is simply RUN TESTPRINT #2. If you leave out the #2, any output indicated in the program for the printer, will default to the terminal. Of course you output to the printer by WRITE (PRINTER, "HI THERE");. For this to work properly without having used the printer previously, you will have to overlay a location in Pascal (DEVINT, for DEVICE INITIalize), with the address of the printer initialization routine (\$CCCO for FLEX 9). With this much of a start, the manual should make sense in that area now.

Lucidata has an area in memory set up as a table of addresses of device drivers. DEVTAB is the name they gave this table. Entry #2 has been pre set to be a jump vector to \$CCE4 for FLEX9, or \$ACE4 for FLEX2 version. You may overlay the other devices in the table with addresses of your assembler generated driver software. You could, for example, set up device #4 to be a modem. Lucidata covers the procedure to get the various devices (usually ports)

initialized properly by writing a program (subroutine) in assembler to initialize all the devices you are going to use, and overlaying DEVINT with the address of that routine.

The method described here to link internal and disk filenames is probably the "most standard" for Pascal implementations, but it is not the only way among all the implementations available. The concept of an internal and external filename is quite standard. How they are associated seems to vary a bit. Some Pascals allow the association in the command line only. Others allow prompting for the filename or "building it in" to the program (OmegaSoft in particular), and still others allow either method (TSC).

If I receive a letter or two expressing interest in the pursuit of some of the more obscure features of Pascal, and Lucidata's implementation in particular, I will discuss the topics of SET and POINTER variables in Pascal and the area of interface to assembler routines in some future column.

READER RESPONSES

Generally, when I've asked for responses in the past, I've received no more than three letters. For example, I once mentioned having written a rather complete floating point math package for the 6800, and then having translated it for the 6809. I offered to publish it if any readers were interested. I received two letters expressing interest at the time, and one recently. Since it hardly seems fair to take up a whole month's column with information of interest to three readers, I didn't follow through on that particular offer. The point is that if you really do want to see some continued discussion of the more complex parts of Pascal, send me a card, at least.

6809 ASSEMBLER THOUGHTS

I've had some thoughts about my approach to learning the 6809 instruction set. When I first received my 6809, I translated a few utilities, making use of the Y index register to avoid the continual pointer swapping (STX, LDX) instructions that were required by the 6800 because there was only one index register. I learned how to use the O register and remember being disappointed by the lack of some of the O register instructions such as ADCD, ASLD, CLRD, and related ones. Later, in the process of rewriting my 6800 binary floating point math package, I found that having a user stack eliminated a lot of problems with passing parameters to subroutines. If you use the system stack, there is always a return address in the way on the stack. You can't pull the variables with the 6800 unless you first pull the return address and save it for a later push. With the 6809, you can use the user stack, and you needn't pull the variables to perform the operations on them because you can use the user stack pointer as an index pointer also. LDD 2,U will get the 16 bit parameter two bytes down in the stack, for example.

Having gotten that far, frankly, I didn't ever go back and see what could be done with the register offset indexing

(except once to index into a table of values using X as the pointer to the start of the table, and the value in B just previously calculated, as the offset. The point is that I wonder how many of us are really taking advantage of the power of the 6809. Have you used indirect addressing and the combinations of indirect and indexed modes? I've seen some of these modes appearing in compilers that I have been looking at, so I guess we are getting the benefit of the 6809 anyway when we use some of the better software that is now available for our systems. Perhaps I could take a few paragraphs each time and discuss the application of some of those more interesting instructions to solve some problem in assembler. Have you tried a 16 by 16 bit multiply using the MUL instruction? It is really quite easy to do, and ultra fast.

Have you discovered how nice the user stack is for a "work area"? I have included two listings here that might make the point. Way back, I learned something about assembler programming and using FLEX subroutines by writing a simple program to allow filling a specified area in memory with any specified byte. The listing is included here as FILL. This listing is simply a 6800 version with minimum changes to make it 6809 code, assembled by the 6809 assembler. Somewhere (I think it was a TSC listing) I saw the neat trick of letting the assembler calculate the size of the program. You will notice that it is \$2F or decimal 47 bytes long. The other listing, FILLO9 has been written to use the user stack for a work area. PSHU X is a two byte instruction, and it replaces STX FIRST, STX LAST, which are three byte instructions. In addition, all the indexed instructions are two bytes. The designers of the 6809 really gave us a nice tool when they included the ability to "index off of U" as well as push and pull from the user stack. You will notice that this program is \$24 or 36 bytes long. If your system startup file were to set the user stack at MEMEND, you wouldn't need that instruction here, and the program would be 34 bytes long, a considerable saving by using the '09 as it was intended.

If you had a program with many variables, keeping track of them as 0,U, 2,U, etc. would be most confusing. Fortunately, there is a simple way to use variable names again. The most straightforward way, in this example is to use an equate to assign a name to 0, 2, 4, etc. I've done that in the program FILLO9. Remember that the first variable to get pushed on the stack has the highest offset. By setting FILBYT EQU 5, LAST EQU 2, FIRST EQU 0, you may make the substitutions in the program. (Remember FILBYT is the low order byte of a double byte returned by FLEX GETHEX routine). The only "clutter" is the ",U" that follows the variable names. Actually there is an even "slicker" way to assign the offsets to the variable names, using the SET facility of the TSC Assembler. It would work something like this for FILL.

```
SET SAVE EQU *
ORG 0
LAST RMB 2
FIRST RMB 2
ORG EQU SAVE
```

SAVE is a dummy variable whose purpose it is only to hold the assembler current address counter while the variables are being defined as offset from zero. The SET feature of the assembler may be used to assign SAVE another value elsewhere in the program if more variables are defined. I wish I could take credit for this slick way of assigning variable names on the user stack, while letting the assembler do the work of keeping track of them, but the technique comes from a Monitor source listing supplied by Creative Microsystems with some of their 6809 hardware.

There is another surprising benefit to using the user stack for variables. I was going to go back and look at this utility and make it position independent, but I found that it already is! The only absolute references are to FLEX, and these should be absolute, since FLEX doesn't move when the utility is loaded in a different place. You might not have noticed, but when the stack is used for variables, the importance of direct page addressing fades considerably. The 6809 has the ability to move direct addressing to any page of memory. I wonder how many of us have taken advantage of that?

A few paragraphs up, I asked for reader response concerning more Pascal information. I'll add another question to my poll. Would you like to see more examples of 6809 assembler programming techniques such as this? Perhaps I am a bit simple minded, but I find that I learn faster from a few simple examples than a highly technical description of some nice programming technique. If you like this approach, I will continue it.

```
* FORMAT: FILL,BYTE,FIRST ADR.,LAST ADR.
*
* BYTE IS HEX BYTE DESIRED TO FILL MEMORY
* FIRST AND LAST ADDRESSES ARE INCLUSIVE.
*
* EXAMPLE: FILL,00,0100,01FF
*
* RESULT: FILL ALL MEMORY FROM $100 TO
* $1FF WITH '00'
*
```

*SYSTEM EQUATES

CD03	WARM	EQU	\$CD03
CD42	GETHEX	EQU	\$CD42
C100		ORG	\$C100
			*
C100 20 01	FILL	BRA	START
C102 01	VN	FCB	1
			*
C103 BD CD42	START	JSR	GETHEX
C106 BF C12B		STX	FIRST
C109 B6 C12C		LDA	FIRST+1
C10C 34 02	PSHS	A	
C10E BD CD42	JSR	GETHEX	
C111 BF C12B		STX	FIRST

C114	B0	CD42	JSR	GETHEX
C117	30	01	LEAX	I,X
C119	BF	C120	STX	LAST
C11C	35	02	PULS	A
C11E	8E	C12B	LDX	FIRST
;				
C121	A7	80	FLOOP	STA ,X+
C123	BC	C120	CMPX	LAST
C126	26	F9	BHE	FLOOP
C128	7E	CD03	JMP	WARMs
;				
; VARIABLES				
;				
C12B		FIRST	RMB	2
C12D		LAST	RMB	2
;				
002F	SIZE	EQU	I-FILL	
	END		FILL	

0 ERROR(S) DETECTED

FORMAT: FILL,BYTE,FIRST ADR.,LAST ADR.
 ;
 ; BYTE IS HEX BYTE DESIRED TO FILL MEMORY
 ; FIRST AND LAST ADDRESSES ARE INCLUSIVE.
 ;
 ; EXAMPLE: FILL,00,0100,01FF
 ;
 ; RESULT: FILL ALL MEMORY FROM \$100 TO
 ; \$1FF WITH '00'
 ;

SYSTEM EQUATES

CD03	WARMs	EQU	\$CD03
CD42	GETHEX	EQU	\$CD42
CC2B	MEMEND	EQU	\$CC2B
0000	LAST	EBU	0
0002	FIRST	EQU	2

C100		ORG	\$C100	
;				
C100	20	01	FILL	BRA START
C102	01		VN	FCB 1 FLEX VERSION NUMBER
;				
C103	FE	CC2B	START	LDU MEMEND PUT STACK AT MEMEND
C106	B0	CD42	JSR	GETHEX GET BYTE TO FILL
C109	34	10	PSHS	X PUSH ON SYSTEM STACK
C10B	BD	CD42	JSR	GETHEX GET FIRST FILL LOCATION
C10E	36	10	PSHU	X
C110	BD	CD42	JSR	GETHEX GET LAST FILL LOCATION
C113	36	10	PSHU	X
C115	A6	42	LDX	FIRST,U STARTING LOCATION
C117	35	06	PULS	D GET BYTE TO FILL AS LOW ORDER
C119	E7	80	FLOOP	STB ,X+
C11B	AC	C4	CMPX	LAST,U
C11D	23	FA	BLS	FLOOP
C11F	33	44	LEAL	4,U REMOVE PARAMETERS FROM STACK
C121	7E	CD03	JMP	WARMs
;				

COLOR User Notes

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 Norcross, GA 30071

Yep, ANOTHER address. Well, this one is HOME BASE! I've been TDY for several months; now back home again (would you believe how much stuff you can get stuffed into a small, temporary, room?). It's finally moved back; the COLOR COMPUTER is back "on line"; and I'm running late on getting this column in. (Oh yeah, I've also changed jobs in the middle of all of this, too.) It's been a hairy few weeks climaxing an unbelievable ten months; maybe things can get back to normal again. All this is a "round about" apology to all who have written and not received a reply; letters are arriving with three different address changes, which takes a WHILE to catch up with me. Also, I have a dozen or so that I just haven't had a chance to answer. If you don't get an answer, drop me another letter, if you will, and I'll get back with you ASAP.

Meanwhile, back to business. Still not much from Radio Shack on the EDITOR/ASSEMBLER or the SCRIPSPIT Wordprocessor; both are still "due" shortly. The Disk Systems haven't started appearing again, either. I look for a lot of Radio Shack Software to start appearing early this spring, and the arrival of the Disk Systems, in quantity, will release a bunch of non-Radio Shack Software also. I know of several excellent Programs that are about ready to go, but release is being held up so they can be made compatible with the RS DOS. I don't think there will be too much problem marrying this COLOR Computer FLEX Operating System to RS DOS, either. If you've been following this Column, you know I am running Steve Odneal's conversion of FLEX on my COLOR COMPUTER, and it's still operating GREAT. We have identified a few "bugs", but nothing that has caused any real problems, and all have been easy to fix without any major revisions to the conversion. All in all, Steve has done an outstanding job.

My personal dream is to get this Computer running a "C Compiler"; from what I can find out about it, I think C will be the Programming Language of the future (all right; ALL RIGHT! I DON'T have FORTH running on it, either - I'd also be interested in that too). As we'll see shortly, we are just beginning to tap some of the potential of this little "machine".

First, let's look at the MC6883 Synchronous Address Multiplexer that eliminates a board full of chips and adds MORE power to the already extremely powerful 6809E CPU chip which is in this Computer. The "SAM", which sure beats "Synchronous Address Multiplexer", has been discussed in several publications, normally in reference to the Video Capabilities of the Color Computer. It DOES add some more "graphics" modes to the Computers' stable, but that is sort of a fallout or overflow of its real job, that of CONTROLLING the COMPUTER. The CPU does all of the "bit banging", but the SAM is the "system coordinator". Its job is to keep

0024 SIZE EQU I-FILL
 END FILL

0 ERROR(S) DETECTED

everybody talking to each other, making sure the RAM gets refreshed (without getting in the way of anything else), allocating and controlling the "screen memory", providing the SYSTEM timing, etc., etc., etc. As you can see, the whole system would break down without it. Normally, the CPU is called the "heart" of a computer, but, in this computer, it is just the "brain" (and we all know that can't be very important, because most of us get along pretty well with very little). The SAM has to be the "heart" of the Color Computer.

Motorola has Data Sheets on the MC6883 SAM now, and an Application Note is in the works. I won't waste valuable magazine space with a copy of the Data Sheet, which is fairly extensive. Also, the following discussion will be kept general; this will provide a look at SOME of the SAM's relatively unknown capabilities. Those interested in further study will find the Data Sheet to be fairly complete, and can use their imagination in utilizing that info. This discussion provides:

- 1—The non-technical Color Computer readers with an idea of how their machine works;
- 2—The general SS-50C Buss users with information about a new LSI that is available;
- 3—Some insight into why some of us are so enthusiastic about the Radio Shack Color Computer;
- 4—Some thoughts on "possibilities";

5—And provides a look at a fantastic Product available for this Computer from ATOMTRONICS in Los Alamos, N.M.

The MC6883/SN74LS783 (yep, it's a member of the 74LS family) Synchronous Address Multiplexer is just that; it marries the MPU (6809E in the Color Computer), VDG (Color Video Display Generator), and all Memory into an effective, compact Computer SYSTEM. Since it DOES have control of ALL memory, it can accomplish many tasks which would require extensive design and numerous chips to implement without the SAM. If you look at the physical "internals" of the Color Computer, you are immediately struck by the low parts count. The System Block Diagram shown on the MC6883 Data Sheet is the Block Diagram of the Color Computer.

The SAM effectively resides in the \$FFCO to \$FFFF Memory area, with the area between \$FFEO to \$FFF2 reserved for future expansion by Motorola. They also recommend that the I/O area between \$FF60 and \$FFCO be reserved for future expansion of Control Registers and Special I/O applications. Remember, the 6883 controls access to the memory; this means that it expects certain memory locations to contain specific types of access; primarily it expects the I/O (Input/Output) region to be from \$FF00 to \$FF60. Some may feel this limits design freedom; personally, I think it will prove to be a boon to Computer Designers because we now have sort of a "standard" I/O area specified, and it is high enough in memory to allow a lot of freedom with RAM utilization. Although the 6883 was obviously designed to support the 6809 series chips, it sure is not restricted to use with them only. The SAM's Control Registers are not utilized in the normal method of reading or writing a byte to an address location, but are controlled by the PROCESS of writing TO a SPECIFIC location. The information "written" is meaningless; a write to an EVEN numbered address CLEARS the bit and a write to an ODD numbered address SETS the bit (for example, writing to location \$FFDE clears the "Map Type" to zero, while writing to location \$FFDF sets the "Map Type" to one). This allows simple loop routines to provide the SAM Control, in most cases.

The "Map Type" referred to above provides one of the more powerful, and as yet little used, functions of the SAM. This is the MEMORY Map Type; Memory Map

Type #0 (zero), and Memory Map Type #1. Memory Map Type #0 is used for "ROM Based Systems" (such as the Color Computer), and the Memory Map Type #1 is used primarily for "RAM Based Systems". Three Device Select pins are provided; S0, S1, and S2. These can be decoded to allow switching between ROM and RAM in the same address areas (for example, the 16K of BASIC ROM's in the Color Computer at \$8000 thru \$BFFF can be switched off and RAM activated in this area). The Type #0 Memory Map also allows "Page Switching" of the bottom 32K of memory, providing an indirect addressing range of 96K on the Color Computer. This is accomplished by "folding" 64K memory for the two pages, leaving the ROM's in their present position, and adding RAM from \$C000 to \$FF00 thru the Cartridge Slot. (I have just implemented this setup on my Color Computer, and it WORKS. We'll see how shortly, and look at more details and information next month.) The Map Type #1 provides a full 64K RAM (less the \$FF00 and up area, which should be reserved for the SAM and I/O). Stick a ROM at the top of memory, say from \$F800 up, leaving "holes" for the SAM and I/O, and you have 62K of continuous memory from \$0000 up. This is what Mike Wolf of ATOMTRONICS has accomplished with his 64K/WOLFBUG Mod Kit.

QUICK LOOK:
ATOMIC CITY ELECTRONICS (ATOMTRONICS)
3195 Arizona
Los Alamos, N.M. 87544

ATOMTRONICS has several products out for the Color Computer. The most expensive, and the most valuable, is the 64K RAM KIT with the WOLFBUG Monitor in a 2716 ROM. This KIT sells for \$349.95, and includes a small EPROM Adapter Board, the Wolfbug ROM, a set of 8 MCM6665 64K RAMs, and a RAM Adapter Board; along with solder, extra wire for some required jumpers, full instructions, etc. This Installation requires a few changes inside the Computer, so will void the Radio Shack warranty when installed. Also, even though installation is fairly simple and the instructions complete, I would not recommend you attempt the mod yourself if you do not have a GOOD, isolated, 1/16" tip soldering iron, and are experienced in working with high density Solid State boards. The kit can be installed by an experienced friend, or, normally, by the Dealer from which you purchase the kit. All of ATOMTRONICS products are available through

SOUND CENTER RADIO SHACK
WHITEROCK SHOPPING CENTER
LOS ALAMOS, N.M. 87544
505-672-9824

They will install the kit at no charge if you will pay shipping both directions. The actual modification consists of unplugging the RAMs that are in the Computer, installing the RAM Adapter Board, connecting a couple of wires, and installing the RAMs. If you want the Memory Map Type #1 (64K RAM - up to \$F7FF in this mod, WOLFBUG lives from \$F800 up) capability, you also must perform a small mod to the Computer Circuit Board, which requires cutting a few lands and installing some jumpers. The EPROM Adapter Board is installed by unplugging the Color BASIC ROM, plugging the EPROM Adapter Board into the empty socket, installing the BASIC ROM in the provided socket, and hooking up 4 wires. The WOLFBUG Monitor ROM is already installed on the Adapter Board. The instructions cover power-up procedures and a discussion of using the WOLFBUG Monitor Routines thru BASIC to check the system out by displaying two different messages using the Memory Map Type #0 two-page capabilities. A discussion of the Monitors' use and a Commented Source Listing is also provided.

The WOLFBUG Monitor contains several "User Accessible Routines" for controlling the Memory Map Type #/0 and Map Type #/1 capabilities of the SAM, along with Routines for manipulating the two Map Type #/0 Pages. These can be called through BASIC "EXEC" or "USR" statements, and through assembly language Interrupt Service Routines. These require that the SWI3 Vector be initialized, but this is also taken care of with WOLFBUG. A Routine is also provided which, when called via a BASIC "EXEC" command, loads BASIC into RAM, goes to the all RAM mode (Memory Map Type #/1), and restarts BASIC. The WOLFBUG Monitor uses single key commands in providing the following functions:

- "A"--ASCII dump of memory
- "B"--Enter BASIC
- "D"--Route output to display
- "F"--Floppy Disk boot
- "G"--Go to a machine language program
- "H"--HEX dump of memory
- "L"--Load and Run, Transfer BASIC to RAM In 64K and run BASIC
- "M"--Examine and/or change memory
- "P"--Route output to the Printer (600 Baud)
- "T"--Memory test routine. Can not test the bottom 1K of memory
- "R"--Switch to the all RAM mode (Map Type #/1), 64K only
- "X"--Allows typing of text to the screen

The memory dumps are "paged" with the <ENTER> Key. The cursor is non-destructive and may be called as a "USR" function from BASIC. WOLFBUG uses the SWI3 as a "breakpoint", providing a register dump when encountered for program debugging. The "L" command provides RAM from \$C000 to \$F7FF, which is usable for machine language subroutines, etc., when running BASIC. The Monitors' RS232 output routine outputs 8 bits to allow full graphics capabilities on the popular Printers. Additionally, and more important with the Color Computer, it provides the use of the "shift down arrow" as a Control Key, allowing full Printer controls. A "Control Key" is badly needed for the Color Computer, with its limited keyboard, and should be a feature of ANY monitor for this computer. The WOLFBUG Monitor allows effective use of the SAM's Memory Management capabilities, and, with the 64K Mod, will allow an imaginative programmer to begin to utilize some of the Color Computers' potential.

ATOMTRONICS has a 16K RAM Kit for \$35.00 and a 32K RAM Kit for \$75.00. Neither of these kits require any soldering; the 16K Kit is the standard "replace the 4K RAMs with 16K RAMs" modification, but the 32K RAM Kit is different from most we have seen. The normal 32K Mod consists of "piggybacking" two 4116 16K RAMs on top of each other, soldering all the IC Leads to the chip under it except the "chip select" pin, which is lifted and routed to the SAM. The difference in the ATOMTRONICS Kit is that they are ALREADY SOLDERED, with the "chip select" pins already tied together, and a 33 ohm isolation resistor connected to them. All that is done to accomplish the Mod is to remove the RAMs already in the Computer and carefully insert the new "RAM stacks" and hook up the resistor. This sure eliminates a lot of work, and a HIGH potential for inducing problems with bad solder joints or solder bridges between the leads. You may save a few dollars by purchasing a couple sets of chips and installing them yourself, but is 20 to 30 dollars worth the trouble and '88 Micro Journal.

potential for damaging a chip (at the LEAST, with a good possibility of causing much worse damage)?

Another ATOMTRONICS Product I have been using lately, with NO problems, is their "Color Computer to Epson Printer Interface". This little jewel sells for \$49.95, and consists of a small Adapter Board which installs in the Serial Interface Board location inside the Epson MX-Series Printers. The Board has a 5 or 6' cable attached to it which plugs into the Serial I/O Connector on the Color Computer. If you have a V1.1 Color BASIC ROM, or use the 8-bit Printer output Program, and run at the Color Computers' normal 600 baud for the Printer, all you have to do to install the mod is cut one PC Land, open the Printer, plug the Board in, fasten it with the two screws provided, plug the cable into the Computer, and have at it. If you have an early Version Computer with a V1.0 ROM, you don't even have to cut the land. It works like a champ, and is extremely simple. I am running it at 2400 baud (that's what my FLEX Printer Driver Routine is set up for) with no problems; in fact, installing this interface helped me identify a problem with my Epson 8150 2K Buffer Serial Interface. I have been missing a CR on the Printer every once in a while, and getting two lines of text on one line from the Printer. The problem disappeared when I installed the ATOMTRONICS interface; therefore, the problem is in the 8150. I do miss the 2K Buffer, but I can't say it's worth the price difference. Obviously, from this discussion, you can change the Baud Rate of the ATOMTRONICS interface: cut a couple of lands and install some jumpers; all explained in the Instructions. As I said, the interface is so simple, I don't expect to ever have a problem with it. It also MAY eliminate a shortcoming of the Epson Serial Interfaces; some of the GRAFTAX ROM functions are not operational when using the 8150 Serial interface. I haven't tried them yet with this interface, but I think they will work with it. The GRAFTAX ROMs more than double the control capabilities of the MX-80, and having the use of all of them would be nice.

Things to come from ATOMTRONICS include a Color Computer Expansion Bus, Parallel and Serial I/O cards, possibly an EPROM programmer, and, probably by the time you read this, a Floppy Disk Controller. Generally, the Disk Controller is targeted for around \$150, and will have features like single/double density, FLEX and Radio Shack DOS compatibility, use 35, 40, or 80 track Drives, plus allow the addition of other I/O devices. It will probably contain 8K of Memory for DOS operation. Sounds good; again, the holdup is the Radio Shack DOS availability.

OK, we have 64K on the Color Computer, and a means to control it. Where to from here? First, let's look at another feature of the MC6883 SAM; the MPU Rate control. In the 6883's present configuration, you can have three types of clock speeds when in the Map Type #/0 memory mode. Normally, the "system" clock is around 0.9MHz (this is determined by the TV's horizontal sweep frequency). Changing the clock frequency would destroy the Displays horiz. and vert. sync, making it unreadable. But, what if we just changed the speed for certain memory areas, and left it "as is" for the Screen Display memory block. Well, that's what the SAM does. Since it has complete control over all memory, it can allow some memory areas to run at 0.9MHz, and the rest to run at double that, or around 1.8MHz. A "write" to address \$FFD7 produces a clock of 1.8MHz for any accesses to memory in the areas between \$8000 to \$FEFF and between \$FF20 and \$FFFF. The rest of memory accesses run at 0.9MHz. A "write" to \$FFD6 switches the clock back to the standard 0.9MHz rate. (As presently configured, the 6883 will not allow the clock rate switching when in the Map Type #/1 mode - all RAM.) Therefore, you can run the full system at the normal 0.9MHz rate, in an "address dependent rate", or at 1.8MHz (which is not usable, as previously mentioned, because it would destroy the Display timing).

ATOMTRONICS has provided 64K; where does the other 32K come from to get the 96K I mentioned. The "easy" way is with the EXATRON Color Computer expansion interface which I reviewed a couple of months ago. I have been running mine HARD for several months without a "hitch", and it contains 32K of dynamic memory with its own refresh circuits. A minor mod to the addressing logic should allow it to be brought "on line" when the SAM is in the Memory Map Type #0 mode, where the 64K is being used for page 0 and 1. WOLFBUG already protects the area from \$F800 up, so the BASIC ROMs can be "saved" to Disk and the Expansion RAM activated from \$8000 up. It will take a little working with, but should be no real problem. Now we can run the normal BASIC Operating System on page 0 and switch over to page 1 for FLEX, for example, allowing it to also have a "bottom" memory operating area. Another item to think about; put a "blt cruncher" program at \$8000 and up, say a Sort/Merge, for example, and let the Computer run in the "address dependent rate" mode, so you are running that program at double the normal speed. But, you say, what happens to your Display when you switch pages to run FLEX, or whatever? Put it up out of the way, in the \$E000 area, which most SS-50 Systems use for I/O, and therefore is NOT used by the normal SS-50 Operating Systems. Now we have a "clean" 48K (\$0000 to \$C000) for FLEX, OS-9, etc. Another possibility; BASIC is now in RAM, so rewrite the Keyboard/Display Routines so that the Display Screen Memory normally lives from \$E000 to \$F800 - that's the 6K needed for full Graphics.

Well, I didn't promise you COMPLETE solutions, just possibilities. I promised "thoughts on possibilities", and these should keep the "grey matter" active for a month or so. With that, I'll leave it with you for this month.

RLN

Eds Note: Please change all occurrences of ® to the foreslash. Our printer does not know what a foreslash is, it therefore converts to the 'registered' symbol.

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Please note that in the monthly column 'BIT BUCKET' appears, for the year, hundreds of small and not so small articles of special interest to most 68XX users. Most of these would comprise a full article in some magazines, but because they came to us as letters, etc., with listing we decided to place all the hints and kinks, fixes, suggestions on improving hardware and software and other valuable subjects in this one grouping. By appearing in BIT BUCKET in no way demeans an article or it's value. In fact, most all readers feel that some of the most valuable information published, appeared in BIT BUCKET.

COMPUWORLD P/R

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Mid South Pharmaceutical, Inc.
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Hixson, TN 37343

I would like to preface my comments on Compuworld's UniFLEX Version Payroll program by saying that I am not exactly a computer enthusiast. I enjoy using our computer as a multi-faceted time-saving tool. My position requires me to be knowledgeable in many of its uses, including payroll. I have worked with several payroll programs in the past year as our computer was being expanded to better serve our needs. I have entered and reentered data for hours trying to accomplish a task I could do manually in the same time with less effort. This gives me a deep appreciation of a well organized payroll program.

Compuworld's UniFLEX Payroll program was put on our computer just as we were going into a new fiscal year which allowed us to start up with a clean slate,

This program is very impressive in several areas. The manual that accompanies the program is well written (82 pages with 3 detailed appendix's of examples) and geared towards people like me. It provides several pages of basic data that would insult the intelligence of anyone having any computer education. I am trained in the Social Sciences so it is most reassuring to have available. This manual also leads you by the hand through the startup procedure until you actually have the diskettes initialized and the master menu on the screen. Once at the master menu with the manual at ready, you are set for a journey that turns out to be very smooth with only one notable exception.

The Employee Maintenance section allows you all the functions required for exceptionally good records including adding, deleting, or updating employee records. The Auto Insert mode of entering a new employee is very fast and efficient with easy error correction capabilities. The main bug for me here was that upon completion of entering in of employee data, and your satisfied that all data is correct, you must type in "U" to update the information out to the main files. Several times I accidentally hit a CR before entering the "U" and lost all the data for that employee. I suppose that dealing with valuable info such as payroll data, every precaution should be taken to protect against the mistake of writing BAD data though. Deleting employees at year end is very easy and quick also. To update an employee's information is simple and fast but also requires you to select a "U"pdate option upon completion.

This program allows you to get a list of employees and their number but does not provide for a hard copy of the data stored on each employee. While not needed often, this can be useful in assessing for promotions, etc.

The major gripe I have against this program is that we did not receive the updated version of the tax tables and had to spend 12 manhours entering this data. I am sure this problem is resolved and truly hope that Compuworld provides updated tax tables promptly when they fluctuate again under the administrations planned tax cuts. I will admit that much of the time we spent was entering tables for unused figures such as weekly wages from \$1.00 to \$15.00 which is more time consuming than larger wages. Other payroll systems we have used worked by percentages only, and during that Murphy's Law situation of the computer down on the Friday afternoon that had several changes in the normal payday, it is impossible to hand figure checks and then match the computer later which is a big problem. Thanks to the table system compuworld uses the checks will match!!! The tables are entered a page at a time and pages are incremented so that a special function incorporated can allow only one column to be entered and it will automatically "Slide" the data to all other columns making the task much easier. Compuworld has gone all out in providing various pay variations, such as shift, weekend, and overtime differentials. It also provides for many deductions including union dues, credit union, insurance, and garnishment.

Once all the basic data has been entered and checked and you have familiarized yourself with the program, you will anticipate the ease with which payroll can be run. We have 16 people on payroll divided equally between hourly and salaried workers with 3 part timers thrown in for good measure. This program does not differentiate between hourly and salaried employees. Instead it treats everyone as hourly. It uses a 3 decimal accuracy to allow for exact calculation of salaried employees.

To run payroll I use the Automatic Time-Card Entry selection. With my records for my actual hourly employees at hand, I begin and in less than 20 minutes I

am finished with this part. The prompts that appear make it easy to skip an employee.

This program is nice in that it allows you to AUTOMATICALLY REVIEW one or all of the time cards you have entered to double check yourself thus preventing wasting time printing bad data. This normally takes less than 15 seconds per employee.

If new information comes to light concerning an employee before you have posted the current payroll data to the appropriate file, you can easily update an individual time card and recompute before posting. After all time cards have been entered and checked you go back to the Master Menu and select option #5 (Calculate Employees Paycheck).

When working through the Calculate Paycheck section you need to begin with option #1 which is calculate employee paychecks. You then can review any or all paychecks prior to printing and posting. This gives you one more opportunity to review the data to make sure it is correct. I do not use computer produced checks with our small payroll instead I proceed directly to option #6 which is Post Current Calculated Checks. Next I select option #7-Print Payroll Register which gives me in a clear and concise form all the data required to write checks along with a hard copy for permanent records. This feature has a defect that is present in all the print check printouts which are that it does not print out the employee name. Instead you get employee number, check number, social security number, gross wages, various deductions, and net amount. Using a hardcopy of our Employee List mentioned earlier, I am able to write checks. The program includes a complete check writing routine that allows alignment of checks and prints complete information on each check.

The entire process to this point takes only 10 to 15 minutes. Upon completion I go directly to the Master Menu and select option #9- Backup Payroll Diskette. This selection guides the inexperienced through the vital task of making backup records, with a series of prompts and directions that make it understandable to the novice. Upon finishing this task which requires ten minutes, you have completed a normal payroll. You can now take a pen and your Payroll Register and write checks.

For those of you with more employees do not fear because this program is set up to handle 500 employees on a single diskette.

Compuworld has also given us various other functions which could make you into a more efficient manager. The Master Payroll Report Generation Program (option #7 at Master Menu) provides five reports including:

- 1) Report of Regular Earnings and Hours worked
- 2) Report of Check Totals for Period Ending
- 3) Report of Weekly Earnings up to \$DDD.CC
- 4) Report of Wages Taxable under FICA (form 941A)
- 5) Report of Unemployment Gross Pay for All Quarters up to \$DDD.CC

All of these reports will save your accountants time and effort thus producing a savings in fees. When end of year comes round after having used this for the entire year, I will be in high cotton because with Compuworld's Program here I can end out the year and start backup in an hour, or less.

All in all, let me say this program gets my hearty approval. It is well organized so that you have many chances to catch and correct errors. The program and the manual, when used together, are self explanatory in most cases. It has helped me make more efficient use of my time by drastically reducing the

amount of time required to take care of paying our employees while maintaining superlative records.

Lastly let me say that Mr. Fred Calev "the main man" at compuworld was only a phone call away for us, and in the very beginning what little help we needed, he was very much available with quick concise answers to our questions. With a 1 year warranty and the promptness which updates are shipped out I would end by saying it would be hard to find a better payroll package anywhere.

SIMULATION, GAMES, AND RANDOM VARIABLES by

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final

A PASCAL program for computing the autocorrelation function of a sequence for k values between one and twenty is shown in Listing 8. The random number generator to be tested is implemented in procedure RANDOM.

A random number generator algorithm which passes the Chi-squared and autocorrelation tests is not necessarily "good" in an absolute sense, since other tests may detect certain non-random characteristics not visible to these tests. A perfectly random sequence would have successive numbers uniformly distributed, triplets of numbers uniformly distributed, and so forth. Some of the more powerful tests actually evaluate these characteristics. The Chi-squared and autocorrelation tests do, however, provide an evaluation of the two most basic characteristic requirements of a random number generator -- uniform distribution and low serial correlation.

There are some general principles to be observed when selecting values of A and C in a linear congruential sequence to be used in random integer generation. Following these principles does not guarantee a good generator, but one can have a fair amount of confidence in its efficacy if it also passes the Chi-squared and autocorrelation tests. These guiding principles are as follows:

- 1) The modulus M should be large, and selected in accordance with concepts discussed previously. Modular arithmetic must be done precisely, with no roundoff error.
- 2) The multiplier A should be selected in accordance with the concepts discussed previously. In addition, A should be larger than M, preferably

larger than $M/100$, but smaller than $A - \sqrt{A}$. The digits in the binary or decimal representation of A should not have a simple or regular pattern.

- 3) The additive parameter C should be an odd integer and, if M is a power of 10, should not be a multiple of 5. It should be chosen to satisfy the relationship

$$C/M \text{ approximately equal } 0.211$$

- 4) Since the least significant digits of the integers produced by the sequence may not be very random, use should be influenced primarily by the most significant digits.

For confidence in the generator in extensive or sensitive uses, further tests should be applied. (See Reference 1.)

While all examples of random integer generators presented here are implemented in a high level language, the most efficient implementation occurs at the assembler level where the modular features of register arithmetic can be exploited. For example, the quantity $B = A \bmod M$ can be determined by a single divide operation by using the remainder rather than the quotient as the answer. In the PASCAL implementation, this must be achieved by the instructions

`Q:=A DIV M;`

`B:=A-Q*M;`

or by use of the MOD instruction. Modular multiplication in assembler code can be accomplished using the full word size of the register (or the "effective register size" if one is working in multiple precision), since the multiplication of n-bit numbers produces a $2n$ -bit product, of which the low order n-bits constitute the product modulo 2^n . There are also clever ways to produce products modulo $2^n \pm 1$ when using an n-bit word size. Most of the advantages of working in assembler code are in the reduced execution time of the number generator. This can be quite significant in a simulation environment where calls to the random number generator constitute a significant portion of the total execution time.

For simulation purposes, random

numbers produced from a uniform distribution on the interval $(0,1)$ are very useful for determining the outcome of a random experiment of some kind. Often, it is desired to produce a random variable with a distribution other than uniform, such as the exponential or normal (Gaussian or "bell-shaped") distributions. Fortunately, random variables with virtually any distribution can be produced from a sequence of uniformly distributed random numbers. For example, a normal distribution can be produced by utilizing the principles embodied in the Central Limit Theorem of statistics, which states that the sum of independent random variables tends towards a normal distribution. That is, the distribution of the random variable Z defined as

$$Z = ((RN_1 + RN_2 + \dots + RN_K) - K/2) / (K/12)$$

tends towards a normal distribution with zero mean and variance equal to one as K grows large. For $K=12$, the computation simplifies to

$$Z = (RN_1 + RN_2 + \dots + RN_{12}) - 6,$$

a form found in many standard generators. Any arbitrary mean μ and variance σ^2 can be obtained by the further transformation

$$X = \sigma Z + \mu$$

A PASCAL program for generating normally distributed random variables utilizing these concepts is shown in Listing 9.

Listing 10 presents a PASCAL program for generating exponentially distributed random variables; making use of the fact that, if RN is a random variable uniformly distributed on the interval $(0,1)$, then

$$X = (-1/\mu) \ln RN$$

where $\ln RN$ is the natural logarithm of RN, is a random variable exponentially distributed with parameter μ .

There are methods of producing random variables of virtually any desired distribution by use of a sequence of random numbers uniformly distributed on the interval $(0,1)$. This result follows from the fact that, if $F(x)$ is the Cumulative Distribution Function describing the desired distribution, and if RN is a random variable uniformly distributed on the interval $(0,1)$, then solution for X of

the equation

$$F(X) = RN$$

yields a random variable X with the desired distribution. The solution for X may be analytically obtained, as in the case of the exponential distribution above, or it may be obtained by table lookup when analytical methods fail. The fact that such distributions can be obtained make simulation a viable and effective tool.

As a final example, simulation will be used to solve a problem which cannot be solved in any other manner, although the model is much simpler than any real-world situation would produce. Suppose there is a small pond containing 10,000,000 cu. ft. of water, and this pond is fed by a stream whose flow is 40,000 cu. ft. per day. A second stream drains the pond at such a rate to keep the water level constant. Suppose further that it has been proposed to dump wastewater containing ten percent pollutants by volume into the pond. Since the pond is constantly flushed by the stream flow, the pollutant level in the pond will rise to a constant level determined by the rate at which the wastewater flows into the pond. This level could be easily determined by the relationship pollutant flow in = pollutant flow out,

which exists after the pollutant flow has risen to its constant value, if it were not for the rainfall which occurs in the area of the pond. Since rainfall is a random phenomenon, there is no way to describe its effect other than by stating probabilities. Suppose that rainfall histories have produced the following data. The probability of rain on any given day is 0.45, and the amount of runoff entering the pond directly or by way of the stream flow, if rain does occur on any given day, is a random variable whose distribution is approximated by an exponential distribution with parameter 1/30,000. Now it is not possible to determine the behavior of the pollution level because it can only be described in probabilistic terms. What can be done is to simulate the system and generate enough data so that statistical methods can be used.

The simulation equation, determined with one day as the basic time unit, is

$$P_n = P_{n-1} + 0.1W - (40,000 + R_n)P_{n-1}/10,000,000$$

where P_n is the volume of pollutants on day n , W is the wastewater flow per day, and R_n is the volume of rainfall flowing into the pond on day n . This is merely a computation of the change in the volume of pollutants from day $n-1$ to day n . The rainfall volume R_n is determined in the following way.

- 1) Generate a random number RN .
- 2) If $RN > 0.45$, set $R_n = 0$ and terminate.
- 3) Generate R_n as a random variable from an exponential distribution with parameter $1/30,000$ and terminate.

In this manner, each day is simulated to determine if rainfall occurs, and if it does, the amount is determined. This leads to a day-by-day tabulation of the pollution level in the pond, starting with day one. When this is obtained, it represents only a single result out of an infinite set of possible results. To obtain more information, additional simulation runs must be made, starting with different random integer seeds, and these results then averaged to obtain estimates of the probabilistic characteristics of the real-world system. If it were desired to determine, for example, the number of days before the pollution reached a given level, the varying answers produced by the different simulation runs would be averaged to produce a quantity which would be considered to be the mean of the random variable which is the time to reach that level. This interpretation is necessary because the system itself is random, and statements relating to definite values cannot be made.

If only long term behavior is of interest, the system may be simulated for a long time interval and the data produced during the initial "start-up transient" ignored. For this simulation, the start-up transient period is about 200 days.

A simulation of this system, written in PASCAL, is shown in Listing 11. The wastewater flow rate W is entered as an input, so that the sensitivity of the

results to this parameter can be studied.

While the simulation problem just considered is a simple one, it is representative of many ecological simulations used frequently by environmentalists. This simulation could be progressively modified to make it more realistic by accounting for factors not considered in the model above. Each modification would be relatively simple but would contribute to the overall credibility of the simulation. Soon a complex model would result, but the individual concepts involved in making that model are no more difficult to understand than what has been done here.

There are a myriad of other applications where random simulation models are effectively used. War-gaming, hurricane storm surge and wave action predictions, evaluation of computer operating system procedures, optimal control system analysis, population dynamics, and urban dynamics, among others, come to mind. And, of course, there are the ever popular computer games where the results of play action is determined in accordance with certain probability rules. In this respect, such games are similar to war-gaming simulations used to evaluate tactics, strategy, or weapon system concepts, or to train military leaders under simulated battle conditions.

References

- Knuth, D.E., The Art of Computer Programming: Volume 2, Semi-numerical Algorithms, Addison-Wesley, 1969.
- Elbert, T.F. and Enzian, R., "Trap-door Function Encryption with the 6800," 68' Micro Journal, July 1980.

```
PROGRAM RANDTEST;
VAR A,I,C,M,X : INTEGER;
BEGIN
  WRITELN('PROGRAM TO GENERATE A SEQUENCE OF PSEUDO-RANDOM INTEGERS');
  WRITELN;
  WRITELN('ENTER NUMBER OF INTEGERS DESIRED');
  READLN(N);
  WRITELN('SEQUENCE IS X(N) = (A*X(N-1) + C) MOD M');
  WRITELN;
  WRITELN('ENTER THE MODULUS M, MAX = 100');
  READLN(M);
  WRITELN('ENTER VALUE OF A');
  READLN(A);
  WRITELN('ENTER VALUE OF C');
  READLN(C);
  WRITELN('ENTER SEED VALUE X(0), MUST BE < MAX');
  READLN(X);
  I:=1;
  WHILE NDO DO
    BEGIN
      X := (A*X+C) MOD M;
      WRITELN(X);
      IF I=10 THEN
        BEGIN
          WRITELN;
          I:=0
        END;
      I:=I+1;
      N:=N-1
    END;
END;
```

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Page 1 LISTING 2 *** EUCLID'S ALGORITHM FOR FINDING GCD ***

```
PROGRAM EUCLID;
VAR A,B,GCD : INTEGER;
BEGIN
  WRITELN('PROGRAM TO FIND GREATEST COMMON DENOMINATOR GCD(A,B)');
  WRITELN;
  WRITELN('ENTER VALUE OF A < 102');
  READLN(A);
  WRITELN;
  WRITELN('ENTER A VALUES < 102 IN SUCCESSION');
  WRITELN('ENTER ZERO TO TERMINATE');
  WRITELN;
  READDLNA;
  WHILE ADO DO
    BEGIN
      WHILE ADO DO
        BEGIN
          B := A;
          A := B DIV A;
          B := B - A*A;
          GCD := A;
        END;
      WRITELN(A);
      WRITELN('DO YOU DESIRE ANOTHER CALCULATION');
      READDLNA;
    END;
END.
```

Page 1 LISTING 3 *** MODULAR EXPONENTIAL PROGRAM ***

```
PROGRAM EXP;
VAR A,AMOD,B,D,PROD,M,R : INTEGER;
  AGAIN:CHAR;
BEGIN
  WRITELN('PROGRAM FOR CALCULATING X^B MOD M');
  AMOD:=1;
  WRITELN('ENTER M, THE MODULUS < 101');
  READLN(M);
  WHILE AGAIN='Y' DO
    BEGIN
      WRITELN;
      WRITELN('ENTER EXPONENT B < 102');
      READLN(B);
      WRITELN('ENTER MANTISSA A < 102');
      READLN(A);
      PROD:=1;
      AMOD:=A;
      WHILE BDO DO
        BEGIN
          B1 := B;
          B := B DIV 2;
          IF (2*B)>B1 THEN
            BEGIN
              PROD := PROD*AMOD;
              R := PROD DIV M;
              PROD := PROD-R;
            END;
          AMOD := AMOD*AMOD;
          R := AMOD DIV M;
          AMOD := AMOD-R;
        END;
      WRITELN;
      WRITELN('THE MODULAR EXPONENTIAL VALUE IS ',PROD);
      WRITELN;
      WRITELN('DO YOU DESIRE ANOTHER CALCULATION?');
      READDLN AGAIN;
    END;
END.
```

Page 1 LISTING 4 *** RANDOM NUMBER GENERATOR ***

```
PROGRAM RANDTEST;
VAR A,I,C,N,X : INTEGER;
  TEMP:FILE OF CHAR;
BEGIN
  REWRITE(TEMP,'OUTPUT.TEXT');
  WRITELN('PROGRAM TO GENERATE A SEQUENCE OF PSEUDO-RANDOM INTEGERS');
  WRITELN;
  WRITELN('ENTER NUMBER OF INTEGERS DESIRED');
  READLN(N);
  WRITELN('SEQUENCE IS X(N) = (A*X(N-1) + C) MOD 32767');
  WRITELN;
  WRITELN('ENTER VALUE OF A');
  READLN(A);
  WRITELN('ENTER VALUE OF C');
  READLN(C);
  WRITELN('ENTER SEED VALUE X(0)');
  READLN(X);
  I:=1;
  WHILE NDO DO
    BEGIN
      X := A*X+C;
      IF X>32767 THEN
        X := X-32767;
      IF X<0 THEN
        X := X+32767;
      WRITE(TEMP,X);
      IF I=10 THEN
        BEGIN
          WRITELN(Y);
          I:=0
        END;
      I:=I+1;
      N:=N-1
    END;
  CLOSE(TEMP,LOCK);
END.
```

page 6 LISTING 5 *** SIMULATION OF GAMBLERS RUIN PROBLEM ***

```

PROGRAM DARTBLL;
CONST
  NORM=32767.0;
  VAR
    I,II,K,M,N,X: INTEGER;
    AVM,P,PROB,RN,WON,LOST,REAL;
    FUNCTION RANDOM(X: INTEGER): INTEGER;
    (GENERATES RANDOM INTEGER)
  BEGIN
    X := 10000;
    IF X<0 THEN
      X := -32767+1;
    RANDOM := X;
  END;
  BEGIN
    Writeln('A PROGRAM TO SIMULATE THE DARTBLLES INVIN PROBLEM');
    Writeln('MATERIALS');
    Writeln('ENTER INDIVIDUAL GAME WIN PROBABILITY');
    ReadReal(P);
    Writeln('ENTER 000 INTEGER RANDOM NUMBER BEED < 32767');
    ReadReal(RN);
    Writeln('ENTER BEGINNING RESOURCES < 32767');
    ReadReal(M);
    Writeln('ENTER STOPPING RESOURCES < 32767');
    ReadReal(N);
    Writeln('ENTER NUMBER OF SIMULATIONS DEBIREO');
    ReadReal(K);
    WON := 0;
    LOST := 0;
    AVM := 0;
    FOR N := 1 TO M DO
    BEGIN
      I := 0;
      RN := NORM;
      IF RN=P THEN
        I := 1;
      ELSE
        I := -1;
      M := M+I;
      IF I=0 THEN
        AVM := AVM+1;
      IF I>0 THEN
        WON := WON+1;
      ELSE
        LOST := LOST+1;
      Writeln('GAME WON IN ',M,' PLAYS');
      Writeln('GAME LOST IN ',M,' PLAYS');
    END;
    Writeln('WON GAMES = ',WON);
    Writeln('LOST GAMES = ',LOST);
  END.

```

FIGURE 2 LISTING 9 ... SIMULATION OF GAMBLER'S RUIN PROBLEM ...

```

      MON MON+1
      END.
      AVM = 0.1*(N-1)*AVPMH/N

END.
DO50 MON=1,MON+LOST.
      WRITELN('MON=LOST');
      WRITER;
      WRITELN(' THE WIN PROBABILITY BASED ON ',N,' PLAYS IS ',PROB);
      WRITELN(' THE AVERAGE NUMBER OF PLAYS WAS ',AVR);
      WRITELN(' THE AVERAGE NUMBER OF PLAYS WAS ',AVR);

END.

```

LITERATURE REVIEW ON SIMULATION OF WAGGING SYSTEMS

```

PROGRAM SYSTEM;
CONST
  > NORM=32767: DI
  VAR
    BET: I..11; R, M, N, I: INTEGER;
    AVW, P, PROB, RM, WON, LOST, REAL,
    FUNCTION RANDOM(X: INTEGER): INTEGER;
    (GENERATES RANDOM INTEGER)
  BEGIN
    X := 13809;
    IF X<0 THEN
      X := X+32767;
    RANDOM := X
  END;
BEGIN
  WRITELN('A PROGRAM TO SIMULATE GAMING SYSTEM');
  WRITELN;
  WRITELN('ENTER INDIVIDUAL GAME WIN PROBABILITY');
  READLN(P);
  WRITELN;
  WRITELN('ENTER TWO INTEGER RANDOM NUMBER SEED < 32767');
  READLN(DI);
  WRITELN;
  WRITELN('ENTER BEGINNING RESOURCES < 32767');
  READLN(RM);
  WRITELN;
  WRITELN('ENTER STOPPING RESOURCES < 32767');
  READLN(M);
  WRITELN;
  WRITELN('ENTER NUMBER OF SIMULATIONS DESIRED');
  READLN(N);
  WON := 0;
  LOST := 0;
  AVW := 0;
  FOR N:=1 TO N1 DO
  BEGIN
    RM := 0;
    I := 1;
    BET := 1;
    WHILE(I>0) AND (RM>0) DO
    BEGIN
      X := RANDOM(DI);
      RM := R*Norm;
      IF X<P THEN
        BEGIN
          I := I+BET;
          BET := 1;
        END
      ELSE
        BEGIN
          I := I-BET;
          BET := BET+BET;
          IF BET>I THEN
            BET := I
        END;
    END;
    M := M+1;
  END;
END.

```

TABLE 2 LISTING OF THE SIMULATION OF WADING SYSTEM DUE

```

100.
IF I=0 THEN
BEGIN
  WRITELN('GAME LOST IN ', M, ' PLAYS');
  LOST:=LOST+1;
END
ELSE
BEGIN
  WRITELN('GAME WON IN ', M, ' PLAYS');
  WON:=WON+1;
END;
AVM:=(WON-B)/AVVM+B/M;
END;
PROB:=AVM/(WON+LOST);
WRITELN('THE MIN PROBABILITY BASED ON ', N, ' PLAYS IS ', PROB);
WRITELN('THE AVERAGE NUMBER OF PLAYS WAS ', AVM);
WRITELN('THE AVERAGE NUMBER OF PLAYS WAS ', AVM);

```

PAGE 1 LISTING 7 ... CHI-SQUARED TEST ...

```

PROGRAM CHISQUARE;
COMBT
  SI=37 2; 82=69 3; 83=85 5; 54=112 2; 89=129 1; 66=151 4;
  VAR
    I, J, N, K, E, INT, QINT : INTEGER;
    COUNT, ARRAYVAL, DATA : INTEGER;
    ER, RA, S, T, DT : REAL;
  PROCEDURE RANDOMIVAR I : INTEGER; VAR RN : REAL;
  BEGIN
    K := 18104;
    IF I<0 THEN
      I := -I-32767-1;
    RN := K*32768 0;
  END;
  PROCEDURE TESTT10 REAL);
  CONST
    C1=32 6; C2=30 9; C3=92 2; C4=73 6; C5=91 6; C6=106 6;
  BEGIN
    WRITELN;
    IF (RN>C1) OR (RN>C2) THEN
      IF (RN>C3) OR (RN>C4) THEN
        IF (RN>C5) OR (RN>C6) THEN
          WRITELN('TEST FAILS AT 99% CONFIDENCE LEVEL. REJECT');
        ELSE
          WRITELN('TEST FAILS AT 95% CONFIDENCE LEVEL. SUSPECT');
      ELSE
        WRITELN('TEST FAILS AT 90% CONFIDENCE LEVEL');
    ELSE
      WRITELN('TEST FAILS AT 75% CONFIDENCE LEVEL');
    END;
    WRITELN('TEST DOES NOT FAIL AT 75% CONFIDENCE LEVEL. GOOD TEST');
  END;
  BEGIN
    WRITELN('PROGRAM TO APPLY CHI-SQUARED TEST TO THE RANDOM NUMBER');
    WRITELN('GENERATOR IMPLEMENTED IN PROCEDURE RANDOM');
    WRITELN;
    WRITELN('ENTER QUANTITY OF RANDOM NUMBERS TO BE USED. SHOULD BE > 300');
    READLN(N);
    WRITELN;
    WRITELN('ENTER RANDOM NUMBER SEED');
    READLN(RS);
    WRITELN();

```

DATA 3 LISTING 7 *** CHI-SQUARED TEST ***

```

        CLR
        T = 7 - DT
        DT = DT/2
        DINT = DINT DIV 2
        DBL,
        COUNT(INT1) = COUNT(INT3) + 1,
        RANDOM(X, RM)
        DBL;
        WRITELN('THE HISTOGRAM DATA:');
        WRITELN(' ');
        WRITELN(' ');
        FOR I = 1 TO 64 DO
        BEGIN
        IF I ADD SH0 THEN
        BEGIN
        WRITELN();
        FOR J = 1-7 TO I DO
        WRITE(COUNT(J); ' ')
        END;
        END;
        WRITELN();
        WRITELN(' ');
        EX = 1/64;
        S = 0;
        FOR I = 1 TO 64 DO
        S = S + (COUNT(I)-EX)/EX;
        WRITELN();
        WRITELN('THE TEST STATISTIC IS ', S);
        TEST(S)
        DBL;

```

Pete I LISTING 8 *** AUTOCORRELATION TEST ***

```

PROGRAM AUTOCORR;
TYPE
  VECTOR=ARRAY[1..20] OF REAL;
VAR
  I, N, M, L, U: INTEGER;
  AV, LD, HI, LO, VA: REAL;
  V: VECTOR;
PROCEDURE RANDOM(VAR I: INTEGER; VAR RN: REAL);
{CONTAINING THE RANDOR NUMBER GENERATOR TO BE TESTED}
  BEGIN
    I := 101*I;
    IF I < 0 THEN
      I := I+32767;
    I := I-32767*I;
    RN := I/32767.0
  END;
PROCEDURE TABLE(VAR V: VECTOR);
VAR
  I: INTEGER;
BEGIN
  WRITELN(' ');
  WRITELN(' THE AUTOCORRELATION FUNCTION IS :');
  WRITELN(' ');
  WRITELN('   K       R(K)       K       R(K) ');
  WRITELN(' ');
  FOR I:=1 TO 20 DO
    WRITELN(I:10, ' ', R(I):10, I:10, ' ', R(20-I):10);
  WRITELN(' ');
END;
BEGIN
  WRITELN('PROGRAM TO DETERMINE SERIAL CORRELATION OF THE RANDOM');
  WRITELN('NUMBER GENERATOR IMPLEMENTED IN PROCEDURE RANDOM');
  WRITELN(' ');
  WRITELN('ENTER QUANTITY OF RANDOM NUMBERS TO BE USED. SHOULD BE > 100');
  READLN(N);
  WRITELN(' ');
  WRITELN('ENTER RANDOM NUMBER SEED');
  READLN(R);
  WRITELN(' ');
  WRITELN(' ');
  AV := 0;
  VA := 0;
  FOR N:=3 TO 20 DO
    BEGIN
      RANDOM(I, RN);
      VEN := RN;
      AV := (AV+N*VEN)/N;
      VA := ((M-1)*VA+(RN-AV)*(RN-AV))/N
    END;
  FOR N:=1 TO 20 DO
    R(N) := 0;
  FOR N:=21 TO N1 DO
    BEGIN
      RANDOM(I, RN);
      FOR I:=1 TO 20 DO
        RIS := ((I-1)*R(I)+RN*R(N))/20;
      FOR I:=1 TO 19 DO
        V(I) := V(I)+1;
      V(20) := RN;
      AV := ((N-1)*AV+RN)/N;
      VA := ((N-1)*VA+(RN-AV)*(RN-AV))/N
    END;
  VA := SQRT(VA);
  FOR I:=1 TO 20 DO
    RCI := (R(I)-AV)*VA;
    TAU(I) := RCI/V;
  END;
  WRITELN(' ');
  WRITELN('MEAN = ', AV);
  WRITELN('VARIANCE = ', VA);
  LO := (-1-2*SQRT(V)/(N-1))/((N-1)/(N-1));
  HI := (1-2*SQRT(V)/(N-1))/((N-1)/(N-1));
  WRITELN(' ');
  WRITELN('THE 99% CONFIDENCE CORRELATION LIMITS ARE ', LO, ' TO ', HI)
END;

```

LISTING 9 •• GENERATION OF NORMAL RANDOM VARIABLES ••

```

PROGRAM NORMAL;
VAR I,J,N,M,X : INTEGER;
MU,SIGMA,RN,V : REAL;
TEMP ARRAY [1: 50] OF REAL;
PROCEDURE RANDOM (VAR I : INTEGER; VAR RN : REAL);
(GENERATES RANDOM NUMBERS)
BEGIN
I := 1800;
IF I<0 THEN
I := 32767+1;
RN := I/32767.0
END;
PROCEDURE NORMAL (MU, SIGMA : REAL; VAR I : INTEGER; VAR V, RN : REAL);
(GENERATES GAUSSIAN VARIABLE FROM 12 RANDOM NUMBERS)
VAR
I : INTEGER;
BEGIN
RN := 0;
FOR I := 1 TO 12 DO
BEGIN
RANDOM (I, RN);
V := RN;
END;
V := V - MU;
SIGMA := SIGMA + V*V;
END;
BEGIN
WRITELN ('PROGRAM TO GENERATE SEQUENCE OF RANDOM VARIABLES');
WRITELN ('ENTER MEAN = MU, VARIANCE = SIGMA^2');
WRITELN ('');
WRITELN ('ENTER MEAN (RETURN), VARIANCE (RETURN)');
READLN (MEAN);
READLN (SIGMA);
WRITELN ('');
WRITELN ('ENTER RANDOM SEED INTEGER');
READLN (I);
WRITELN ('');
WRITELN ('ENTER QUANTITY OF NUMBERS DESIRED (MULTIPLE OF 5)');
READLN (N);
WRITELN ('-----');
I := 1;
FOR N := 1 TO N DO
BEGIN
NORMAL (MEAN, SIGMA, I, RN);
TEWD (I) := V;
IF I>0 THEN
BEGIN
WRITELN ('');
FOR J := 1 TO 5 DO
WRITE (TEWD (J));
I := I+1;
END;
ELSE
I := I+1;
END;
WRITELN ('-----');
WRITELN ('-----');
END;

```

Page 1 LISTING II FOR SIMULATION

```

PROGRAM PONDINS;
VAR I, N1, N2, NO, X: INTEGER;
    AV, P, R, RN, VA, W: REAL;
PROCEDURE RANDOM (VAR Z: INTEGER; VAR RM: REAL);
BEGIN
  X:=181*X;
  IF X<0 THEN
    X:=X-32767+1;
  RM := 4/3248.0
END;
FUNCTION RAND01 (VAR RM: REAL): REAL;
BEGIN
  RANDOM(Z,RM);
  RAND01 := (RM-1)/RM;
END;
BEGIN
  WRITELN('THIS PROGRAM SIMULATES THE POLLUTION LEVEL IN THE');
  WRITELN('POND FOR SELECTED WASTEWATER FLOW RATES');
  WRITELN;
  WRITELN('ENTER WASTEWATER FLOW RATE IN CUB. FT. PER DAY');
  READLN (NO);
  WRITELN;
  WRITELN('ENTER NUMBER OF DAYS TO BE SIMULATED');
  READLN (N1);
  WRITELN;
  WRITELN('ENTER DAY ON WHICH TO START AVERAGING');
  READLN (N2);
  WRITELN;
  WRITELN('ENTER ODD RANDOM INTEGER SEED');
  READLN (I);
  WRITELN('-----');
  P:=0; AV:=0; VA:=0;
  WRITELN;
  WRITELN('      DAY      % POLLUTION LEVEL');
  WRITELN;
  I:=0;
FOR N:=1 TO N1 DO
  BEGIN
    RANDOM (X,RN);
    IF RN < 0.5 THEN
      R:=0
    ELSE
      R:= -RAND01 (13,3333E-5);
    P:=P+ (RN-BOR(140000.0+X,R)) * P0; DE:=7;
    WRITELN(N,B,P,I);
    IF NO=N2 THEN
      BEGIN
        I:=I+1;
        AV:=(I-1)*AV+P/I;
        VA:=(I-1)*VA+BOR(P,I)/I;
      END
  END;
  WRITELN('-----');
  WRITELN;
  WRITELN('      DAY AVERAGE LEVEL = ',AV,' WITH VARIANCE ',VA)
END.

```

PAGE 1 LISTING 10 FOR GENERATION OF EXP DIBT RANDOM VARIABLES 88

```

PROGRAM EXPRAND;
VAR
  I, J, N, MU, X, Z: INTEGER;
  MU, RN, REAL;
  TEMP ARRAY[1..50] OF REAL;
PROCEDURE RANDOM(VAR X: INTEGER, VAR RN: REAL);
{GENERATES RANDOM NUMBERS}
BEGIN
  X:=181*X;
  IF X>0 THEN
    X:=X-32767+1;
  RN:=X/32767.0
END;
FUNCTION RANDER(MU: REAL): REAL;
{GENERATES EXPONENTIALLY DISTRIBUTED RANDOM VARIABLE FROM RANDOM NUMBER}
BEGIN
  RANDER:=EXP(-RN/MU);
  RANDER:=-LN(RN/MU)
END;
BEGIN
  Writeln('PROGRAM TO GENERATE SEQUENCE OF EXPONENTIALLY DISTRIBUTED');
  Writeln('RANDOM VARIABLES WITH PARAMETER MU.');
  Writeln;
  Writeln('ENTER PARAMETER MU:');
  Readln(MU);
  Writeln;
  Writeln('ENTER RANDOM SEED INTEGER:');
  Readln(X);
  Writeln;
  Writeln('ENTER QUANTITY OF NUMBERS DESIRED (MULTIPLE OF 5):');
  Readln(N);
  Writeln('-----');
  I:=1;
  FOR N:=1 TO N DO
  BEGIN
    TEMP[I]:=RANDOM(MU);
    IF I>5 THEN
      BEGIN
        Writeln;
        FOR J:=1 TO 5 DO
          Writeln(TEMP[J]:1:1);
        I:=1
      END
    ELSE
      I:=I+1
  END;
  Writeln;
  Writeln('-----');
END.

```

FLEX & CC

FLEX™ on the COLOR COMPUTER!!

For a long time now we, users of the 68XX series of devices have longed for a less expensive way of using the power of the 6809 and the excellent FLEX™ disk operating system from Technical System Consultants, as opposed to starting out with an initial large (couple thousand bucks or more) investment. Also some delight in and justify the initial cost of a computer because it can do other things than just ledgers, journals, and limited graphics applications. I know of literally hundreds of users of other computer systems who really wanted a better system but had to have something that mommy and the kids could derive some benefit from. We lost out! This applies mostly to the hobbyist portion of our group. The serious and business (a fast growing part) user of course is willing to spend the larger amount initially, for that insures them of starting off with the 'full bore' top of the line system. For the hobbyist the first consideration, in most cases, is the start-up cost. Once the system is used and appreciated then they will (as has happened with other systems) go on to bigger and better things. Then we all gain for we have not lost them to some other cheaper (!) system.

The standard hardware that is now available (Standard S50 Bus) is the finest but the cost does keep some from ever finding out really how good we have it with the Standard S50 Bus and the FLEX™ disk system or OS-9™. The availability of some other small and less expensive (to start) computers has hurt us. They have sold the hardware and software and we lost a fellow 68XX user! By the time, if ever, that the person who bought something else finds out about all the shortcomings and lack of utility of his or her xbrand computer, it is too late, the nest egg is spent and they are 'stuck' (in more ways than one) with what they have bought. They loose and we loose, the only ones who won are the other fellows! It is a fact, the more users of a particular system, the more choice of hardware and software becoming available for that particular brand of computer, regardless of its quality or price. It is simply a matter of making available merchandise to sell to the LARGEST group; the more users the more bucks to be garnered. And the more profit any advertiser can show the more time and effort he will spend to develop better products. Nothing hinders the availability of products as much as a lack of sufficient profit!

One of the divisions of Computer Publishing, Inc. (CPI) is Data-Comp/SE Media, who sell hardware and software worldwide. It is the policy of CPI not to allow Data-Comp or SE Media to advertise any item that is advertised by one of our regular advertisers. The logic is simple, we have an obligation to the advertiser who spends his money with us, not to compete with him and you the readers are not shortchanged for the product is still advertised and available. This is more fair than a lot of other computer magazines I know of.

Because of a desire to entice more beginners to start up with the 6809 I have authorized Data-Comp and SE Media to license the Steve Odneal drivers and other utility patches for FLEX™ and FLEX™ related software. Also they will be offering a special version of several disk controller boards including the RadioShack controller (locally modified) or software patches to allow FLEX™ and hopefully OS-9™ later, to run on the color computer. At the present the system is running on the Exatron expansion and controller board, with some RFI (hopefully being corrected). This brings a 16K color computer up to 48K of RAM and the ability of using FLEX™ or the original system, be it RS or Exatron. As of this date we have most all normal FLEX™ software running and more is becoming available very soon.

The system or software can be purchased from Data-Comp in bits and pieces. Say you already have a 4K CC then all you would need is the 16K expansion, the controller with RAM expansion, our patches and modifications and FLEX™; NOTE: here is one very important aspect of this entire matter, FLEX™ is a copyrighted product and we INSIST that this FLEX™ be purchased from an authorized source!!! I know there is always the temptation to 'borrow' a copy from a friend BUT this in some cases will not work and also it HURTS us all, more so than you might believe, for TSC has spent literally thousands of dollars in bringing us FLEX™ and we hurt us all if we 'cop' a copy. Data-Comp will be selling an 'authorized' FLEX™ with all the patches available, at a very reasonable price, so really there is no valid need to 'steal a little' and bum or borrow a copy from some unauthorized source. It may seem a small thing, but it could hurt us all!!!

Data Comp will make available the FLEX™ with patches and mods as necessary, the Memory Expansion kits, disk controller boards, patches and mods to other 6809 software and even the Color Computer for those who do not already have a color computer.

A complete modified Color Computer with 48K RAM, single disk drive (double density type), controller board with modifications done for about \$1595.00. Yep, a computer system with disk for less than \$1600.00. THIS WE HAVE NEEDED FOR A LONG TIME! The system can use up to 4 drives, so storage will be no problem. Also coming will be double sided double density systems for those who really want to go hog-wild. Actually by that time I suspect that the color system will take back seat to a more complete Standard S50 Bus system.

Watch 68 Micro Journal for additional information and reread some of Bob Nay's Color Computer User Notes for additional info about this system.

DMW ---

A to D

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16-CHANNEL A TO D CONVERTER

Question: How do you get a 16-channel A/D converter that is accurate, easier to use, faster, and still less expensive than the popular commercial model? Answer: Build it yourself! It may be easier than you think.

Unlike some other A/D converters, the 40-pin DAS-952R, available from Datel Intersil, does all of its conversion in hardware. This simplifies the software to the point of being trivial. To use the converter from BASIC program, for example, only two lines of code are needed. In addition, since I did not use a PIA or other programmable type interface chip, hardware initialization is not necessary.

The Chip

The DAS-952R is described as a monolithic CMOS 16-channel, 8-bit data acquisition system. High accuracy and excellent repeatability are claimed which I can vouch for. After using SWTP's Joystick Interface, I was amazed by the stability of the DAS-952R. The converter uses a chopper-stabilizer circuit which minimizes thermal effects and long-term drift. The conversion is done with a 256R ladder to ensure monotonicity. This chip is a ratometric converter, meaning the output value is expressed as a percentage of the input reference voltage. As long as variable

resistance inputs are used (joysticks, pots), a precision voltage reference source is not required in the circuit. The chip has latched address inputs, tri-state outputs, and uses less than mA of supply current.

The Circuit

To interface the DAS-952R to the SS-50 bus, only two chips are required. Taking advantage of the motherboard decoding, I built the circuit on a 30-pin I/O card using only a single 74138 decoder and a 8098 (figure). Since so much room was left on the card, I added one additional chip, a 74240, for an 8-bit Input port (figure 2). To this I connected eight toggle switches for use as sense switches.

The circuit for the A/D converter is shown in figure. The analog signals are connected to the 16-channel input pins, CH1 - CH16. I used variable resistors and connected the wipers to the channel input pins. One side of each resistor is connected to +5 volts from the voltage regulator, and the other side is connected to ground.

The four inputs, CA1 - CA4, select which channel of the 16 is to be converted. I connected these inputs directly to the data bus. The 74138 decoder is wired to send a write pulse to the AE (channel address enable) input to latch the channel number. The same pulse initiates the conversion process by strobing the SC (start of conversion) input. This simplifies programming: to select a particular channel, the computer simply writes the channel number into the first address of the I/D port.

When the A/D chip completes its conversion, the EOC (end of conversion) output goes high. I connected this output through a tri-state inverting buffer to bit seven of the data bus. The 74138 decoder enables this bit so a read operation of the first port address will return the status of the conversion.

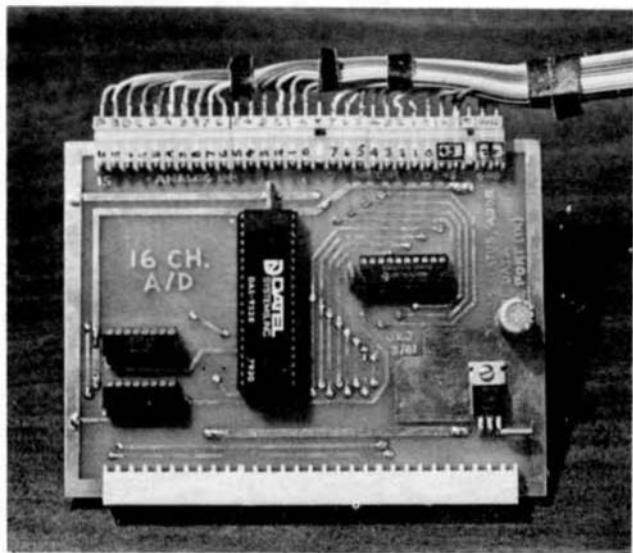
When bit seven goes low, the conversion is complete and the output of the desired A/D channel is ready for the data bus. A read operation on the second address of the port is decoded by the 74138 which sets the OE (output enable) pin high and returns the 8-bit converted data.

The +5 voltage is supplied by a 5-volt regulator (not shown). The +5 supply is tied to the positive reference pin (V+ ref) and to one side of each variable resistor. The negative reference pin (V- ref) and the other side of each variable resistor are connected to ground. Pin EC (expansion control) must be pulled high for the channel multiplexer to work. The output of the channel multiplexer (MUX out) is connected directly to the input of the converter (A/D In).

The .0033 microfarad capacitor sandwiched between two inverters deserves some comment. When I first built the circuit, I used a breadboard strip to test and debug the design. After I was satisfied that it worked, I etched a printed circuit board and made a plug-in card for my computer. To my surprise, the PC card version would not select the channels reliably although the breadboard worked perfectly! A little detective work revealed the AE (address enable) input has to be delayed 50 nanoseconds after the channel address lines (CA1 - CA4) have stable data. The capacitance in the breadboard layout must have delayed the signal enough. For the PC card, the capacitor delays the pulse about 60 ns while the inverters buffer and condition the pulse. I will be the first to admit I am no hardware expert; and although this works, there may be a better solution.

The Card

I used a double-sided PC board for this



project, with molex connectors mounted along the top edge to allow connection to the 16 pots. I included eight additional connection points for the eight sense switches. The entire circuit fits very nicely on a 4" by 5.5" card (photo) with room to spare.

If PC card construction is not your idea of a good time, wire wrap or even point to point wiring should be easy on a circuit this simple. Since only one tri-state inverter was needed out of six in the 8098, I used the first four as simple inverters, grounding their common enable (pin). If you build this circuit, don't forget power and ground to each chip. The DAS-952R is a CMOS chip, so be sure to take the proper precautions to prevent damage by static electricity.

Test the circuit by connecting several potentiometers between +5 and ground. Connect the wipers to CH1 and other inputs. Using the short BASIC routine given below, try reading the channel inputs. The value from the converter should vary from 0 to 255. Unconnected channels will float, returning unpredictable values.

If you have a problem, use an oscilloscope to determine that the proper signals are getting through the 74138 decoder. Pin 5 should go momentarily low when a write is made to the first address of the port. Pins 1 and 10 should show low pulses when the converter reads the first and second addresses, respectively, of the port. By the way, I used a 74138 decoder rather than a 74LS138. I had problems with the 74LS138 when breadboarding the circuit, but never tried one with the final PC version.

One final point, if your system clock is faster than 1.2 MHz, you may have to supply a separate clock or add a flip-flop to divide the system clock by two.

The Software

As mentioned earlier, the software required to use this A/D converter is simple. In BASIC, only two lines of code are required:

```
100 POKE P,C  
110 D = PEEK( P + )
```

The variable P is the address of the port into which you have plugged your card. The variable C is the channel (1 through 16) that you desire to read. The variable D will contain the 8-bit data from the selected channel.

A program to read all 16 channels for test purposes might look like this:

```
10 REM : DEFINE PORT ADDRESS
15 P = HEX("E070")
20 REM : READ EACH CHANNEL
25 FOR C = TO 16
30 POKE P,C
35 PRINT PEEK(P+C)
40 NEXT C
```

Now, what could be simpler!

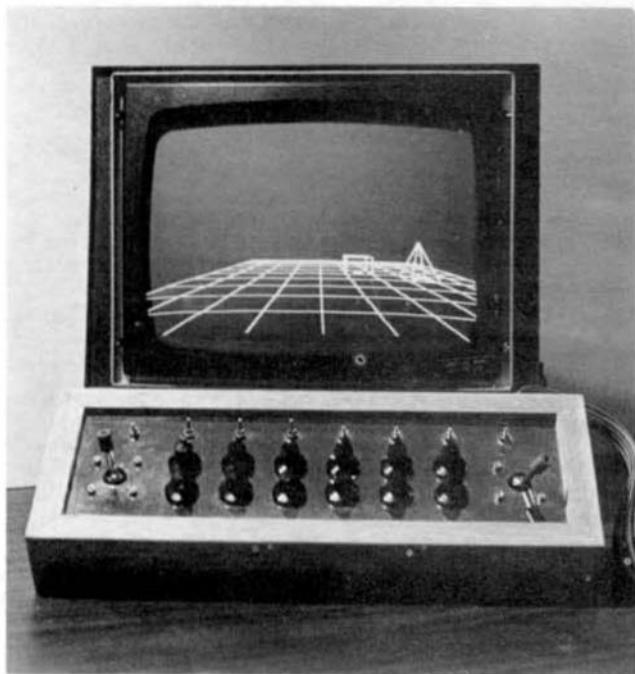
If you need to work in assembly language, the code required is nearly as simple. The only additional requirements are a short delay and a test for the end of conversion. For example, using extended addressing and the 6809 processor:

```
* ENTER WITH THE DESIRED CHANNEL
* NUMBER IN THE B-REG.
* EXIT WITH THE CONVERTED DATA
* IN THE A-REG.
```

```
GETAD STB PORT
LBRN *      DELAY
LBRN *
GETAD2 LDA PORT GET STATUS
BMI GETAD2 WAIT FOR EOC
LOA PORT+1 READ DATA
```

The STB at the label GETAD selects the channel and starts the conversion. The two LBRN's are more than enough delay to allow EOC (bit 7) to go high. The loop at GETAD2 waits until EOC returns low, signaling the end of conversion. The final LOA reads the converted data.

With assembly language, 0000 to 12000 conversions can be made each second!



What Now?

What do you connect 16 channels to? Any combination of joysticks, rotary pots, or slide pots are easily connected. The value of the resistor is not critical. I have tried everything from 500 ohm to-MEG pots and nearly anything works. With higher resistances, however, noise is more likely to be a problem. With-MEG pots, I had trouble when the wires from the pot were routed near a video monitor. On the other hand, if the resistance is too low, you might draw too much current (remember, each resistor is connected directly between +5 and ground). You should have no trouble with anything between 500 ohms and 0K ohms.

I built this circuit primarily to control a 3-D graphics program. I needed to control seven parameters giving the position and orientation of the viewer within a three dimensional data base. I made a control box (photo 2) with two joysticks, twelve pots, and eight toggle switches. Each of the parameters are controlled by one or two pots, depending on the precision desired. Any parameter may be switched by software control to either axis of either joystick. The toggle switches are connected to the 8-bit input port on the A/D card. If a switch is up, the associated control is interpreted as proportional data. If a switch is down, my program turns that control into an integrating A/D input. The flexibility provided by the 16 channels of input allows me to travel at will through the 3D data base using the video screen as a window.

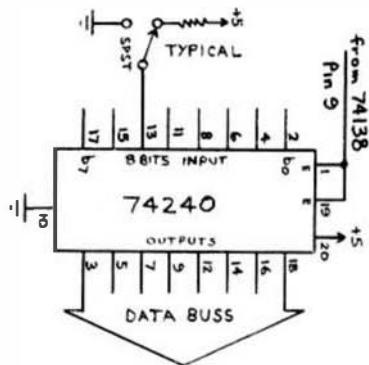
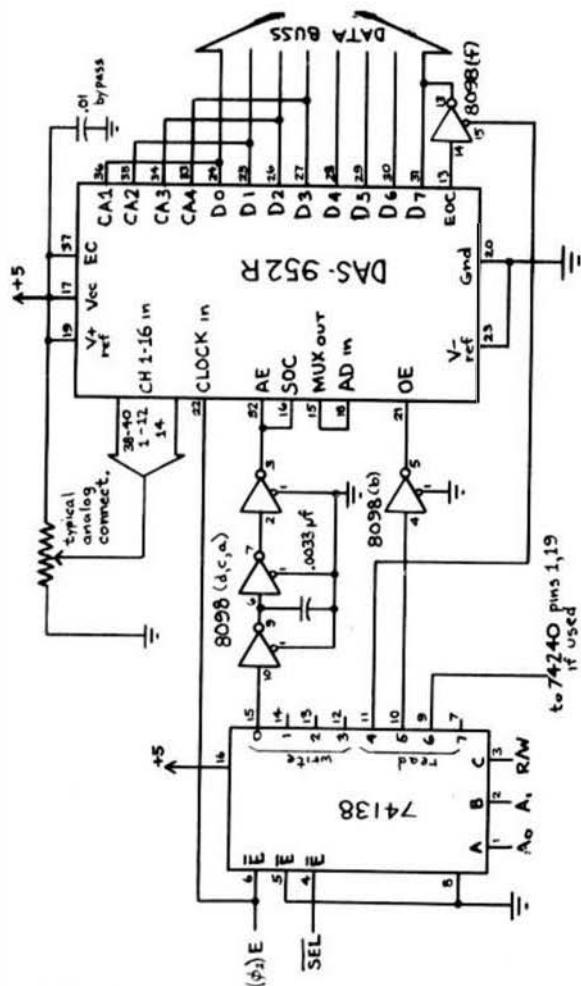
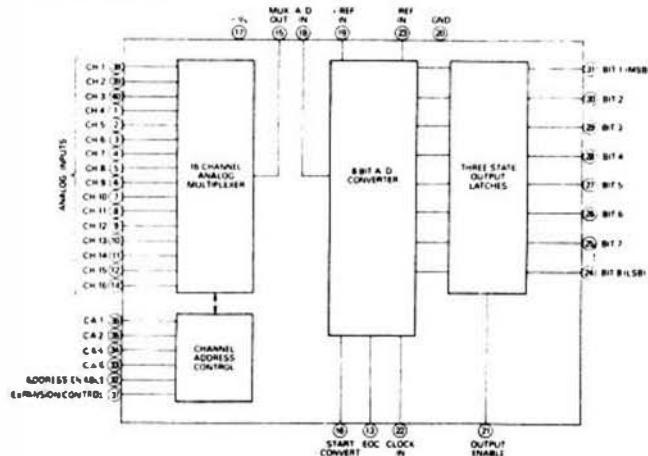
Incidentally, I am using an S-100 bus ALT-512 high resolution graphics board as my video graphics output. This card packs a lot of features, including two-page operation for flicker-free animation. I built a piggy-back interface card to use the ALT-512 on the SS-50 bus.

The heart of the 3D program is a subroutine from SUBLOGIC that does the 3D to 2D transformation. A 6809 version is not available, so I disassembled the 6800 program, did some optimization for speed, and reassembled it for the 6809 processor. Deciphering the program was not trivial - if you desire to do the same, drop me a line and I might be able to save you some time.

In Conclusion

I feel that the time and effort I put into this project was well spent. If you have a use for analog input to your computer, consider building this circuit. I don't think you will be disappointed.

The DAS-952R is available from DATEL INTESSIL, INC., 1 Cabot Boulevard, Mansfield, MA 02048 / phone: (617) 339-9341. Price is \$29.50 for quantity to 24. The National Semiconductor ADC0816 / ADC0817 appears from the data sheet to be pin compatible with the DAS-952R.



B+Tree Index

B+-TREE INDEXING AND ACCESS SYSTEM

The B+-Tree Indexing and Access system allows the user to create, maintain, and use indexes to a file of records. The system is written in TSC's version of Pascal for the UniFLEX operating system. The system is provided by MetaMicro Library Systems, Inc. of San Antonio. The user should be familiar with the UniFLEX operating system, the programming language Pascal, and TSC's implementation of Pascal for UniFLEX.

So, what is the B+-Tree Indexing and Access System? If you use data files with a large number of records, it could be a great help. The system provides the user a method to index and access the main record number and up to 101 keys per record for each record in a data file. For example, in testing the system programs a file which varies in record size from 925 to 950 was used. Each record consists of a student name, i.d. number, various kinds of personal information, and the classes each student attends during the school year. The Indexing and Access System programs are used to insert, delete, and retrieve the record numbers and certain information (the keys) for each record. In this manner the data file itself does not need to be manipulated to retrieve data. The Indexing and Access System programs are run to look up desired information. If need be, the record of the data file can be accessed directly by use of the UniFLEX procedure seek.

The b+-tree holds the index when the index itself is too large to fit in main memory. MetaMicro states that storage utilization is no worse than 50 percent; any key in an index containing 1,000,000 keys can be found in only four disk accesses. The "+" in b+-tree means that once any key is found, the next key can be retrieved in at most one disk access; in 27 out of 28 times no additional disk access is necessary.

Now you're asking yourself, what do I get with MetaMicro's Indexing and Access System? The system consists of eight programs on an 8 inch single sided, single density disk.

binit creates an empty b+-tree file.
binsert inserts keys into a b+-tree.
bdelete deletes keys from a b+-tree.
bfindx searches for keys specified exactly.
bgetx retrieves record numbers marked by the key found by bfindx.
bfind searches for keys that begin with a specified character sequence.
bget retrieves record numbers found by bfind.
brdkeys reads keys from a b+-tree.

The programs are written in TSC's Pascal and compiled into executable files. There are no source listings for the programs.

The program 'binit' is an operating system executable command to initialize the b+-tree. The other seven programs cannot be run directly, but must be run from a user written Pascal program using the procedure run. The seven programs are compiled under the "standard environment" of TSC's Pascal compiler. Also provided are seven additional programs which are the same seven programs compiled in the "system environment."

In addition, provided on the disk, there are source code listings for five programs written in Pascal with the p-extension. These five programs are sample programs which run the programs of the Indexing and Access System. They were especially helpful in testing the system programs. It must be mentioned that even though these sample programs were provided as an aid to the user, one of the programs was not complete because of a disk CRC error. Although the error was simple to correct, it was an inconvenience.

The user's manual is well-written. There seems to be a sufficient amount of information concerning the b+-tree structure to aid the user, and still not confuse him with unnecessary detail. Those desiring additional information are referred to four well-known references on data structures.

Each of the seven programs comprising the Indexing and Access System are described in a manner that seems to be most helpful to the programmer who will write the programs which run the indexing and access system programs. That is precisely the function of this user's manual: a guide to the programmer who is writing the programs to implement the system.

The programs in the system seem to run quite successfully. Using several test runs, there were no problems with the Indexing and Access System programs. The programs seem to be efficient and fast. The sample programs were converted somewhat to test the system programs. Although these sample programs served their purpose as provided, there are several refinements which should be incorporated to make them more usable: make the input either interactive or file based, better checks on possible input errors, better format on the screen output.

There was no price quoted to the reviewer; therefore, it is difficult to estimate some sort of usefulness factor for the prospective user. However, if the potential user uses TSC's Pascal in his UniFLEX operating system and uses large data files, MetaMicro's Indexing and Access System is worth considering.

DISCUSS.CMD

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DISCUSS.CMD

This program is an adaptation of John Champlain's DISKSAVE in the August '68'MICRO, but I almost completely rewrote it to take advantage of the 6809 instructions, and also to add a couple of features I thought would be useful.

The main function of the program remains the same, to allow the user access to a disk without going through the directory (especially if the directory has been damaged, which happens to me fairly often), and to copy a sector or block of sectors to another disk. I changed the names for some of the commands I found confusing: B (backup) becomes L (last sector), C (change) becomes W (reWrite), S (save) becomes C (copy), D (display commands) becomes M (menu), and M (monitor) is changed to F (FLEX).

SO THE NEW LIST OF COMMANDS READS:

- R- Read sectors
- N- Next sector
- L- Last sector
- S- Skim through file
- T- Table of sectors
- W- reWrite data
- C- Copy to other disk
- A- Another file
- F- Return to FLEX
- M- Menu of commands

More serious is a change to the whole format of the "change (rewrite) data" section, which I put in a form very similar to that of the S-BUG "M" function. I also added to it the possibility of inputting the new data either in hex or in ASCII format (44 and 'D mean the same, as in the Assembler), and to go back one byte. The ASCII format input I added to allow small

corrections to a text file without having to go through the Editor (at the condition that the change made fits into the same space on disk), or (mostly in my case) to change the strings in machine language programs and functions without having to disassemble and reassemble the whole thing. I have used it to translate into French and Spanish the messages in FLEX and the utilities and disk commands.

I also made some changes in the display on screen, partly to clean it up, and partly because I don't have a SWTP terminal, and my control codes are different. But since I gave those codes labels right at the start of the program, you can change them back easily to what suits your system, or your aesthetic sensibilities.

The two added features are the S (skim through file) command, and the A (another file) command. The first starts from the present sector, and builds a file sector map without showing each sector, and without requiring you to type N,N,N,N... ad infinitum. On a 50 or 100 sector file, it's pretty useful. The second "remembers" the last sector of the last file you read or copied, and starts looking for another file right at the next sector. Good for going through a bombed disk searching for various important pieces of data or programs.

This is one of my first efforts in assembly language, and I would like comments, corrections and suggestions. In particular, about the way my look-up table for commands is set up and stepped through using the indexed JMP instruction, and about the way I eliminate the return addresses on the stack with a LEAS 2,S in the error-catching subroutines. Is there any danger of getting lost doing that? It seems to work fine. I hope this program will be as useful to others, in its new form, as DISKSAVE has been to me. With a big thanks to John Champlain,

Yves LECLERC

Note: The sometimes strange labels are abbreviations from the French in which I first wrote the program. And by the way, if anybody out there is French or Spanish speaking and uses SWTP or other 6809 SS50 stuff, and is interested in

exchanges in a language other than English (I have French versions of FLEX and XBASIC working, and Spanish ones in the works), I wish they would get in touch with me.

DISCUS.CMD

12-1-81 TSC ASSEMBLER PAGE 1

* DISCUS (Slot 0)

* ADAPTED FOR THE 6809 FROM DISKSAVE
* BY JOHN CHAMPLAIN
* 68 MICRO 8/81

* Yves Leclerc 11/29/81

* FILE ROUTINES

CD3C	OUTHEX	EDU	9CD3C	
CD42	DETMER	EDU	9D42	
CD27	NXTCHR	EDU	9C027	
CD24	PCRLF	EDU	9CD24	
CD18	PUTCHR	EDU	9CD18	
CD03	WHRMS	EDU	9C003	
CD1E	PSTRNG	EDU	9CD1E	
CD15	GETCHR	EDU	9CD15	
CD1B	INBUFF	EDU	9CD1B	
CD34	OUTDEC	EDU	9CD39	
CD	GETFIL	EDU	9CD2D	
CD33	SETEXT	EDU	9CD33	
D404	FMS	EDU	9D406	
C600	FCROUT	EDU	9CB40	
9520	FCBIN	EDU	90520	
0530	PIST	EDU	FCBIN+30	Sector/hard address
0523	DISOI	EDU	FCBIN+3	Disk slot #
C0B0	LINBUF	EDU	9C0B0	Input buffer address
0220	SPAC	EDU	920	ASCII for <space>
D0D0	CRET	EDU	900	ASCII for <carriage return>
000A	LNFED	EDU	90A	ASCII for <line feed>
000C	CLSCR	EDU	90C	ASCII for <clear screen>

* VARIABLES

0100		ORG	9100		
0100		TABLE	RMB	\$300	Table of sectors
0400		POTBL	RMB	2	Pointer to table
0402		LIGN	RMB	1	Print pointers
0403		LIGN2	RMB	1	
0404		TEMP	RMB	2	
0406		TEMP1	RMB	2	Utilities
0408		TEMP2	RMB	2	
040A		DFLGS	RMB	1	
0408		DPIST	RMB	2	
04D0		LDNQ	RMB	2	Last sector address
					Length of present file

* TABLE OF COMMANDS

U40F S2		COMM	FCC	"R"
0410 C2F6			FDB	OBJECT
0412 45			FCC	"L"
0413 C1B0			FDB	COP (FL
0415 53			FCC	"B"
0416 C3C2			FDB	SMFILE
0418 57			FCC	"H"
0419 C244			FDB	REWRIT
041B 4E			FCC	"N"
041C C140			FDB	MTBCI
041E 41			FCC	"A"
041F C3F4			FDB	ANOTHF
0421 54			FCC	"T"
0422 C10B			FDB	SHOTAB
0424 4C			FCC	"L"
0425 C16B			FDB	PRECE
0427 4D			FCC	"M"
0428 C124			FDB	REBEG
042A 46			FCC	"F"
042B C003			FINEND	WHRMS

0100		ORG	9C100	Beginning of program
------	--	-----	-------	----------------------

C100 20	01	PREM	BRA	DEBUT
C102 03		VN	FCD	Version

* PREPARATION

C103 7F	016A	DEBUT	CIA	ONFLG	Initialize flag
C106 79	0100		CIA	TABLE	and table
C109 7F	0101		CIA	TABLE+1	
C10C BE	0100		LDX	#TABLE	Note address of table
C10F BF	0400		BTX	POTBL	
C112 BD	C027		JSR	NXTCHR	Which disk?
C115 24	02		BCC	UNITE	
C117 B6	30		LDA	"0"	A Default: 0
C119 B0	30	UNITE	SUBA	0030	From ASCII to hex
C119 B1	01		CMPA	01	
C11D 23	02		BLS	CANA	
C11F B6	01		LDA	01	Default: 1
C121 B7	0523	CAVA	STA	DISOI	

* GENERAL MENU

C124 BE	C414	REBEG	LDX	#MSG1	Point to message
C127 90	CD1E		JSR	PSTRNG	and display
C12A BD	C015	CLAV	JSR	DETCHR	Take a command
C12D BE	040F		LDX	SCPPRD	Point to list
C130 A1	00	LKCMD	CMPA	0,X+	Look for a match
C132 27	00		REG	OKCMD	Yes, next step
C134 30	02		LEAI	2,X	No, loop on
C136 BC	042B		CMPX	#INCMD	End of list?
C138 25	F3		SCB	LKCMD	No, continue
C138 7E	ED03		JMP	WHRMS	Yes, exit
C138 4E	94	OKCMD	JMP	(0,1)	Jump to routine

C140 12 00E3 H18C1 LDBR FLGY
 C143 FD 053E LDBR P1BT Present sector
 C146 FD 040B STD P0BT Becomes last
 C149 BE 0520 LDY #FCBIN Goto FCB
 C14C BC 08 20 LDD ZA,X Length of file
 C14F FD 060D STD LDMD
 C152 EC 08 40 LDD A,X Next sector
 C155 C1 00 CMPB #0 All done?
 C157 27 16 BEQ FINI
 C159 FD 053E B10 P1ST Yes, new address
 C15C 7E C312 JMP AJMEL Show sector
 C15F BE C622 FINI LDT #FCGO
 C162 BD C01E JSR PSTRD
 C165 16 00B5 LBRA [INS] Go for next command

I LAST SECTOR

C168 17 D0B0 PRECZ LDBR FLGY
 C169 BE 0600 LDX P0TBL Refer to table
 C16E 30 1E LEAI -2,X Go back one step
 C170 BC 0100 CMPX #1ABE Start of table?
 C173 27 00 BED PABO? Yes, jump out
 C175 30 1E LEAL -2,X No, go back one more
 C177 EC 04 LDD X,A
 C179 FD 053E B10 P1ST
 C17C BE 0600 BTR P0TBL
 C17E 7E C312 JMP AJMEL Display sector
 C182 BE 04F1 PABO? LDX BEAR2
 C185 BD C01E JSR PSTRD
 C188 16 00B2 LBRA [INS] Go for next command

I FILE MAP

C1BB 17 004B BHOTAB LDBR FLGY
 C1BE BE CA54 LDA BM807 Table heading
 C191 BD CD1E JSR PSTRD
 C194 BE 0100 LDI P1ABLE Top of table
 C197 C6 1A COMPT LDB #10 Define format
 C19C BD C59D DIR JSR IMPHEX Print addresses
 C19F 30 01 LEAI 1,A
 C1A1 BD C340 JSR IMPHEX
 C1A4 30 01 LEAX 1,
 C1A6 8C 0400 DMPB P0TBL Finished?
 C1A8 27 72 MED INT1 Yes, go for next command
 C1A9 34 DEC0 No, next line
 C1AC 27 E9 MED COMPT
 C1AE BD C590 JSR ESPEC
 C1B1 BD C59C JSR ESPEC
 C1B4 20 E6 ZRA DIR

C1B6 BD 0E COPYPL LDBR FLGY
 C1B9 BE C62F LDX BM506 Get file name
 C1B9 BD LDIE JSR PSTRD
 C1B9 BD CD1B JSR INBUFF
 C1C1 BE C640 LDI #FCBIN1
 C1C4 BD CD20 JSR GETFL
 C1C7 BD 90 LDA #5 Adjust extension
 C1C9 BD CD33 JSR BETEXI Define new file
 C1CE AF 00 17 CLR 25,A
 C1D1 AF 0F CLR 15,A Open for write
 C1D5 Be 02 LDA 02
 C1D5 AF 07 STA 0,X
 C1D7 BD 0400 JSR FMS
 C1D9 BD FF LDA #1
 C1DC AF 07 STA 59,X

C1D9 BE 0520 PRSECT LDI #FCBIN Set a sector
 C1E2 FD 0100 LDD TABLE Over, close file
 C1E5 27 2C NEG FERM
 C1E7 ED 00 1E STD SU,1 In slot 01
 C1EA Be 01 LDA 01
 C1EE AF 03 SIA 2,X
 C1F1 Be 04 JSR DEPLA Shift table
 C1F3 Be 04 LDA #9 Read sector
 C1F5 AF 03 SIA 6,X
 C1FB BD D404 JSR FMS
 C1FB 30 00 00C2 LEAX 194,A
 C1FC 1F 12 TFR X,Y
 C1FE BE C640 LDI #FCBOUT Prepare for copy
 C201 AF 04 CLR 0,X
 C203 AF 03 CLR 2,A
 C205 Be 02 LDB #126

C207 AF A3 ECRC LDA B,Y Transcribe
 C209 BD 0406 JSR FMS character by character
 C20C BC INCB
 C20D C1 7E CMPB #126 End of sector?
 C20F 26 F6 BNE SCRC
 C211 20 CC BRA PRSECT Yes, next sector

I DO GET NEXT COMMAND

C210 BE C602 JMBT LDX BM505 Initials of commands
 C220 BD CD1E JSR PSTRD
 C223 7E C12A JMP CLAV
 C224 16 0400 FLGY LDB OKFLG Sector already read?
 C229 27 01 BED FLON
 C229 39 ATR
 C22C 32 62 FLON LEAB 2,B No, get rid of RTS
 C22E BE 0408 LDI #ERA Error message
 C231 90 CD1E JSR PSTRD
 C234 20 E7 BRA [INS] Get next command

I REWRITE DATA

C236 31 3F AVANT LEAY -1,Y Preceding byte
 C238 BD CD24 JSR PCRLF
 C238 1F 21 TFR Y,X
 C23D BC 0404 CMPX TEMP Start of sector?
 C240 24 29 RCC CHECKT
 C242 20 5B BRA FINOCT

C244 BD ED REWRT BBR F BY
 C246 BE 042F LDX #FCB10 Get address
 C249 BD CD1E JSR PSTRD
 C24C BD CD1B JSR INBUFF
 C24F BD CD42 JSR GETHEX
 C252 1F 10 TFR X,D
 C254 BE 0500 LDX #FCBIN+64 Find wanted byte
 C257 1F 12 TFR X,Y

C239 10BF 0404 STY TEMP Note lists of text
 C250 31 A9 0100 LEAY \$100,Y
 C261 10BF 0406 STY TEMP1
 C263 3A ABX
 C266 BD C024 JSR PCRLF
 C269 1F 12 TFR I,Y Set it aside

C269 17 0132 D#B0CT LDBR ESPCE Display present byte
 C26E 17 012F LDBR ESPCE
 C271 BD CD3C JBR OUTHER
 C274 17 0129 LDBR ESPCE
 C277 17 0126 LDBR ESPCE
 C27A BD CD15 JSR GETCHR Is it alphanumeric?
 C27D 01 27 CHPA 61 RALPH Is it a stepback?
 C281 01 5E CHPA 61 AVANT
 C285 BD 23 BDR AJMEL No, process it
 C287 1F 09 TFR A,B

C289 50 ASL,B
 C290 50 ASL,B
 C290 BD CD15 JBR GETCHR Get other nibble
 C290 BD 18 BDR AJMEL Process it
 C292 B4 0F PAND #3000001111
 C294 34 04 PSMS B AND combining them

C298 AB 60 ADDA 0,5+ RNP0C1
 C299 20 20 BRA RKA

C29A BD CD15 PRALPH JSR GETCHR Get the character
 C29D 20 28 BRA RNP0C1

C29F 32 62 FINOCT LEAB 2,B Get rid of RTS
 C3A1 17 000C LDBR AFFICH Show sector
 C3A4 20 21 BRA RENDIE

C3A6 32 62 REPROC LEAB 2,B Get rid of RTS
 C3A8 20 1F BRA OCTSUI Data next byte

C3A8 B1 00 AJMEL CHPA #CRET Process nibble
 C3A8 27 F1 SEQ FINOCT End of operation?
 C3A8 B3 30 CHPA #0 No, see if n=0
 C3A8 25 F4 BCS REPROC
 C3B2 B1 46 CHPA #F *** or n>15
 C3B4 22 F0 BHI REPROC
 C3B4 B1 41 CHPA #A *** or between 9 and A
 C3B6 24 04 BCC PRHEK2
 C3B8 B1 39 CHPA #9 REPROC
 C3B8 22 E8 BH1 REPROC

C3B8 BD 30 PRHEK2 SUBA 40 From ASCII to hex
 C3B9 B1 09 CHPA 09
 C3C2 25 02 BLS PRHEK3
 C3C4 80 07 SUBA 47

C3C6 30 PRHEK3 RTS

C3C7 AF A4 EFFECT STA 0,Y Write new byte
 C3C9 31 21 OCTSUI LEAY 1,Y Data next
 C3C9 BD 0024 JSR PCRLF
 C3C8 1F 21 TFR Y,X
 C3D0 BC 0406 CHP1 TEMP1
 C3D3 2C CA BDE FINOCT
 C3D5 20 94 BRA CHB0CT

C3D7 BE 04AB RENDIE LDX #MM614 Rewrite sector?
 C3D8 BD CD1E JSR PSTRD
 C3D9 BD CD15 JSR GETDM
 C3E0 B1 39 CHPA #Y
 C3E2 1026 FF37 LBRN INS1 No, get next command
 C3E4 BE 0520 LDX #FCBIN Yes, send sector to disk

C3E9 B6 0A LDA B10
 C3E9 B7 84 STA 0,X
 C3ED BD D406 JSR FMS
 C3F0 BD CD24 JSR PCRLF
 C3F3 16 FF27 LBRA [INS]

I READ A SECTOR

C3F6 7C 0408 RDSECT INC DMFLG Initialize table
 C3F9 BE 0108 LDX #TABLE
 C3F9 BD 0400 BTX P0TBL
 C3FF BE C561 LDI #MM602 Get address
 C302 BD CD1E JBR PSTRD
 C305 BD CD1B JSR INBUFF
 C308 BD CD42 JBR GETHEX
 C309 1025 FE15 LSCB REBED In case of error, exit
 C30F BE 053E STA P1ST

C312 BE 0400 AJTABL LDX P0TBL Refer to table
 C315 FC 053E C318 ED 81 STD 0,1= Write down address
 C318 AF 84 CLR 0,1 Clear next position
 C31C AF 01 CLR L,X
 C31E AF 0400 BTX P0TBL #FCBIN
 C321 BE 0520 LDX #FCBIN Note position
 C324 B4 05 LDA 0,Y Read the sector
 C326 AF 84 STA 0,X
 C329 BD 0406 JSR FMS
 C329 BD 03 BBR AFFICH and display it

I DISPLAY A SECTOR

C330 7F 0403 AFFICH CLR LIGH2 Clear screen
 C333 B6 0C LDA P1ST
 C335 BD 0D18 JBR PUTDW
 C336 BE 040E LDZ #MM616 Display address
 C338 BD CD1E JBR PSTRD
 C339 BE 053E LDI #FCBIN+30
 C341 BD 3A BBR IMPHEX
 C343 30 01 LEAX 1,X
 C349 BD 56 BBR IMPHEX
 C347 BE C574 LDX #MM604 Column heading
 C344 BD CD1E JSR PSTRD
 C346 BD C024 JSR PCRLF
 C350 BE 0403 LDX #LIGH2 Line numbers
 C353 BD 40 BBR IMPHEX
 C355 BD 49 BBR ESPEC
 C357 20 DA BRA SGT
 C359 BF 0404 BCT ETX TEMP
 C35C BD 47 BBR COL0NN Reposition
 C35E BE 0406 LDX TEMP
 C361 20 0B BRA SEC
 C363 BE 0540 BCT LDX #FCBIN+64 Start of contents

C348 86 10 LDA #16 Count of lines
 C348 87 0402 STA LION# Count of columns
 C348 C6 10 SEC LDB #16 TEMP
 C348 BF 0404 FCB# B
 C370 20 04 SECTS PDIS# B
 C372 BD 29 RGR IMPHE# Write a byte
 C 74 30 01 LEAK 1,2
 C376 35 04 PDIS# B
 C378 5A 01 DEC#
 C379 26 F5 INE SECTS Complete?
 C37B 80 23 SPC# No, goto next
 C37D BE 0404 LDI TEMP Replace counters
 C380 C6 10 LDB #16
 C380 A6 64 SEC# 0,1 Note ASCII equivalent
 C384 64 7F AND# 00FF
 C386 B1 1F CMP# Replace control character
 C388 21 07 SHI SEC#15
 C38A 86 26 LDA #1,2...by a period
 C38C BD CD10 BECTS JBR PUTCHR
 C38F 30 01 LEAK 1,2
 C391 5A EF INE SECT#4 Finished?
 C392 26 FF SEC# No, goto next
 C394 BD CD24 JSR PCRLF
 C397 7A 0402 DEC LION# Next line
 C39A 26 80 INE SECT#2
 C39C 30 RTB
 C39D BD CD3C IMPHE# JBR OUTHE# Writes 2 hexes
 C39E 86 20 ESPCE LDA 0BPAE
 C3A2 7E CD10 JMP PUTCHR
 C3A3 86 10 COLON# LDA #16 Print col. numbers
 C3A7 BB 0403 ADD# LION#2
 C3A8 B7 0403 STA LION#2
 C3A9 BE 0403 LD# MLIION#2
 C3B0 BD EB MBR IMPHE#
 C3B2 20 EC BRA SPC#
 C3B4 10BE 0100 DEPLA LDY #TABLE Shift address table
 C3B6 EC 22 PLUS LDD 2,Y Move back one value
 C3B8 ED A1 STD Y++ by two bytes
 C3B9 30EC 0400 CMPY PDBL
 C3C0 26 F6 INE PLUS
 C3C2 39 RTB
 * SKIN THREBIN A FILE
 C3C3 8E 0520 SKMFIL LD# #FCB#IN Point to FCB
 C3C6 10BE 0400 PRCIN LD# PDBL
 C3CA FC 053E PRCIN LD# PIST Set address
 C3CD FD 0400 STD #PIST Note it
 C3D0 EC 00 40 LDD 64,X Get next address
 C3D3 27 12 DEO DEM# End of file?
 C3D5 FD 053E STD PIST MD, note address
 C3D8 ED A1 STD 0,Y++ Move the pointer
 C3D9 4F A4 CLR L,Y Clear position
 C3D8 6F 21 CAR L,Y
 C3DE 86 09 LDA 0,1 Read the new sector
 C3E0 A7 04 STA FRS
 C3E2 BD D406 JSR FMS
 C3E3 20 E3 BRA PRCIN Goto next
 C3E7 EC 00 20 BERN LDD 32,X Get file length
 C3E8 FD 0400 STD LONG
 C3E9 10BE 0400 STD PDBL
 SF4 17 FE2F ANOTH# LDB# FLDY Initialize table
 C3F7 8E 0100 LDB# #TABLE
 C3FA BF 0400 STD PDBL Compute next address
 C3FB FC 053E INC#
 C400 3C C001 C1 3A CMPB #53A Change track if
 C403 23 09 MLC ANTH#2 necessary
 C405 C6 01 LDB #1
 C407 4C INCA #4C
 C408 B1 4C CMWA #4C
 C40A 102E 0403 LDB# NARMS End of file, exit
 C40E FD 053E ANTH#2 STD PIST Note address in table
 C411 16 FEPE AJTAR
 * MESSAGES
 C414 DC MBDI FCB CLSCR HARD-READ AND COPY A DISK*
 C415 20 20 20 20 FCC "#"
 C43C 000A FDB \$000A
 C43E 2 2 0 20 FCC
 C464 000A FDB \$000A
 C466 20 49 4E 53 FCC " INSTRUCTIONS"
 C473 000A FDB
 C475 20 50 52 20 FCC " R- Read some sectors",CRET,LNFEED
 C48D 20 20 4E 20 FCC " N- Next sector ",CRET,LNFEED
 C490 20 20 41 20 FCC " A- Another field in sequence",CRET,LNFEED
 C49F 20 50 53 20 FCC " B- Block through file ",CRET,LNFEED
 C4DB 20 20 43 20 FCC " C- Copy on other disk ",CRET,LNFEED
 C4F1 20 20 57 20 FCC " D- Rewrite data ",CRET,LNFEED
 C504 20 20 46 20 FCC " E- Return to FLEX ",CRET,LNFEED
 C519 20 20 54 20 FCC " F- Table of sectors ",CRET,LNFEED
 C530 20 20 4C 20 FCC " G- Last sector ",CRET,LNFEED
 C542 20 20 4D 20 FCC " H- Menu of commands ",CRET,LNFEED
 C559 57 68 69 63 FCC " Which? ",\$04
 C561 41 64 72 63 MBD2 FCC "Address of sector? ",\$04
 C574 20 20 20 20 MBD4 FCC " 00 01 02 03 04 05 06 -"
 C58C 20 30 37 20 FCC " 07 08 09 0B 0C 0D 0E 0F "
 C5A7 20 20 30 31 FCC " 0123456789ABCDEF",CRET,LNFEED
 C5B8 20 20 20 20 FCC "-----"
 C5D4 20 20 2 20 FCC "-----"
 C5EF 20 20 20 20 FCC "-----",,\$04
 C602 43 6E 6E 60 MBD5 FCC "Command (R-N-L-S-C-A-Z-H-M-F)? ",\$04
 C622 4E 20 20 20 MBD6 FCC "No more sectors ",\$04
 C634 DC MBD7 FCC " (A SEC#-CRET,LNFEED,\$04
 C63F DC MBD8 FCC CLSCR
 C640 20 53 6E 75 FCC " Source disk must be"
 C654 20 61 6E 20 FCC " in e1",\$07,CRET,LNFEED
 C65D 4E 61 60 65 FCC "Name of file (prt .DAT)? ",\$04
 042F ORG \$42F
 042F FCC "Byte can be written in hexadecimal"
 0452 20 60 6F 72 FCC " form, (A5)-CRET,LNFEED
 045F 6F 72 20 69 FCC " or in ASCII, preceded by a single "
 0481 71 75 6F 74 FCC " quote ('')",CRET,LNFEED
 048E 43 6B 61 6E FCC "Change which byte (hex)? ",\$04
 0488 32 67 72 M8 14 FCC "Rewrite this sector? ",\$04

MBD16 FCC " ADDRESS: ",\$04
 MBD16 FCC "Illegal command.
 0484 79 6F 73 20 ERR1 FCC "you must do R first.",CRET,LNFEED,\$04
 0481 6E 69 72 73 ERR2 FCC "First sector."
 FCC "Command (R-N-L-F-E-D)",\$04
 END PREM

MAGISPEL

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STAR KITS' MAGIC-SPELL--review

by Dennis Doonan
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Text editing and word processing are useful functions performed by a personal computer. The text editors and processors for 6800 and 6809 systems are among the most versatile available. It is easy to type an article or report on a terminal, then edit awkward phrases, misspellings and mistakes as you go along. The text may be saved on a disk file or a hardcopy printout made. Words and lines of text can easily be changed, deleted or moved. About the only thing a text editor will not do is proofread copy and correct misspellings or typographical errors.

STAR KITS (P.O.Box 200, Mt. Kisco, New York 10549) is offering such an option to users of 6809 systems. MAGIC-SPELL is a 4K machine language program that compares each word in a text file against a master dictionary and lists each word it does not find.

MAGIC-SPELL is delivered on two 5½" diskettes in versions for TSC's FLEX 2, FLEX 4, Mini-FLEX and the Percom DOS. The price is \$84.95.

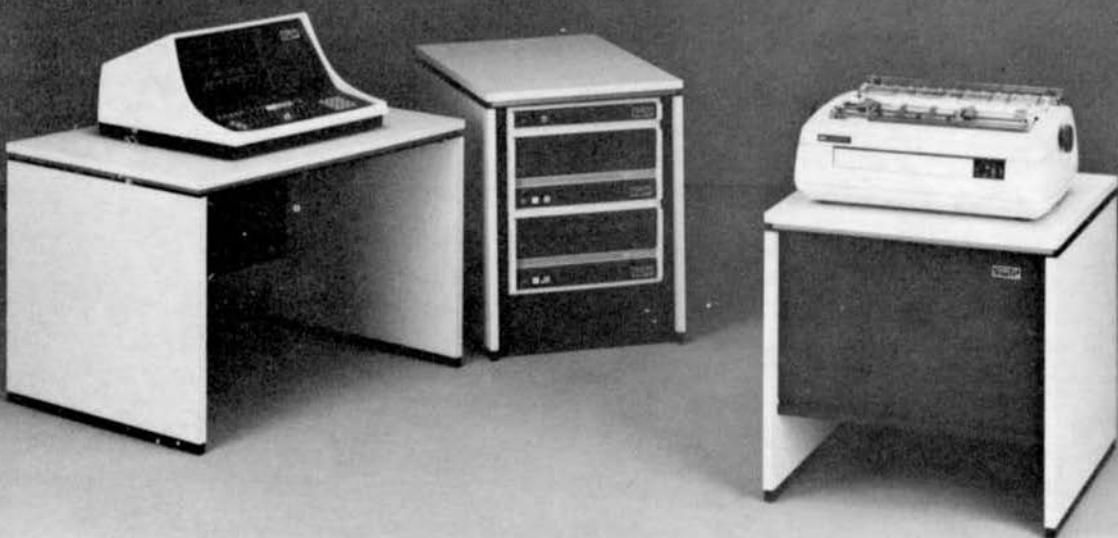
One disk contains the MAGIC-SPELL program and a commented source listing. The second disk contains MAGIC-SPELL's dictionary file.

The dictionary is a single file occupying the entire disk. It contains over 10,000 words and is easily modified by a text editor to customize it for names or technical terms. It may be added to, shortened or special editions created.

The dictionary is a text file with each word, in all caps, separated by a carriage return, arranged in alphanumeric order. When MAGIC-SPELL's option to add words to the dictionary is selected, it asks for the name or the OSRG of the new dictionary and creates it. The new file contains all the words in the old file and the additions. Dictionary size is limited only by the capacity of the disk. A dictionary can even be split between several disks. A longer dictionary requires more processing time since the main program reads the entire file.

MAGIC-SPELL never updates a file in place. It writes a new file rather than rewriting the existing file. This allows complete recovery from operator or system errors.

When MAGIC-SPELL is run, it asks for the name or the OSRG of the text file to be proofed. Words in this file may be up to 31 characters long. Hyphenated words at the end of a line are reconstructed. Words with internal punctuation are not separated. It then asks if it should consider any group of characters surrounded by spaces or carriage returns or if it should only select



THE COMPLETE BUSINESS SYSTEM

+Multiuser+Highly Expandable+Cost Effective

S+ THE CONCEPT

The S+ system is a modular computer system in which all portions of the hardware and software are designed to work together in the most efficient way possible. An S+ single user system with floppy disk storage is a competitive and cost effective entry level system. Unlike most other small computers being sold as "personal", or "small business" machines, the S+ system may be expanded to maximum capabilities using this same hardware and software. You cannot end up with a DEAD END system that cannot be expanded and whose software is not compatible with larger machines. A basic S+ system may be expanded to thirty-two users, a megabyte of main memory and hundreds of megabytes of hard disk storage by simply plugging in, or connecting the desired upgrade equipment.

TOTAL DESIGN—Hardware and Software

The S+ system is an integrated hardware and software design. The two complement and enhance each other in this system. The UniFLEX® operating

system used in the S+ systems is patterned after the Bell Laboratories UNIX® operating system, one of the most admired and widely used operating systems in the world. Instead of being an afterthought, the software is part of the design of the S+ system. You can be sure that with this approach that all parts of the computer operate with maximum efficiency and cost effectiveness.

THE CENTRAL PROCESSOR

The basic S+ system is configured with 256K bytes of memory and can be expanded to more than 1 million bytes. An efficient and fast hardware memory management system is used to allocate the available memory among the users on a dynamic basis. As little as 8K bytes, or the entire memory—if needed—can be used by any individual user. This makes it possible to run very large programs on the system, but it also uses no more memory than necessary for a particular job. The increase in cost effectiveness of this system over crude and outdated bank switching arrangements is dramatic.

The central processor runs in both user and supervisor states. It can detect and reject a defective user program. It is impossible for a user program to go bad and stop the entire system, as can happen quite easily in less sophisticated systems.

Task switching is accomplished by use of a multiple map RAM memory, with sixty-four individual task maps. Each task can access from 4 to 64 K-bytes of memory. Multiple tasks may be used in programs that require more than 64K bytes of memory for execution. When a task is completed the memory is automatically released for other use.

SOFTWARE

The S+ operating system, UniFLEX® is a multiuser, multitasking operating system based on the UNIX® operating system that has been used for many years on Digital Equipment Corp. PDP-11 series minicomputers. It is considered one of the most sophisticated and "user friendly" operating systems available. Variations of UNIX® are rapidly becoming standard on mini and larger microcomputers.

A large variety of languages are available for use with the system. These include FORTRAN, COBOL, BASIC, and Pascal. Word processing packages are also available to give you full text processing capability on the system.

Applications programs are available in large quantities in many fields. This includes general business, medical, dental, veterinary, library and real estate management; plus others. Since the system is multiuser it can also be connected to cash registers to produce a point-of-sale terminal system combined with the computer. The possibilities for application of this system are endless.

THE I/O SYSTEM

The S+ system is totally interrupt driven. All terminal and printer I/O devices connect to an I/O bus separate from the main bus. Up to thirty-two separate devices may be connected to the I/O bus at any one time. If I/O activity is great enough to cause an unacceptable slowdown in system operation, a separate I/O processor can be installed in the system. This plug-in option removes all I/O handling

overhead from the main processor and allows operation of up to thirty-two external devices at 9,600 baud. Without an integrated total design, as in the S+ system, it would become impractical to use a UNIX® type operating system in a situation with heavy terminal I/O activity.

DISK STORAGE

A wide range of disk storage capacity is available for the S+ system, from 2.5 M-byte floppy disks to an 80 M-byte Winchester and many sizes between. All disk controllers use direct memory access (DMA) type operations to maximize data transfer and to minimize overhead on the main processor. The Winchester disks also use intelligent controllers along with DMA transfers to preserve the performance that these type devices are capable of giving. Without this distributed intelligence the system performance would be greatly degraded. The UniFLEX® operating system is designed to work at maximum efficiency with this type disk system. The data transfer rates achieved by this combination rival those of large minicomputers.

COMMUNICATIONS

A high speed local network communications system is available to interconnect S+ systems. The VIA-BUS® network will allow communication between systems at data rates of over 400K baud. Such a system makes it possible to share data between local systems in an efficient and low-cost manner.

AVAILABLE SOON

Tape backup—20M-Byte in less than 15 minutes on a standard 1/4 inch cartridge.

Mini-Wini—5 and 10 M-Byte Winchesters—5 1/4 inch package. Winchester performance, for smaller systems in a small package. UniFLEX® compatible design.

Large Capacity—190 and 340 M-Byte Winchesters, plus SMD cartridge drives.

UniFLEX is a registered trademark of Technical Systems Consultants, Inc.

UNIX is a registered trademark of Bell Labs.

VIA-BUS is a registered trademark of Southwest Technical Products Corporation.



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219 W. RAPSODY
SAN ANTONIO, TEXAS 78216

(512) 344-0241

reasonable words. The first option is normally used for proofreading. The second, which does not consider words with numbers or punctuation, is used to scan a text file to add words to the dictionary.

The text file is read and each distinct word is stored, in alphanumeric order, in memory. A file much larger than would normally fit into memory can be made since each word is saved only once, despite the number of times it is used. A 32K system will process 400K of characters with little difficulty.

Once the text is created in memory, the program asks for the name of the DSEF of the dictionary. The original text file disk may now be removed. If new words are to be added to the dictionary, this option is selected. Each word in memory is then compared to each word in the dictionary. When an undefined word is encountered, the program prints the word and prompts the user to select one of several options. Control may be passed to the DOS, the word may be ignored, added to the dictionary, marked with "+++" in the text file or a fact mark feature may be selected which automatically marks any questioned words without issuing the prompts.

FLEX's "P" command, or a user supplied printer driver for the Percom system, will provide a listing of all questioned words. If a text file is being saved, all questioned words are marked and can easily be found with an editor. When MAGIC-SPELL is finished processing, control is returned to the DOS.

The latest version of MAGIC-SPELL also includes an error correcting feature. When a questioned word is found, the program asks if a new text file should be made with the words either marked or corrected. If corrections are desired, it issues a prompt for each word it does not understand. The options include ignoring the word, marking or changing it. If a change is desired, the previous three or four lines of text are listed and the user enters the correct word, of up to 32 characters long.

The only option needed in the Percom version is a switchable printer selection. There is room in the source code for this, but the user must supply his own routines.

MAGIC-SPELL's frequent prompts and clear documentation make it easy to use. The package is usable with even a single drive system. Two or more drives allow the full power of MAGIC-SPELL to easily be used.

BIT Bucket

NEWSRELEASE

FOR IMMEDIATE RELEASE

DECEMBER 12, 1981

COMPUTER MANUFACTURER INTRODUCES TAPE BACKUP OPTION FOR WINCHESTER BASED COMPUTER SYSTEM LINE

WESTLAKE VILLAGE, CA...SMOKE SIGNAL, manufacturers of the CHIEFTAIN (tm) Series of business computers, and PATHFINDER (tm) Series of Development Systems, announces tape streamer backup as an option for the company's line of 8-inch and 5½-inch Winchester-based systems. The tape streamer is now a standard option to any CHIEFTAIN and PATHFINDER Winchester system for an additional \$1500 to list prices of the computer system. The tape streamer will store up to 20 Megabytes on 6-inch cartridge tape.

With a tape streamer, the computer systems can now transfer 20 Megabytes of data in less than 5 minutes at 90 ips. The CHIEFTAIN/PATHFINDER

Series also incorporate two means of backup with the streaming tape drive: file by file, or a complete backup with one single command. Drive-to-tape and tape-to-drive data transfer is provided.

The CHIEFTAIN/PATHFINDER Series of Winchester computer systems are based around 5½-inch and 8-inch Winchesters ranging from 4 up to 60 Megabytes of storage and more. All systems are based around the state-of-the-art 6809 2MHz microprocessor, and all computers come standard with 64K of RAM. Both CHIEFTAIN and PATHFINDER have standard options of operating Smoke Signal's DOS69D operating system or the UNIX compatible multi-user, multi-tasking operating system - OS-9 LEVEL I & LEVEL II. CHIEFTAIN computers have application software, business accounting software, data base management applications and special applications available. The PATHFINDER Series provides complete 68XX processor family development tools such as a 68XX-family macroassembler, a powerful DEBUGGER, and M6805/M6809 capabilities. An in-circuit emulator is an option for PATHFINDER Development Systems. Tape streamer options for the models 98MIC, 98M30, and 9815720 are available from Smoke Signal and Smoke Signal Dealers. Dealer opportunities are available.

For further information please contact: Deborah Conrad, Manager Dealer Sales and Support
and Jim Allday National Sales Manager

at Smoke Signal Broadcasting
31336 Via Collinae
Westlake Village, CA 91362
(213) 889-9340

word's worth

P.O. Box 28954
Dallas, Texas 75228

FIG-FORTH for FLEX 9.0

FIG does not supply machine-readable source to their otherwise excellent package. Two disks - (1) Source in FLEX format, by H. J. Talbot, Jr., (2) polyFORTH-compatible editor published in *Forth Dimensions*, by S. M. Daniel ('screen' disk). Supplied without comments, except for a few minor bug fixes. Intended to supplement, not replace, the FIG-supplied documentation. Saves about 10 hours of typing. FLEX 9.0 version available now, DOS69D version to be announced. This software is published by FIG, and is in the public domain.

FORTH for FLEX 9.0 (2 disks)

\$19.50

C Compiler for 6809

Based on SMALL-C as published in *Dr. Dobbs* by Ron Cain. Transported to DOS69D by Allen Betteiger, adapted to FLEX by Bill Knight. FLEX version requires a special loader (included). Current plans are to produce a full C in three steps: Ver. 1.0 available December 1st; ver. 2.0 - 2nd Q/82; ver. 3.0 - 4th Q/82. Prices to be announced. Liberal upgrade policy. User's guide, binary for compiler, and source for run-time library. 48K system recommended.

For FLEX 9.0 (with loader)
For DOS69D

\$52.50
\$47.50

LOAD 3.0

Relocating linking loader for TSC's absolute assembler. Enhanced version of LOAD published in '81 Micro Journal by HL Markness. Source and documentation on the disk. Public domain.

LOAD for FLEX 9.0

\$17.50

These programs normally supplied on 5" disk. For 8", add \$2.00 per disk. Prices good until February '82. Shipping and handling included. Texas residents: add 8.25 sales tax per 5" disk, \$0.35 per 8" disk. Specify operating system and disk size. Visa and MasterCard prices \$2 bigger. Allow 4 weeks for check. Please do not send cash through the mail.

FIG stands for the FORTH Interest Group.
DOS69D is a trademark of Smoke Signal Broadcasting.
FLEX is a trademark of Technical Systems Consultants.

STAR-KITS

P.O. Box 209
MF 4200 NEW YORK 10108

December 6, 1981

Mr. Don Williams
'68 Micro Journal
P. O. Box 3018
Hixson TN 37343

Dear Don:

I'd be grateful if you would publish this letter in your Bit Bucket:

One of our major principles is to make sure that Star-Kits stands behind its products. If we discover a bug, or make a major improvement, we try to let our customers know as soon as possible. For example, we are right now making a major change in MAGIC SPELL to speed it up by a factor of two or so.

The problem is that we have hundreds of customers, not just in the U.S. but also in Europe, Asia, Africa, South America, and Australia (Sorry ... not in Antarctica ... yet!) It becomes a bit difficult to maintain a current mailing list, and expensive as well. So we ask our customers to help. Each Star-Kits manual has a User Registration Form at the back, and a request that each customer send us the form and a self-addressed, stamped envelope. We then use these envelopes to send out updates, modifications, or User's Group Newsletters.

Unfortunately, many customers don't bother to send in their forms and envelopes, thereby missing out on some very useful information. The purpose of this letter is to ask all our customers to help us help them. Make sure to send in your forms and envelopes. Folks!

Sincerely yours,

Pete Stark

Peter A. Stark



TEXAS COMPUTER

817-275-1848 • PO BOX 120816 • ARLINGTON, TX 76012

Mr. Don Williams
'68 Micro Journal
3900 Cassandra South
Hixson, TN 37343

Dear Mr. Williams:

TEXAS COMPUTER would like to announce the immediate availability of the DPP dual port serial board for the 8830 bus.

We at TEXAS COMPUTER believe that this board will fill a need in the line of serial boards presently available for the 8830 bus. We have tried to "design in" as many user options and conveniences as possible while maintaining an attractive price.

How many times have you borrowed or bought a terminal or printer and connected it to your system only to find that you can't seem to make it work without the RS232 handshake lines. With the TEXAS COMPUTER DPP board your problems are over! The DPP board buffers and brings all MC6850 handshake lines and two RS232-C driver lines out to a jumper area for easy connection to the DB25 connector. This method makes it easy for the user to select the lines necessary for handshaking and then connect them as needed.

The TEXAS COMPUTER DPP is dual port serial board using two MC6850 ACIA's. As always, gold molex connectors are standard and two side mount DB25 connectors are used for easy hookup to the users equipment. All voltages (+5, +12, -12) are provided by 1 amp regulators. All signal lines are buffered by Motorola RS232-C line drivers and receivers which limit current output to 10mA. All IC's are socketed. Separate transmit and receive clocks may be implemented on this board by cutting one etch and jumpering the baud rate desired between two jumper pads.

Documentation is included that demonstrates several full handshake hookups from terminals and modems to the port. The DPP board is available for \$69.00 + \$3.00 for shipping and handling. Texas residents please add 5% for sales tax.

Sincerely,

Dave C. Bolan
Dave C. Bolan

MR. WILLIAMS, EDITOR
COMPUTER PUBLISHING, INC.
68 MICRO JOURNAL
3900 CASSANDRA SMITH
HIXSON, TN 37343

DEAR DON:

BEING A NEW READER TO YOUR MAGAZINE I WOULD LIKE TO TAKE THIS OPPORTUNITY AND TELL YOU WHAT A FINE MAGAZINE YOU HAVE FOR THE 6800 USER'S. MY OWN SISTER IS A 6800 OWNER OF WHICH I AM NOW TRYING TO EXPOSE TO A DISK SISTER.

MY MAIN REASON FOR WRITING THIS LETTER IS TO INFORM YOU AND I HOPE THROUGH YOUR MAGAZINE, ALL THE 6800 USER'S OF A 6800 USER MEETING AND GET TOGETHER AT THE "CHARLOTTE MPP FEST & COMPUTER SHOW" WHICH WILL BE MARCH 20 & 21 1982. THE SHOW WILL BE LOCATED AT THE CHARLOTTE CIVIC CENTER IN DOWNTOWN CHARLOTTE, NORTH CAROLINA. A LOT OF THE OTHER USERS WILL BE HOLDING MEETING AT THIS SHOW ALSO. WE HAVE TENTHILY SELECTED THE MEETING TIME FOR THE 6800 USER GROUP FOR SUNDAY THE 21ST AT 11:00.

DON, YOU SHOULD HAVE RECEIVED A LETTER FROM THE SHOW COMMITTEE ABOUT BUYING A BOOTH FOR THIS SHOW. I DON'T KNOW WHAT THE ATTITUDE OF THE COMPUTER GROUPS ARE ABOUT GOING OT SHOWS BUT WE ARE GOING TO TRY TO TRY AND MAKE THIS A VERY SUCCESSFUL ONE FOR THE COMPUTER PRIMERS. HOPE YOU WILL BE ABLE TO HOME.

YOURS TRULY,

Wayne L. Setzer

WAYNE L. SETZER
3215 DUNHIRE DR.
CHARLOTTE, NC 28205

APPLICO

SYSTEMS & SOFTWARE
ORGANIZATION INFORMATION SYSTEM

(714) 875-5969

The APPLICO Organization Information System (ORGINFO) can be an asset to any business, club, church, school, or union. If you want to keep track of your customers or clients or members, you can do it better and cheaper with ORGINFO.

The menu-driven programs in this system are easy to use and have been written with the most current techniques. All output in ORGINFO may be "spooled" to a file for background processing, may be routed to a printer, or may be directed back to the requesting terminal.

ORGINFO will produce rosters, mailing labels, phone directories, and many more functions that are tedious work around the office. ORGINFO maintains names, addresses, and phone numbers, and allows you to define your own "categories" of data for your members. What is a category? It is a piece of information that has some value to you, such as office holder, skills, wife's name, important dates (birthday, hire date, etc.), or type of customer. Anything, it's user-defined.

Some of the unique functions of ORGINFO include:

1. Produce an organization roster. This is a formatted listing of every name and all category data in the file.

2. Produce mailing labels. Print standard size (3 1/2" x 15 1/8", top 5615) labels 1-up, 2-up, 3-up, or 4-up in any of three different sequences: zip-code sequence, name (alpha) sequence, or non-profit organization sequence. The non-profit sequence is required by the U.S. Post Office to obtain reduced non-profit organization mailing rates. You may print labels for all names within an organization or select one or more "categories" of names. The sort/merge program used is a very efficient modified "shell" sort algorithm which dynamically allocates needed memory. If you wish, four trailing labels will be produced to be used for return mail.

3. Produce the phone directory. This report is a compact listing which includes the name, address, and phone number of your members as well as any category data you select.

4. Produce a category report. This program selects all names in a requested category (such as preferred customers, or officers), sorts the selected file in name sequence, and produces a formatted report.

5. Build "micro-script" interface files. This program selects all names in a requested category or all names in the file and produces a file that may be used to print form letters.

Start saving time on your membership lists now.

APPLICO SYSTEMS & SOFTWARE
5601 Palomar Avenue
San Bernardino, CA 92404

November 20, 1981

Mr. Don Williams
'68 Micro Journal
3018 Hamill Rd.
Hixson, TN 37343

Dear Don,

As many Flex users have noted, printer spooling does not seem to function when a 6809 processor card is used in an 'old' 6800 box. Confusion concerning this problem is compounded by the fact that users of 8 inch systems and users of newer chassis claim the problem exists (frustrating to say the least!).

The following patches will fix the printer spooler bug in FLEX Ver 2.8.1 as distributed by SHFPC for 5" disk systems. The first patch will move the MP-T timer back to port 84. This is a bug when running 5" FLEX in the old (6800) box. Using the FIL command, make the following changes to PGP.CMD:

AC381 - 00 old data

00 04 new data

The next patch disables the disk drivers from deselecting the drives while spinning. Fix FLEX.SVBs
6CDBB - B6 80 B7 E0 14 old data
12 12 12 12 12 new data

LINK FLEX.SVB

This fix will alleviate the problem and allow proper spinning activity, but is not a best fix. If any readers are interested in a reassembled version of Flex.sys which we feel is a better fix, please write or call and we will be happy to provide our solution. We would like to thank Norm Asztalos at SMTPC for his gracious help in providing the information necessary for a solution.

We may be reached by phone at (214) 422-3119 / 422-3377 (8 am - 4 pm cst)

Sincerely,

Jim Cordill
John Ross



MARK DATA PRODUCTS

NEWS RELEASE

Mark Data Products releases CAVE HUNTER, a new action game for the 16K TRS-80 Color Computer. Maneuver your way to the bottom of a spooky old cave to retrieve the treasures. It's not so easy! Passages lead in all directions and angry cave creatures pursue you relentlessly.

CAVE HUNTER is a fast-paced arcade game using hi-res graphics, sensational colors and a variety of unique sounds. Single or multiple players. Joysticks are required.

Priced at \$24.95 this game is available postpaid from Mark Data Products, 23802 Barquilla, Mission Viejo, California, 92691. (714) 768-1551.

23802 BARQUILLA • MISSION VIEJO, CA 92691 • (714) 768-1551



COMPUTERS • PERIPHERALS • SYSTEMS

2888 Buff Street Suite 105 • Box 1559 • Boulder, Colorado 80306 • (303) 499-4236

NEW S550 PRODUCT

META LAB, established in 1974 as a research, development, and engineering firm, announces the first in a series of products for the S550 Bus Computer --- the ADC 1200.

The ADC 1200 is a 12 BIT ANALOG TO DIGITAL CONVERTER BOARD designed for industrial, scientific, educational, and laboratory applications that require high speed and 12 bit accuracy and resolution.

The ADC 1200 combines a data acquisition module with a 6522 versatile interface adapter (VIA) in a powerful and compact form. This allows a full 12 bit, 16 channel data acquisition system on a single 30 pin board. The ADC 1200 features a high speed sample and hold amplifier, instrumentation amplifier, precision multiplexer, unipolar or bipolar operation, external clock input/output, external trigger, and a one year warranty.

Sampling rates up to 80,000 samples/second (12.5 uSec) are possible using the computer configurable single channel burst mode. Random sampling can be performed at rates in excess of 60 KHz using an overlapped mode. An aperture delay of less than 0.1 uSec can be achieved using a direct external trigger mode. The sampling can also be clocked externally or the external event can be triggered by the ADC 1200 using a handshake mode.

Extensive Pascal and Assembly Language Programs are provided which interface directly with the ADC 1200. These assembly modules and user interface programs provide tested examples of several of the hardware modes. The source of the assembly modules is included and can be used with little or no modifications to implement the user's specific requirements.

The ADC 1200 is one of a series of high quality S550 based products produced by META LAB. Please write for detailed specifications on the ADC 1200 and other S550 compatible products.

68MICRO JOURNAL
5900 CASSANDRA SMITH
COMPUTER PURISHING CENTER
PO BOX 849
NICON, TN 37343

30 NOVEMBER 1981

DEAR STRS:

IN THE DECEMBER ISSUE OF THE 68MICRO JOURNAL WAS AN ARTICLE WRITTEN BY D.F.W. MILLIAN OF BURNABY, B.C., CANADA. ENTITLED "DIRECTORY", THIS PROGRAM PROVIDED THE AMIGA WITH A MUCH NEEDED PROGRAM FOR MAKING A MASTER CATALOGUE OF DISKS WRITTEN WITH FLEX. THERE WAS ONE PROBLEM WITH THE PROGRAM. IT WAS WRITTEN IN "MINIFLEX" WHICH SEEMS TO BE A FORGOTTEN SYSTEM, OR AT LEAST A SYSTEM THAT HAS BEEN SUPERSEDED BY BETTER SYSTEMS. IT WITH THIS THOUGHT IN MIND THAT THE FOLLOWING REVISIONS ARE SUBMITTED AS A SUGGESTION AS TO HOW TO UP-DATE THE PROGRAM TO RUN IN THE NEWER FLEX 2.0.

THE CHANGES ARE FEW AND ARE EASILY MADE. THE WORD AS TO WHY THE CHANGES ARE NECESSARY. THE MINIFLEX USES 128 BYTES PER SECTOR FOR STORAGE ON THE DISK AND FLEX 2.0 USES 256. SO IF THIS IS NOT TAKEN INTO ACCOUNT, THE PROGRAM WILL OVER-WRITE ITS SELF IF YOU TRY TO RUN THE ORIGINAL PROGRAM IN FLEX 2.0. SO THE CHANGES TO BE MADE ARE AS FOLLOWS AND INCLUDE THE CHANGES TO RUN WITH 6800 FLEX 2.0 AND 6809 FLEX 2.0.

THE FIRST CHANGE IS TO THE "EQUATE" FUNCTIONS:

FUNCTION	EQU	MINI	6800	6809
WARM	EQU	\$7103	\$AD03	\$CD03
DETCHR	EQU	\$710F	\$AD15	\$CD15
INBUFF	EQU	\$7115	\$AD1B	\$CD1B
PSTANO	EQU	\$7118	\$AD1E	\$CD1E
POB_F	EQU	\$711E	\$AD24	\$CD24
NITCH	EQU	\$7121	\$AD27	\$CD27
DETFL	EQU	\$7127	\$AD2D	\$CD2D
SETEXT	EQU	\$712D	\$AD33	\$CD33
PTERR	EQU	\$713C	\$AD3F	\$CD3F
FRBCLS	EQU	\$7803	\$B403	\$B403
FMS	EQU	\$780A	\$B806	\$B406

DELETE THE CODE FROM LINE 166, ADDRESS 0145 THRU LINE 170, ADDRESS 014E AND SUBSTITUTE THE FOLLOWING CODE:

166	LDX	#RDFCB+3	STOR	READ FCB
167	STX	#XTEMP1		
168	LDX	#WRITFCB+3	STORE	WRITE FCB
169	STX	#XTEMP2		
170	LDAB	#12	COUNT 12	
171	LOOPD	LDX XTEMP1	GET FCB ADDRESS	
172	LDAA	0,X	GET CHARACTER FROM RDFCB	
173	INX		MOVE POINTER TO NEXT CHAR IN RDFCB	
174	STX	XTEMP1	STORE RDCFB	
175	LDX	#XTEMP2	GET WRITE FCB ADDRESS	
176	STAR	0,Y	MOVE CHARACTER TO WRITE FCB	
177	INX		MOVE POINTER TO NEXT CHAR IN WATFCB	
178	STX	XTEMP2	STORE WATFCB	
179	DEC B		DECREMENT COUNTER	

CHANGE LINE 708 TO READ # FILE CONTROL BLOCK IS 320 BYTES LONG.

CHANGE LINE 741 TO READ: BUFFER RMB 256 BYTE 64-319=SECTOR BUFFER FOR DATA
CHANGE LINE 754 TO READ: BALNC2 RMB 298

CHANGE LINE 760 TO READ: DIRFCB 320 FILE CONTROL BLOCK FOR DIRECTORY

ADD BELOW LINE 780 'SAVER'
XTEMP1 RMB 2
XTEMP2 RMB 2

AND THAT IS ALL THERE IS TO IT. THESE CHANGES ADD A FEW LINES TO THE PROGRAM LENGTH, SO THE LINE NUMBERS GIVEN ABOVE ARE BEFORE THE ADDITIONS WERE MADE AND FOLLOWS THE NUMBERING OF THE ORIGINAL PROGRAM AS PUBLISHED IN THE DECEMBER ISSUE OF 68MICRO JOURNAL.

SINCERELY,

Dave & Hugo

ODIN VANBEEK
16381 S STOLTZ RD
OREDON CITY, OREGON 97045
HUGO C WILDSOHN
11304 SE STANLEY AVE
MILWAUKEE, OREGON 97227

TSC 6809 BASIC - CFM/3 Ver. 9

HERE IS A WAY TO SAVE YOUR TSC 6809 CASSETTE BASIC PROGRAMS USING CPC'S CPC/3. THESE PATCHES FOR SAVE, PGMNAME AND LOAD HAVE BEEN IN USE FOR A FEW MONTHS. THE CPC (680960) MAY BE USED FROM BASIC IN THIS FORM: X.CFM/3D . CPC'S WPS IS ERROR AT E800 ON THE SWTP JP-89 BOARD! SWITCH JP-89 AND USE THE SECURE ROM SOCKET IN FROM THE RIGHT EDGE OF THE BOARD.

BEWARE THAT THIS IS THE FIRST VERSION OF THESE PATCHES. PERHAPS CPC PRODUCTS: CBM/C64 WILL OFFER SOME MINOR REFINEMENTS IN THE FUTURE. MEANWHILE ENJOY THE 6809 WITH CPC/3!

RAYMOND E. PATTERSON, FOX, AR 72051

D9000 BE00 NRM 0FM3-9 PATCHES FOR TSC 6009 BASIC
 D9012 D000 EBU 0FM3-9
 D9014 0000 LDX 0FM3-9
 D9016 0000 LDV 0FM3-9
 E8000 0000 EDI 0FM3-9
 E8004 RENDVP EGU 0FM3-9
 * SAVE PG799999 (FROM TSC 6009 BASIC)
 0737
 0752 RL 18 CD
 0755 10RE CB 91
 0758 10P 0000
 0758 10 01
 0759 10 21
 0741 10RE CB 9C
 0742 10 11
 0743 10RE CB C9
 0744 10 11
 0745 10RE CB 00
 0751 10 01
 0755 10 0-02
 0758 10 CB 78
 0758 17 E004
 0759 16 F963
 0761 10E 01
 0764 39
 MOVE RTS
 * X.CFMCB (FROM TSC 6009 BASIC)
 0274 ORG #274
 0274 079F FDR #079F
 0528 ORG #328
 0526 9600 FDB #9600
 0524 00 FDB #0000
 0525 00 FCB #000
 0747 ORG #747F
 0799 30 CB 76 LEAX #76_U
 0742 17 E063 LDAR VMOVHD
 071C
 071C 2A! C17 78 LEAP #78_U
 072F 17 6003 LBSR VEXLOR
 0702 01 0042 LDX #0042
 0783 10AE 82 LOV # -X
 071C 10N4 11 CD STV # -933_U
 07DC 00 13 BSR READUE
 071E 10RE CB C9 STV # -37_U
 07E2 00 0D BSA REMOUE
 07E4 10AE 18 9C STV # -962_U
 07E8 HD 07 BSR FERDUE
 07E8 10H 18 91 STV # -96_U
 07E9 16 0009 LBSR 0FM3-9
 07F1 10H 03 LEAVE LDV # -X
 RTS
 * CFM3-9 LOAD TSC 6009 BASIC USER PROGRAM
 ORG #7CC
 071C
 071C 2A! C17 78 LEAP #78_U
 072F 17 6003 LBSR VEXLOR
 0702 01 0042 LDX #0042
 0783 10AE 82 LOV # -X
 071C 10N4 11 CD STV # -933_U
 07DC 00 13 BSR READUE
 071E 10RE CB C9 STV # -37_U
 07E2 00 0D BSA REMOUE
 07E4 10AE 18 9C STV # -962_U
 07E8 HD 07 BSR FERDUE
 07E8 10H 18 91 STV # -96_U
 07E9 16 0009 LBSR 0FM3-9
 07F1 10H 03 LEAVE LDV # -X
 RTS

Specialty Electronics, Inc.



INVENTORY

The Specialty Electronics Interactive Accounting System Inventory Control Package provides the tools for complete control of a large and active inventory, providing:

1. Reports for quantities on hand, quantities on order, activity and many other categories.
 2. Complete item description, category groups, supplier information, order dates, reorder quantities, etc.
 3. Simple input and reconciliation procedures

Requires OS9®, Runb®, direct addressed cursor terminal, 500K or larger disk system, 132 column printer.

Inventory Control I-code \$299.00
Geodatas Documentation \$ 19.95

Specialty Electronics Interactive Accounting System is based on the Osborne Publication.

¹Osborne is a trademark of Osborne McGraw Hill.
²OS9 and Rumb are trademarks of Microware, Inc. and Motorola Corp.

Osborne is a trademark of Osborne McGraw Hill.
OSG and Rumb are trademarks of Microware, Inc. and Motorola Corp.

(105) 233-1632 • P.O. BOX 541 • ENID, OKLAHOMA 73702

Dear Don,

Here is a listing of a proportional spacing print driver for the 40CPS Serial STARWRITER printer. Although at present I know of no text processor that will support proportional spacing on this printer, this is a start. I hope to develop one myself. If any big software house out there would like to cooperate in developing a proportional spacing processor for this printer and others to run under Uniflex I'd like to hear from them.

I might say that I am very pleased with this printer I got from you. This paragraph was written proportionally spaced using this driver. The width table may still need some adjustment. I am using a "Qume" print wheel here (Theme PS 10). The table can be changed for any printwheel.

Does anybody know where I can get some Spanish proportional print wheels?

Steve Searfoss
Apdo. 3-620
Col. Guerrero
Delg. Cuauhtemoc
03600 Mexico, D.F.
MEXICO

PROPORTIONAL SPACING PRINTER DRIVER FOR THE STARWRITER DAISYHEEL PRINTER

• • • • •

二〇〇二年四月

- YOU MUST HAVE SWITCH #2 OF SW1 ON THE I/O PCB SET IN THE OPEN (UP) POSITION FOR THE PRINTER TO WORK IN THE SERIAL MODE AS THIS MODE ALLOWS PROPORTIONAL SPACING.

DATA ARE STREAMED TO SOCIAL MEDIA DRIVERS

SEE US AND OUR NEW LINE

ORG SC300 MUST START AT SC300
PDR ENDS-OPEN LENGTH OF DELIVER

અધ્યાત્મિક

OPSH	LBR A	OPEN	PRINTER INITIALIZE
QUIT	LBR A	CLOSE	PRINTER TERMINATE
CHARA	LBR A	PROCSA	PRINT CHARACTER
CHARB	LBR A	CHECK	PRINTER READ DATA

SERIAL PRINTER DRIVER FILE CONTROL BLOCK

CIA SIDE	PCB PCB	\$E020 0 0	DEFAUT PORT ADDRESS #2 MEMS INTERFACE SIDE VALUE — RESERVED BYT —
-------------	------------	------------------	---

SPECIFY A SIDE IF A SIDE IS SPECIFIED

PEN LOA SIDE,PCR
BPL ARESET 1P POSITIVE NO SIDE SPECIFIED
ASLA ASLA
ASLA
MULTIPLY BY FOUR

ADDA A LAS
STA ACIAS

```

        RESET ACIA DEVICE

RESET LDX ACIA,PCR GET ACIA ADDRESS
      LDA #00000011
      STA 0,X DO MASTER RESET ON ACIA
      LDA #00010001
      STA 0,X SET UP NO PARITY BIT,
      TST 1,X A DATA BITS 2 SLOW BITS

```

53 • SEND OUT CODES TO SET CARRIER
 54 • SPACING PITCH AMOUNT 10/120 INCHES
 55 • CODE IS (DEC E10)
 56
 57 C32C 86 1B LDA #\$1B ESC
 58 RER PUT
 59 C32D 86 45 LDA #\$45 " "
 60 RER PUT
 61 C32E 86 31 LDA #\$31 " "
 62 RER PUT
 63 C32F 86 30 LDA #\$30 " "
 64 C32A 80 5F RER PUT
 65 RTI
 66
 67 • CLOSE DOWN PRINTER PROCESSING
 68
 69 C320 86 00 CLOSE LDA #\$00 SEND OUT CARriage RETURN
 70
 71 • PRINT CHARACTER (FIRST PROCESS LENGTH)
 72
 73 C337 34 10 PROC1 PNS X SAVE X AS IT IS USED LATER OB
 74 C341 81 04 CRPA #\$0A IP IT IS A LINE FEED
 75 C343 27 64 RER PUTEX DO AND EXIT
 76 C345 81 00 CRPA #\$0D IP IT IS A CARRIAGE RETURN
 77 C347 27 64 RER CR DO A SPECIAL ROUTINE
 78 C349 81 20 CRPA #\$20 IP IT IS A SPACE
 79 C34B 27 5C RER PUTEX DO AND EXIT
 80 C34D 81 1F CRPA #\$1F SEE IF IT IS A CONTROL CODE
 81 C34F 23 58 RER PUTEX IF SO DO IT AND EXIT
 82 C351 A7 8D 00P1 STA ATMP,PCR STORE THE CHARACTER ITSELF
 83 C355 90 8D 0069 LEAK CHRTAB,PCR CHARACTER POSITION IN TABLE
 84 C359 A6 66 LDA A,X WIDTH VALUE OF THE CHARACTER
 85 C358 A7 8D 0085 STA PREVAL,PCR STORE IT IN PRESENT VALUE
 86 C359 69 8D 0080 TOT CRPL0,PCR SEE IF THERE WAS A CR BEFORE
 87 C361 27 06 RER ITSCRL IF NOT GO ON TO RECLEAR
 88 C365 67 8D 00A4 CLR CRPL0,PCR IF SO CLEAR ONLY AFFECTS ONCE
 89 C369 20 04 BRA OUTVAL,PCR OUTPUT NOT ADDING PREVIOUS
 90 C368 AB 8D 0006 L7SCLA ADDA LSTVAL,PCR ADD WIDTH OF PRESENT TO LAST
 91 C369 8A 40 OUTCHL ORA #\$40 OR IN \$40 AS PRINTER REQUIRES
 92 C371 A7 8D 00CD STA MOVAL,PCR THIS IS OUR NEW MOVE VALUE
 93
 94 • HOPE WE SEND OUT THE CODES FOR 0112
 95 • THE CODE IS 'ENC H (n1) (n2) (n3)
 96 • NOTE n1,n2,n3 ARE GIVEN IN SPECIAL CODE NOT ASCII
 97 C375 86 1B LDA #\$1B ESC
 98 RER PUT
 99 C379 86 48 LDA #\$48 " "
 100 C37B 80 1B RER PUT
 101 C37F 80 1A LDA #\$40 " " 10:23 DRAFT FORWARD MOV.
 102 C381 86 40 RER PUT
 103 C383 80 16 LDA MOVAL,PCR ACTUAL AVERAGE TO POTS (2=16)
 104 C385 A6 8D 00B9 STA POTS
 105 C389 80 10 RER PUT
 106 C38B A6 8D 00B7 LDA ATMP,PCR AFTER MOVING STRIKE CHARACTER
 107 C38F 80 0A RER PUT
 108
 109 • IF YOU JUMP TO PUT TWICE YOU CAN STRIKE THE
 110 • CHARACTER TWICE FOR CARDON PRINTLINE!
 111 C391 A6 8D 00AP LDA PREVAL,PCR MAKE PRESENT VALUE
 112 C392 A7 8D 00AC STA LSTVAL,PCR LAST VALUE
 113
 114 • WRITE A CHARACTER TO PRINTER (CODES ALSO)
 115
 116 C398 8D 1A PUT ICR CHECK WAIT FOR PRINTER HEAD?
 117 C399 2A PC BPL PUT
 118 C39F 34 10 PDS X
 119 C3A1 AB 8D 1F69 LDX ACIA,PCR GET ACIA ADDRESS
 120 C3A5 A7 01 STA 1,X STORE INTO DATA REG:RPN1
 121 C3A7 35 90 PPS PULS X,PC
 122
 123 C3A9 80 PU PPSI RRA PUT THIS CHARACTER AS IS
 124 C3A8 20 PA *
 125
 126 C3AD 80 EC RRA PUT PUT OUT THE CARRIAGE RETURN
 127 C3AF 86 FF LDA #\$FF
 128 C3B1 A7 8D 1FH CRPL0,PCR SET CR FLAG AT NON-ZERO
 129 C3B5 20 PT RTS
 130
 131 • CHECK TO SEE IF PRINTER IS READY
 132
 133 C3B7 34 04 CHECK PNS B
 134 C3B9 86 90 PPSI LAB EC1A,PPSI GET ACIA STATUS
 135 C3B0 56 RDRB
 136 C3B8 56 RDRB SHIFT READY BIT
 137 C3B9 56 RDRB UTO SIGN FLAG
 138 C300 35 84 PULS B,PC
 139
 140 • TABLE OF WIDTH VALUES TO USE
 141 • ACTUALLY HALF THE VALUE TO BE
 142 • MOVED AS IT MOVES THIS AMOUNT
 143 • PRINTS THE CHARACTER AND THEN
 144 • MOVES THIS AMOUNT AGAIN.
 145 • EXPRESSED IN UNITS OF 1/120 INCH
 146
 147 • TABLE CODES FROM 300 TO 377
 148
 149 C3C2 00 00 00 00 CHTAB PCB 0,0,0
 150 C3C6 00 00 00 00 PCB 0,0,0
 151 C3CA 00 00 00 00 PCB 0,0,0
 152 C3CB 00 00 00 00 PCB 0,0,0
 153 C3C2 00 00 00 00 PCB 0,0,0
 154 C3C6 00 00 00 00 PCB 0,0,0
 155 C3CA 00 00 00 00 PCB 0,0,0
 156 C3CB 00 00 00 00 PCB 0,0,0
 157 C3C2 05 05 05 PCB 5,5,5,5 8P TO #
 158 C3C6 05 05 05 PCB 5,6,6,4 \$ TO .
 159 C3CA 05 05 05 PCB 5,5,5,5 (TO .
 160 C3CB 05 05 05 PCB 5,5,5,5 , TO /
 161 C3C2 05 05 05 PCB 5,5,5,5 0 TO 3
 162 C3C6 05 05 05 PCB 5,5,5,5 4 TO 7
 163 C3CA 05 05 05 PCB 5,5,5,5 TO :
 164 C3CB 05 05 05 PCB 5,6,6,5 TO ;
 165 C402 07 07 06 06 PCB 7,7,6,6 4 TO C
 166 C406 06 05 05 06 PCB 6,5,5,6 5 TO 0
 167 C40A 06 04 05 06 PCB 6,4,5,6 H TO K
 168 C40B 06 07 06 06 PCB 6,7,6,6 L TO O
 169 C412 05 06 06 06 PCB 5,6,6,6 P TO S
 170 C416 06 06 06 07 PCB 6,6,6,7 T TO W
 171 C41A 06 06 06 05 PCB 6,6,6,5 X TO
 172 C41E 06 05 06 05 PCB 6,5,6,5 ??? VARIES WITH PRINT WHEEL
 173 C422 05 05 05 09 PCB 5,5,5,5 ' TO c
 174 C426 04 04 03 04 PCB 4,4,5,4 d TO g
 175 C42A 05 03 04 05 PCB 3,3,4,5 h TO k
 176 C42B 03 06 05 05 PCB 3,6,5,5 l TO o
 177 C432 05 04 04 04 PCB 5,5,4,4 p TO z
 178 C436 04 05 05 07 PCB 4,5,5,7 t TO v
 179 C43A 05 05 05 05 PCB 5,5,5,5 x TO ?
 180 C43E 05 05 06 00 PCB 5,5,6,0 ?? VARIES WITH PRINT WHEEL
 181 C442 00 MOVAL PCB 0 MOVE VALUE
 182 C443 PF CRPL0 PCB SPP CARTRIDGE RETURN FLAG
 183 C444 00 PREVAL PCB 0 PRESENT VALUE
 184 C445 00 LSTVAL PCB 0 LAST VALUES
 185 C446 ATMP PCB 1 TEMPORARY CHARACTER STORAGE
 186 C447 END PCB * END OF DRIVER
 187

0 ERROR(S) DETECTED
 SYMBOL TABLE:
 ACIA C308 ARSHUB C31F ATBPC C446 CHCK C387 CHTAB C3C2
 CLOSE C33D CR C3AD CRPL0 C443 END8 C447 ITSCIR C368
 LSTVAL C344 MOVAL C442 OPEN C312 OUTCRA C369 PCBRAR C308
 PRCRSC C308 POPEN C302 PQU1 C305 PREVAL C444 PRCHCR C33P
 PUT C39B PUTSX C3A9 RTI C3A7 SIDE C310

December 3, 1981

68 MICRD Journal
 5900 Cassandra Smith
 Computer Publishing Center
 P.O. Box 849
 Hixson, Tennessee 37343

Gentlemen:

I know that the SWTPC users are deep into some heavy computing so that they are not normally attracted to such foolishness as computer games. But I have found that it is always handy to have something to show the visiting kids when they want to know what a computer is all about.

Here is a cute little program to have man and monster battle it out in an eternal (read infinite loop) struggle. It also invites the user to create his own designs by changing the DATA statements starting with line 14900. The program may be terminated by a control-C.

Yours Truly,

Clifford Glennon
 3395 Nostrand Ave. Apt 2G
 Brooklyn, New York 11229
 Phone: 212-934-2439

```

100 REM Man VS Monster
200 REM By Clifford Glennon, Brooklyn New York, December 1981
300 REM A graphics game written in FSC's Extended Basic®
400 REM for the SWTPC CT-82® terminal.
500 REM
600 REM TSC is the trademark of Technical Systems Consultants, Inc.
700 REM P.O. Box 2570, West Lafayette, Ind. 47906
800 REM SWTPC and CT-82 are trademarks of Southwest Technical Products Corp.
900 REM 219 W. Rhapsody, San Antonio, Texas 78216
1000 REM
1100 REM WHEN A CONTROL-C IS RECEIVED EXIT THE PROGRAM VIA LINE 10500
1200 REM 1300 ON ERROR GOTO 18500
1300 REM SCALE 0: DISABLE SCALE, A FEATURE WHICH ELIMINATES ROUNDING ERRORS
1400 REM 1500 DIM MAS(10,10),MOS(10,10),BND(10,10):REM MAN, MONSTER, BOOM
1500 REM 1600 REM THE "S" INDICATES AN INTEGER VARIABLE
1700 REM
1800 REM SEED THE RANDOM FUNCTION
1900 REM 1950 INPUT "What is your lucky number today, Sir?":S
2000 REM LET S=RND(1-S)
2100 REM
2200 REM 2250 DEFINE RANDOM FUNCTIONS
2300 DEF FMA(A)= INT((17-A)*D)+1:REM 183-10 PIXELS PER LINE
2400 DEF FMIB(B)= INT((56-B)*D)+1:REM 66-10 ROWS OF PIXELS
2500 DEF FMC(C)= INT((100-C)*D)+1:REM BATTLE OUTCOME
2600 REM
2700 REM CONFIGURE TERMINAL FOR GRAPHICS MODE
2800 PRINT CHR$(129);CHR$(221);
2900 REM
3000 REM READ MAN FIGURE INTO MEMORY
3100 FOR JS= 1 TO 10
3200 FOR JS= 1 TO 10
3300 READ MAS(JS,JS)
3400 NEXT JS
3500 NEXT JS
3600 REM
3700 REM READ MONSTER FIGURE INTO MEMORY
3800 FOR JS= 1 TO 10
3900 FOR JS= 1 TO 10
4000 READ MOS(JS,JS)
4100 NEXT JS
4200 NEXT JS
4300 REM

```

```

4400 REM READ BOOM INTO MEMORY
4500 FOR I=1 TO 10
4600   FOR JS=1 TO 10
4700     READ BOOM(J$,I$)
4800   NEXT JS
4900 NEXT I$
5000 REM
5100 REM TURN OFF BLINKING CURSOR
5200 PRINT CHR$(1);
5300 REM
5400 REM SET SCORES MAN-SOR. MACHINE-SIR
5500 LET SO=0;SI=0
5600 REM
5700 REM ***** START LOOP *****
5800 REM
5900 REM HOME UP CURSOR AND CLEAR SCREEN
6000 PRINT CHR$(161);CHR$(22);
6100 REM
6200 REM PRINT SCORE
6300 PRINT CHR$(201);CHR$(11);CHR$(0);CHR$(30);REM POINT TO TOP OF SCREEN
6400 PRINT "MAN >;SO;" MONSTER >;SI;
6500 REM
6600 REM GET MAN'S POSITION
6700 LET A=FNA(A); B=TNB(B)
6800 REM
6900 REM GET MONSTER'S POSITION
7000 LET C=FNA(A); D=FNB(B)
7100 REM
7200 REM PRINT MAN AND MONSTER IN RANDOM ORDER
7300 IF FNC(C) < SO THEN GOSUB 9400 ELSE GOSUB 9700
7400 REM
7500 REM ZZZAP
7600 FOR I=1 TO 5
7700   GOSUB 13000;REM FIRE
7800   GOSUB 14200;REM RELOAD
7900 NEXT I$
8000 REM
8100 REM DETERMINE WHO WINS
820 IF FNC(C) < SO THEN GOTO 8900
830 REM
8400 REM MAN WINS
8500 LET SO=SO+1
8600 LET E=C;F=D;REM MONSTER CO-ORDINATES
8700 GOSUB 12000;REM GO BOOM
8800 GOTO 5700
8900 REM MONSTER WINS
9000 LET SI=SI+1
9100 LET E=A;F=B;REM MAN'S CO-ORDINATES
9200 GOSUB 12000;REM GO BYE-BYE
9300 GOTO 5700
9400 REM PRINT MAN FIRST
9500 GOSUB 10000;REM PRINT MAN
9600 GOTO 11000;REM PRINT MONSTER AND RETURN
9700 REM PRINT MONSTER FIRST
9800 GOSUB 11000;REM PRINT MONSTER FIRST
9900 GOTO 10000;REM PRINT MAN AND RETURN
10000 REM PRINT MAN
10100 REM
10200 LET E=A;F=B
10300 FOR I=1 TO 10
1040   FOR JS=1 TO 10
1050     IF MAB(J$,I$)=1 THEN GOSUB 13000;REM PRINT DOT
1060   NEXT JS
10700 NEXT I$
10800 RETURN
10900 REM
11000 REM PRINT MONSTER
11100 REM
11200 LET E=C;F=D
11300 FOR I=1 TO 10
1140   FOR JS=1 TO 10
1150     IF MAB(J$,I$)=1 THEN GOSUB 13000;REM PRINT DOT
1160   NEXT JS
11700 NEXT I$
11800 RETURN
11900 REM
12000 REM PRINT BOOM
12100 REM
12200 FOR I=1 TO 10
1230   FOR JS=1 TO 10
1240     IF BOT(I$,J$)=1 THEN GOSUB 13000 ELSE GOSUB 13400
1250     REM PRINT DOT ELSE ERASE DOT
1260   NEXT JS
12700 NEXT I$
12800 RETURN
12900 REM
13000 REM SET DOT
13100 REM
13200 PRINT CHR$(29);CHR$(19);CHR$(7+JS);CHR$(F1+I$);RETURN
13300 REM
13400 REM CLEAR DOT
13500 REM
13600 PRINT CHR$(29);CHR$(20);CHR$(E+JS);CHR$(F2+I$);RETURN
13700 REM
13800 REM DRAW LINE
13900 REM
14000 PRINT
CHR$(29);CHR$(3);CHR$(A1+5);CHR$(B1+5);CHR$(C1+5);CHR$(D1+5);RETURN
14100 REM
14200 REM ERASE LINE
14300 REM
14400 PRINT
CHR$(29);CHR$(4);CHR$(A1+5);CHR$(B1+5);CHR$(C1+5);CHR$(D1+5);RETURN
14500 REM
14600 REM
14700 REM MAN
14800 REM
14900 DATA 0,0,0,0,1,1,0,0,0
15000 DATA 0,0,0,0,0,1,1,0,0,0
15100 DATA 0,0,0,0,1,1,0,0,0,0
15200 DATA 0,0,0,0,0,1,0,0,0,0
15300 DATA 0,1,1,1,1,1,1,1,1,0
15400 DATA 0,1,0,1,1,1,0,0,1,0

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15500 DATA 0,1,1,0,1,1,0,0,1,0
15600 DATA 0,0,0,1,0,1,0,0,1,0
15700 DATA 0,0,0,1,0,1,0,0,0,0
15800 DATA 0,0,1,1,0,1,0,0,0,0
15900 REM
16000 REM MONSTER
16100 REM
16200 DATA 0,1,0,0,0,0,0,0,1,0
16300 DATA 1,0,0,0,0,0,0,0,0,1
16400 DATA 0,1,0,0,0,0,0,0,1,0
16500 DATA 0,1,1,1,1,1,1,1,1,0
16600 DATA 1,1,0,1,1,1,1,0,1,1
16700 DATA 1,1,0,0,1,1,0,0,1,1
16800 DATA 0,1,1,1,1,1,1,1,1,0
16900 DATA 0,0,0,1,0,0,1,0,0,0
17000 DATA 0,0,0,1,0,0,1,0,0,0
17100 DATA 1,1,1,1,0,0,1,1,1,1
17200 REM
17300 REM BOOM
17400 REM
17500 DATA 1,0,0,0,0,0,0,0,0,1
17600 DATA 0,1,0,0,1,1,0,0,1,0
17700 DATA 0,0,1,0,0,0,0,1,0,0
17800 DATA 1,0,0,1,1,1,1,0,0,1
17900 DATA 0,0,0,1,0,0,1,0,0,0
18000 DATA 0,0,0,1,0,0,1,0,0,0
18100 DATA 1,0,0,1,1,1,1,0,0,1
18200 DATA 0,0,1,0,0,0,0,1,0,0
18300 DATA 0,1,0,0,1,1,0,0,1,0
18400 DATA 1,0,0,0,0,0,0,0,0,1
18500 REM CONFIGURE TERMINAL FOR NORMAL OPERATION
18600 PRINT CHR$(28);CHR$(17);
18700 REM TURN CURSOR ON
18800 PRINT CHR$(21);
18900 PRINT "ERROR--";ERR;" ON LINE--";ERL
19000 END

```

68 MICRO JOURNAL

DEC. 15, 1981.

HELP

WE WORK AS INSTRUCTORS AT THE TRANSPORT CANADA TRAINING INSTITUTE IN CORNWALL, ONTARIO, CANADA.

NINE OF US HAVE PURCHASED THE RADIO SHACK "COLOR COMPUTER" AND HAVE A PROBLEM THAT PERHAPS YOU OR YOUR READERS COULD HELP US WITH.

WE WISH TO PURCHASE A "BASIC COMPILER" FOR THE COLOR COMPUTER.

IS ONE DEVELOPED YET?
WHERE CAN WE GET ONE?

PROGRAMMING IN BASIC IS SIMPLE, BUT EXECUTION TIME IS MUCH TO SLOW
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WE ENJOY YOUR INFO AND WRITE-UPS ON THE SOC.
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D.R. Whaley

PS: OUR LIBRARY HAS ORDERED YOUR JOURNAL.

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December 1, 1981.

THE FIRST TRULY DIFFERENT COMPUTER PERIODICAL OF THE '80's

January of 1982 will see the advent of COMPendium, the only monthly publication that serves every computerist's needs.

There are over twenty major personal computer magazines, publishing over three-hundred articles and program listings a month, for use with over a dozen microcomputers.

COMPendium reviews and catalogs these articles and programs by title, author, computer, length, difficulty, computer language, type and contents. Never again will anyone have to ask "Where did I see that article on producing Sanskrit characters on my XYI-70?" or "Is there on the magazine stands now, or in a recent back issue, information about that super-slow Snail Shell sort I need for my Procrastinator's Club mailing list?".

What! More? Yes: all software, hardware, and book reviews to be found in the major magazines are listed by name, machine, and type (business, utility, game, et al.). **COMPendium's** readers can now track down several evaluations on those purchases they are planning to make, before they make them.

Even more!! There's a growing section called "ADwatch" which lists products which are advertised in the major magazines according to machine and type, and points readers to the magazines where the full ads can be found.

With the proliferation of computer magazines and personal computers, with the growing demand for soft ware and hardware, and with the increasing numbers of people "getting into" micros, it is time for a publication which will organize the mass of information which passes before us -- not yearly, not quarterly, but monthly -- so that the news is fresh and immediately usable. Just the annual December issue, which lists the entire year's articles, will be a best-seller.

The charter price for this indispensable service to individuals, companies, schools, and libraries is \$15.00 per year until February 15, 1982; after that date, the subscription rate for twelve issues -- mailed First Class -- is \$18.00. All subscriptions include the Year-End Review. The Year-End Review alone is \$6.00.

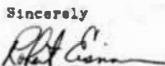
68 Kelly Road
So. Windsor, C.
06074

Dear Editor:

I am constantly reading reviews on the APP Imagination Machine. They all echo the same theme "Lacks Software".

I've been producing programs for this machine for quite some-time. However, with the limited number of machines in use, a national advertisement would be prohibitive.

Would you publish my address so that other APP users may write for a list of available programs.

Sincerely

Robert Eiseman

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INEXPENSIVE PROGRAMS FOR YOUR IMAGINATION MACHINE. GAMES THAT YOU CAN PLAY, MODIFY OR EVEN DISASSEMBLE TO AID IN YOUR OWN PROGRAMMING TECHNIQUE



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(HIGH RES.)

DO YOU LIKE FAST ACTION, SPEED DOWN THE LANE IN A ROADSTER, BUT DON'T FRET AS ACCELERANCE IS STANDING IT.

ZAP!

HIDE THE COMPUTER ROBOT SPACES IN THIS GRAPHIC GAME. (5 SKILL LEVELS)

LOGIC!

BREAK THE CODE IN THIS ONE, MASTERMIND TYPE BOARD GAME

1 or 2 PLAYERS

IDEAL GAME FOR THINKERS

OR THOSE WHO WANT TO BE

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6809 Relocating Recursive Macro Assembler & Loader/Linker with text editor

- Runs on 6809 system (in as little as 32k)
- Interactive or non-interactive (batch) mode
- Supports relocatable and absolute code
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- CROSS ASSEMBLER MODE—can assemble 6800 & 6801 source and generate 6800 & 6801 object
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Powerful Macro Capabilities

- 8 character symbolic (substitution) labels
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- Link and load (with offset)
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MASM 6809 ----- \$ 250.00

User's Manual Only (about 200 pages—refundable)-----
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A version of the above assembler which generates ABSOLUTE code is also available

ASMB 6809 ----- \$ 150.00

The above software is available on 5 or 8 inch FLEX® disks, prices include one year maintenance (single CPU). Even if you already own an assembler you should seriously consider ordering these powerful tools.

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1080 IRIS DRIVE
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404-922-1620

ST-02 VIDEO BOARD

SCREEN FORMAT

- ST-02 HAS FOUR SCREEN FORMATS SWITCH SELECTABLE:
 - 16 x 32
 - 16 x 64
 - 20 x 80
 - 24 x 80

CHARACTER FORMAT

- ST-02 HAS TWO CHARACTER GENERATORS:
 - MC6674 5x7 Matrix
 - 2716 User Programmable 5x7 Matrix
- CHARACTER GENERATORS ARE SWITCH SELECTABLE ON RESET OR MAY BE CHANGED UNDER SOFTWARE CONTROL.

IO INPUT/OUTPUT

- KEYBOARD INPUT IS 7 OR 8 BIT ASCII ENCODED WITH ACTIVE LOW STROBE.
- TERMINAL IS STANDARD RS-232.
- SELECTABLE BAUD RATES OF 300, 600, 1200, 2400, 4800, 9600.
- PRINTER OUTPUT IS PARALLEL 7 OR 8 BIT WITH ACK. THIS PORT MAY BE USED AS SERIAL TO PARALLEL CONVERTER OR MAY BE USED IN SCREEN PRINT FUNCTION.

US SHIPPING \$3.50, FOREIGN ADD 10%
(US FUNDS ONLY)

- THE ST-02 IS A STAND ALONE VIDEO CONTROLLER UTILIZING THE 6802 CPU AND 6845 VIDEO CONTROLLER.
- THE SIZE OF THE BOARD IS 7 1/2" x 8 1/2".
- POWER SUPPLY REQUIREMENTS: 3 amps @ +5 vdc
100 ma. @ +12 vdc
100 ma. @ -12 vdc
- VIDEO OUTPUT IS COMPOSITE VIDEO

CONTROL CHARACTERS

- | | |
|--------------------------|------------------------------|
| CTL J - LINE FEED | CTL L - FORWARD SPACE CURSOR |
| CTL Z - CLEAR SCREEN | CTL M - CARRIAGE RETURN |
| CTL K - UPLINE | CTL N - KEYBOARD UNLOCK |
| CTL G - BELL | CTL O - KEYBOARD LOCK |
| CTL H - BACKSPACE CURSOR | CTL A - HOME CURSOR |

ESCAPE COMMANDS

- | | |
|-------------------------|---------------------------------|
| SEND CURSOR LOCATION | DEACTIVATE PRINTER |
| CURSOR POSITION REQUEST | PRINT SCREEN |
| INVERSE VIDEO | ACTIVATE CRT & PRINTER |
| ACTIVATE PRINTER | SWITCH CHARACTER GENERATOR ROMS |

THESE ARE ONLY A FEW!!!

CURSOR FORMAT

- | | |
|------------------|--------------------|
| BLOCK CURSOR | UNDERLINE CURSOR |
| NON-BLINK CURSOR | BLINKING CURSOR |
| BLINKING BLOCK | BLINKING UNDERLINE |

Assembled

\$325.00

Kit

\$275.00

Char. Gen. 2716 Eprom

\$15.00

Bare Board With Monitor EPROM

\$100.00

Bare Board

\$75.00

Master Charge, Visa, American Express Accepted

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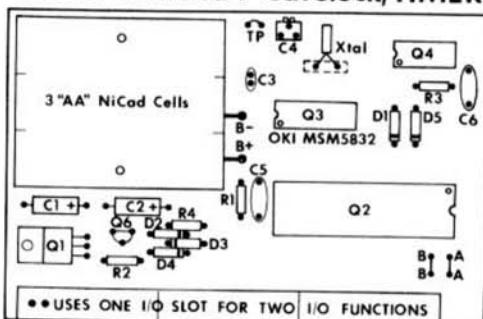
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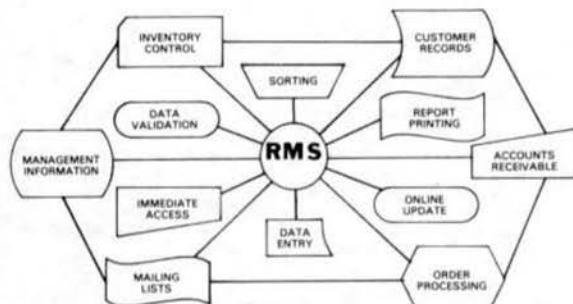
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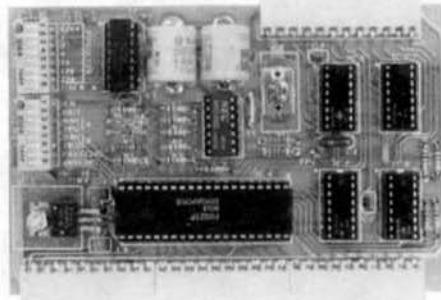


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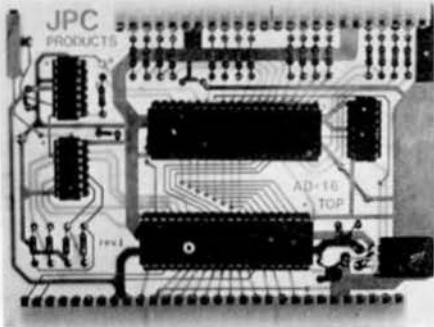


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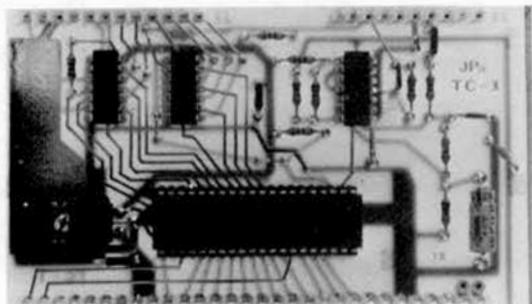
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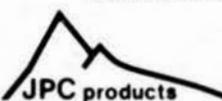
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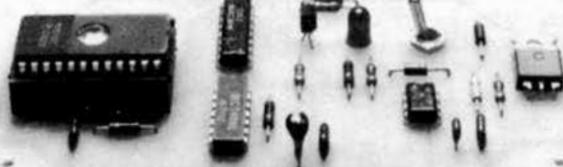
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The software provided gives your computer the power of speech using nothing more than Basic Poet and Poke statements! The SP-1 can add a new dimension to your games, business programs or CAI drifts. Just about any application can benefit from the SP-1!

The SP-1 requires unlimited speech. Also, the SP-1 requires less memory overhead for speech than any other type of unlimited voice synthesizer on the market today. Typically, fewer bytes of memory are needed than the equivalent number of letters in English Text! Basic data statements suffice to store most text you want! Also, no machine language routines are needed to drive it!

The SP-1 comes with sample software in Basic to demonstrate the power of this fantastic device. Alford's even supplies a version of their VOC-EDITOR to allow users who have 16K of memory to edit speech files quickly and easily! (Extended BASIC is NOT required!)

The SP-1 includes a comprehensive manual which provides speech theory, use of the included software, program code charts, sample programs and much more!

COMPUTERS SPEAK TEXT!

Alford's has been on the lookout for a good speech converter program for use with their SP-1 and VS-1 speech synthesizers, and reports having finally found it! The program takes English text in ASCII form, converts it and then directs the synthesized output to either the appropriate control codes, two case conversion tables, text conversion or speaking letters or speaking slant-right phoneme information!

The standard version is designed to work just like OCTACH or OCTEX. In all modes, you call the converter with characters in the A-MODESTER. In conversion mode, characters are accumulated until a word is complete, then it is converted and spoken. In letter mode each character is treated as an ASCII character and pronounced. In phoneme mode, each character is treated as a single inflected phoneme code.

The Color Computer version is even easier to use. Once loaded, Basic has a new verb -- SAY. To use the converter, you simply use the command to make the computer talk: i.e., SAY TRAILER A COMPO-TEK SPKEDER TEST, and the Color Computer will:

TREK-68, COLOR-TREK

Ever since Alford's introduced TREK-68 last year, we've been asking them to do a version for 6800 users and for the new TRS-80 Color Computer. They replied that they could have, but their old home-type computer had not been compatible, so they might not have been compatible. Rather than take a chance, they held off until they decided on which systems they wanted to buy. Well, they finally decided, and at least, our wishes have come true!

If you have a MEMORY-MAPPED DISPLAY board TREK-68 or TREK-88 are for you. If you have a color computer with 16K memory (extended is not required), then you should ask for COLOR-TREK.

They took the classic Trek-game and rewrote it completely in assembly code, making it run in REAL-TIME. The result is, we feel, the finest TREK game available!

Spacey dentro era chase you ever see? You move about the quadrant. Their multiple battle planes make them hard to evade. You dodge torpedoes and return fire. Damage is sustained and repairs occur as you play. Missiles flame on and off. Again, all in REAL TIME!

Game difficulty levels run from SIMPLE (for beginners) to a level which, to our knowledge, only one person other than the developer himself has ever reached. This is not a simple game. The best timing record for the avul SUICIDE OPTION in twenty-nine minutes. The fastest loss we have seen occurred in only eleven seconds!

Have a MEMORY-MAPPED DISPLAY? Then all we can ask is, why haven't you TREK-68?

SPEAKER BARE BOARDS!

Alford and Associates recently lowered the price on its VS-1 synthesizer. A company spokesman stated that the reduction was due to the great response that the VS-1 community has given the board. Now they are going one step further. You can now buy a bare board, manual, disk or system with chip separately. See the price list for details.

**Alford & Assoc.
P.O. Box 6743
Richmond, Va.
23230
804-320-6722**

NEW SCREEN EDITOR!!

Does your terminal have an addressable cursor like the DEC-10-128? Does your terminal scroll when you do a *SEE LINE*, and the bottom line? Does your terminal run full-duplex? If not, then you should skip this ad. Otherwise, you may be ready for SCREDITER III!

How would you like an editor that will handle a 252-column spread sheet? Or one that allows you to move memory anywhere and at any time? Or that handles true multi-column edit jobs like this page? Or that formats text as you type? Or that allows you to set or clear tabs at any time anywhere, with a single keystroke? SCREDITER III!

Like to be able to define what single-key operations you do with what single keys? Or for that matter, what command names you want to give the commands? YOU CAN WITH SCREDITER III!

Wouldn't it be nice to be able to define up to twenty-six editing macros, with a macro length of up to 1000 characters? Or to be able to mix commands, operations and text, all in the same macro? Or store and edit the macros themselves just like text? Or save and load your macros from disk files? YOU CAN WITH SCREDITER III!

How about file handling. Would you like to edit multi-edited files? Or to be able to read selected lines out of one file into another? Or how about conditional preloaded reading to let you see the lines before inserting them? Or be able to write lines out to one files? Or to specify where to start reading or writing, and how much at a time, and how many times? YOU CAN WITH SCREDITER III!

Just about it. Thirty-two control-code operations, about fifty other commands and the number is growing. Twelve justification commands almost ONLY WITH THE ALL-IN-ONE SCREDITER III.

As if all of this was not enough, this new editor is available for FLEX 1.0, FLEX 2.0, TREK-8, DISK8 and DOS8! OS-8 versions to be available soon! maybe as you read this, even!

In talking to John Alford, proprietor of Alford and Associates, we were told that he is tired of writing editors. He indicates that he knows only two ways to stop: get out of the business, or write the ultimate editor. It doesn't appear that he is going out of business soon!

If this hasn't convinced you that you should be using SCREDITER III, then call or write for more details, or for the complete SCREDITER III upon receipt. Our only question is why continue to edit, when you can SCREDITER?

SCREDITER III is available for most serial terminals, and all memory-mapped displays. 6800 and 6809 versions are ready now!

88B DOS UTILITIES

As many of you know, Space Signal Broadcasting's 88B is one of the best around. There was, however, one thing we felt to be lacking...disk names!

Alford and Associates has finally found how to go about naming a disk in a non-destructive and secure way, and started to write some utilities using the disk information record. Their 88B utility allows you to name your disks. The information sector includes the disk name, serial number, creation date, last update date, a comment field, and last but not least, a disk file access code.

The access code led them to the second program, LOCX. With this program you can create, delete, and LIST LOCX your files!

With their LIST program, the list-locked files do not list unless you give the access code for the disk. In addition, you do not have to look at a pile of trash to find the file you want. The LOCX allows you to option to list for certain files. LIST even lets you list the disk information record!

The UPD8T program lets you change the information record! They also include PURGE to clean up disks, DUMP to make pretty card images, and TITLE to print title pages on all of your listings.

The UTILITIES 88 disk is available for DOS8, versions 4.0 and up, and for all versions of OS8. The manual itself is a good reason for buying this package, as it has a batch of information on 88B disk structure.

THE LAST WORD...

This year (our third) has been the most successful ever. I would like to thank all customers for their support and understanding.

Most of all though, I would like to express my thanks to God, who is the ONLY "Associate" in my business. At this time of year it seems especially appropriate to pause to give thanks and to remember the many miracles which He has performed for all of us. Too often, we take His grace for granted.

As this season, we celebrate the miracle of the eternal light and the Miracle of the birth of the Messiah. Both stand for hope for the human race. Both stand for God's light in our life. And both above His love for us, is that He provides for our needs in many ways, especially in times of darkness.

I pray that in this next year, my walk with Him will lead me closer to the place He wants me, and that I might become even better able to serve every one of you, my customers. I also hope that each one of you will pray for me and my family so that we might grow as He wants us to, and that we might be more worthy of your patronage.

Thank you again, in love and in prayer, from all of us, and especially from...

John L. Alford (proprietor)
Sally Anne Alford (most everything else)
Alford and Associates

PRICE LIST

SP-1 SPEAKER-PACK, COMPLETE . . .	\$179.95
SP-1 MANUAL ONLY	19.95
VS-1 SPEAKER, COMPLETE	\$189.95
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VS-1 MANUAL ONLY	19.95
VS-1 VOL-EDIT PROGRAM DISK ONLY . . .	24.95
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TREK-8 DISK, MANUAL	\$24.95
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SEE DOS UTILITIES 88 DISK	\$24.95
SCREDITER III/6809 DISK, MANUAL	\$89.95
SCREDITER III/6800 DISK, MANUAL	89.95
SCREDITER III MANUAL ONLY	24.95

PRICE OF MANUAL REFUNDABLE ON ANY ORDER. PRICES SUBJECT TO CHANGE WITHOUT NOTICE. BE SURE TO SPECIFY DISK SIZE, OPERATING SYSTEM AND PROCESSING TYPE WHEN ORDERING.

GENERAL INFO

All of Alford's software is available on 5- or 8-inch disk except where noted. Also, except where noted, all software is available for FLEX 1.0, FLEX 2.0, FLEX-B, OS8 or DOS8. Versions for OS-8 are coming soon. Software orders are normally shipped within three days. Hardware runs from stock to 30 days.

You should add \$5 for shipping on any order under \$100. Alford's pays shipping over \$100. Overseas orders add \$10 for air mail delivery. Virginia residents add 4% sales tax. Any order received without shipping or tax info will be returned unshipped. Unless you specify otherwise, shipping is by UPS in the U.S.

Alford's accepts Mastercard, Visa, COD's or checks in U.S. funds. Open account orders by prior arrangement only. Personal checks may delay shipping by two or three weeks.

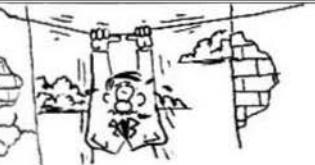
FLEX is a trademark of Technical Systems Consultants. TREK-8 and Color Computer are trademarks of the Tandy Corporation. OS-8 is a trademark of Microware Systems Corporation.

Most people don't realize it, but the total national debt in this country is just now passed one trillion dollars. Today's economists tell us that this is why we have inflation. What they don't tell you is that the total debt is the private sector in over nine trillion dollars! That's right, the private sector is approximately 1.5 times larger than the public sector.

KINGMAN (MP). Today, 46 year old Harley S. Kungnat was observed biting a squirrel dog in Marion Park. When asked by this reporter what prompted such a singular activity, Harley replied that he had been coming to the park daily for fifteen years, and that on each and every outing, the dog had bitten him. At last, "he could stand it no longer."

FUDGY MURKIN. AR -- This reporter can't believe that he had done everything, but found that there is truly something new under the sun, and the honorable Senator Billige Pump was caught in the very act of telling the truth to his constituents.

When queried about his upcoming laws, he stated, "that he did



Software piracy costs each of us. Most people don't realize that the reason most of the software available is pirated is that it does simply because the writer has to increase the price of his product to make up for the lost sales which result from people who think they are doing a good deed for a friend. A positive is that

DISK DRIVE WOES?
PRINTER INTERACTION?
MEMORY LOSS?
ERRATIC OPERATION?

Don't Blame The Software!

Power Line Spikes, Surges &



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Floppies, printers, memory & processor often interact! Our patented ISOLATORS eliminate equipment interaction AND curb damaging Power Line Spikes, Surges and Hash.

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68000

Many of you have asked when we are going to introduce 68000-based products, and on which bus structure. We are pleased to announce our first modular unit for 68000 systems — a 128-kb memory board with VERSABUS™ compatibility. This is one of a range of VERSABUS™ compatible modules that we plan for 1982. We are also watching developments on the VME bus, and may offer products for that bus should sufficient demand develop.

- VERSABUS Plug-compatible
- 128-kb with Parity Generation and Checking
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- LED STATUS LIGHTS indicate the following hardware conditions:
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 - 1 - Data Parity Error
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*VERSABUS is a Trademark of Motorola, Inc.



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Now your 6800 or 6809 system can proof-read your text files and fix your spelling and typographical errors in just minutes.

MAGIC SPELL™ compares each word in your file against a dictionary, displays or prints every word not found, and lets you correct it on the spot. The result is an error-free text within minutes.

MAGIC SPELL™ is written for the non-technical user. It has several options, including listing to a terminal or printer, correcting or marking errors, adding new words to the dictionary, or generating a custom dictionary to fit your writing style. Although it comes with a complete manual which explains every option in plain language, MAGIC SPELL prompts for all the information it needs so even beginners can use it without constantly referring to the manual.

MAGIC SPELL™ is extremely fast and compact. It will run in systems as small as 16K, can correct documents up to ten times larger than available memory, and reads text at a rate of about ten pages per minute. MAGIC SPELL™ is in stock now for Technical Systems Consultants' MiniFlex, 6800 Flex, and 6809 Flex, as well as for Percom Disk systems. OS-9 and SSB versions will be available soon. It is available in two versions:

MAGIC SPELL I is for the general user. It comes with a 10,000 word dictionary, and costs \$89.29. This is the version we ourselves use.

MAGIC SPELL II is for the professional writer who demands the very best. It contains several speed enhancements, and features a 75,000 word dictionary. This version is available only for 6809 systems, and costs \$239.29.

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Back panel has 10 cutouts for "D" type data connectors
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Without power supply	\$250.00
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Powers 2 5-1/4" floppy drives: \$50
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ELEKTRA CPU 8/9

Choice of 6808 or 6809 CPU
(6808 is software compatible with the 6800 at the opcode level).
DEVICE 6809 ADDRESS 6808 ADDRESS
3 2716 Eprams Eeprom #3 F800-FFFF F800-FFFF and E000-E7FF
 Eeprom #2 F000-F7FF F000-F7FF
 Eeprom #1 E800-EFFF E800-EFFF
1K Scratchpad RAM E400-E7FF A400-A7FF and A000-A3FF
MC6840 Triple Timer E210-E217 8200-8207
MC14411 Baud Rate Generator producing baud rates of:
Low Range 110, 150, 300, 600, 1200, 4800, and 9600
High Range 440, 600, 1200, 2400, 4800, 19200, and 38400
The board does not contain a DAT and does not support extended addressing.
The board supports DMA by either HALT or BUSREQ when a 6809 CPU is used.
DMA to the devices on the CPU card is not supported.

The board will run any of the MKBUG™ compatible monitors in the 6808 mode and SDUG-E, HUMBUG (Special Version), and GMBUG-09 in the 6809 mode. The ELECTRA CPU 8/9 will run any of the GMIX® disk controller boards with the appropriate GMIX® version of FLEX™. A special version of OS-9™ L-1 is available.

Bare board: \$50.00* Kit: \$225.00* Assembled: \$275.00

ELECTRA DPS Dual Port Serial Card

Fits the standard 30 pin SS-50 bus I/O slot.
Can be configured for 4 addresses per port with the B port 2 addresses higher than the A port or for 16 addresses per port with the B port 4 addresses higher than the A port.
Each port is terminated at two 16 pin dip sockets, one socket configured for modem and the other socket configured for terminal or printer. RTS, CTS, DTR, DCD, DTR are appropriately implemented.
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Bare board: \$20.00* Kit: \$60.00* Assembled: \$80.00
Assembled cable (two required for each interface board): \$20.00 each

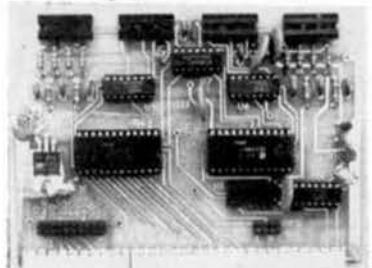
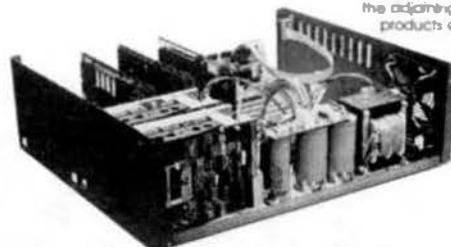
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Bare board: \$20.00* Kit: \$60.00* Assembled: \$80.00
Assembled cable (two required for each interface board): \$20.00 each

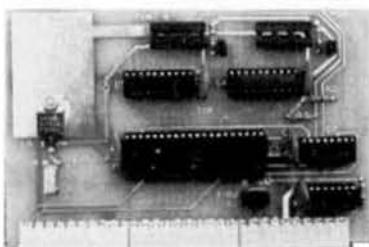
ELEKTRA COMPUTER PRODUCTS



The CPU, 512k memory board, and DMA controller board in the adjoining picture are products of GMIX, Inc.



DPS Dual Port Serial



DPP Dual Port Parallel

*WARNING

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We have introduced our line of computer equipment with the purpose of offering the highest quality of components possible at affordable prices. For those of you whose needs dictate the state of the art in technology, we recommend the GMIX line. When practical, our equipment is intended to be upward compatible to GMIX equipment. We do not intend to offer a complete line of equipment and we recommend that you choose GMIX components to round out your system.

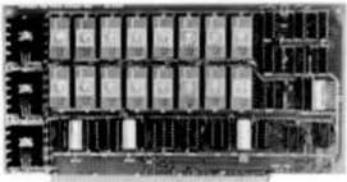
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SPECIAL: 2718 EPROM's (450 NS) Are \$9.95 Ea. With Above Kit.

KIT FEAT RES

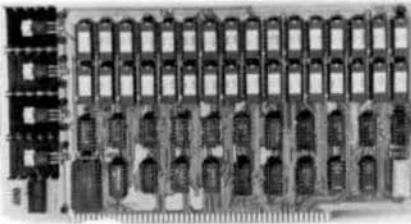
1. Uses +5V only 2718 (2Kx8) EPROM's.
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FOR 4MHZ
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KIT FEATURES:

1. Addressable as four separate 4K Blocks.
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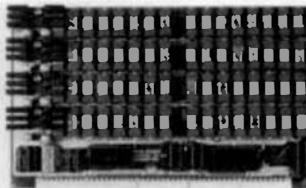
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For S TPC
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Fully Assembled,
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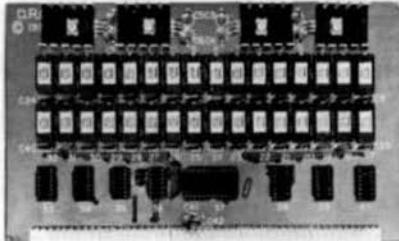
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\$139.95
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1. Addressable on 16K Boundaries
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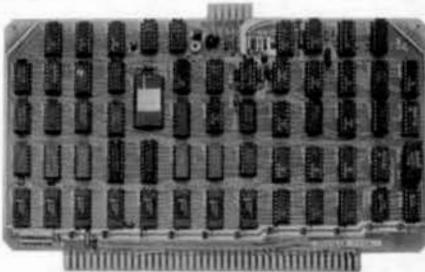
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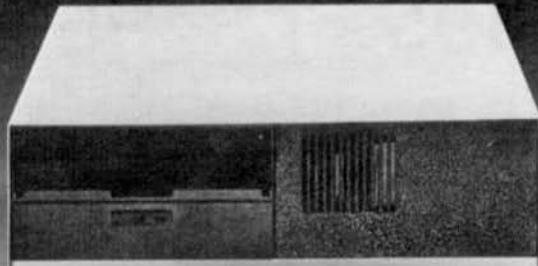
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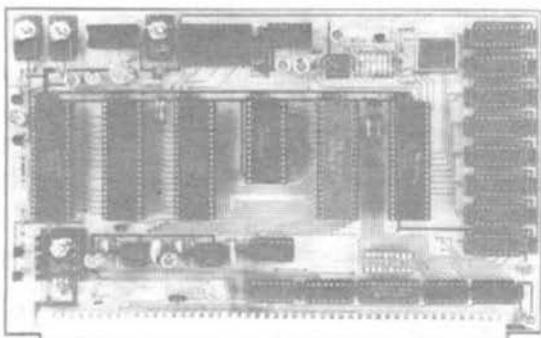
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Double head, single or double density capability, 80 X 2 tracks			470.00
MP-1 - 5 1/4" Manual			20.00
ELEKT A A dual drive cabinet for 5 1/4" drives with power supply, line cord, fuse, power switch, and power cables to drives			125.00
Dual Drive cabinet and power supply for 8" drives			350.00
Microtime 6800 Calendar and Clock Board (assembled and tested)			105.00
Barcode, connector, and documentation only of above			35.00
(See review Feb. 1980 '88 Micro Journal)			
Microtime II			89.95
Data Mart 16K EPROM bareboard (2708 chips)			30.00
Printers			
Epson MX-80 (Centronics compatible Parallel Interface) (with Serial RS-232 interface option)		add	495.00
Spare Print Head			75.00
Spare ribbon cartridge			39.95
C. Ioh Comet t 125 cps. 9 x 7, bidirectional, serial or parallel			15.00
GIMIX (The Ultimate)			445.00
6800 CPU Board			224.03
with timer			288.06
without baud rate option		add	30.00
with 2MHz option		add	15.00
2 MHz 6809 Plus CPU, time of day clock, battery backup, 1K NMOS	AM		578.05
CMOS			8.00
AM substitution			35.00
GIMIX Dynamic address Translator			15.00
SWTPC compatible DAT			312.00
6511A Arithmetic Processor (4MHz)			265.00
9512 Arithmetic Processor (3MHz)			98.65
GIMBUG-09 (Terminal Based) 1K scratchpad required			30.00
Bootstrap PR			30.00
Video Prom (includes bootstrap)			30.00
Manual and Source Listing only			38.62
Mailing cycle detect card			38.23
Disk Controllers (All have data separators and can be used with either single or double headed drives)			
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5" and 8" single density controller complete			226.58
5" double density controller with variable precomp			348.28
DMA 5" AND 8" double density controller with variable precomp			588.68
GIMIX version of FLEX™ (without Editor and Assembler)			90.00
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Ribbon cable for two 8" disk drives (long)			44.25
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CMOS WITH NMOS NO BAT. BACKUP BAT. BACKUP			
16K Static RAM Board with control registers*			368.16
32K Static RAM Board with 16K of RAM installed			298.12
*discontinued limited quantity available			
64K Static RAM Board with 24K of RAM installed		N/A	348.27
64K Static RAM Board with 32K of RAM installed		648.36	398.37
64K Static RAM Board with 48K of RAM installed		N/A	518.47
64K Static RAM Board with 56K of RAM installed		694.56	578.57
64K Static RAM Board with 64K of RAM installed		988.64	838.87
16 Socket EPROM/ROM/RAM Board			238.32
8K Promboard (2708)			98.34
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Dual port 30 pin serial interface (Requires 2 cable sets)			128.43
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Cable sets for above boards (specify board)			22.95
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80 X 24 without RAM character generator			398.74
80 X 24 with RAM character generator			458.76
High resolution (512 X 512 dot resolution)			996.77
2MHz 6809 PLUS Computer System with 56K Memory*			2498.29
Above System with #58 Controller and Special Software Pkg.			2988.59
Above System with #68 Controller and Special Software Pkg.			3248.49
*with CMOS RAM and Battery Backup		add	300.00
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This ad is our catalog.

See GIMIX Ad Pages 3 & 4

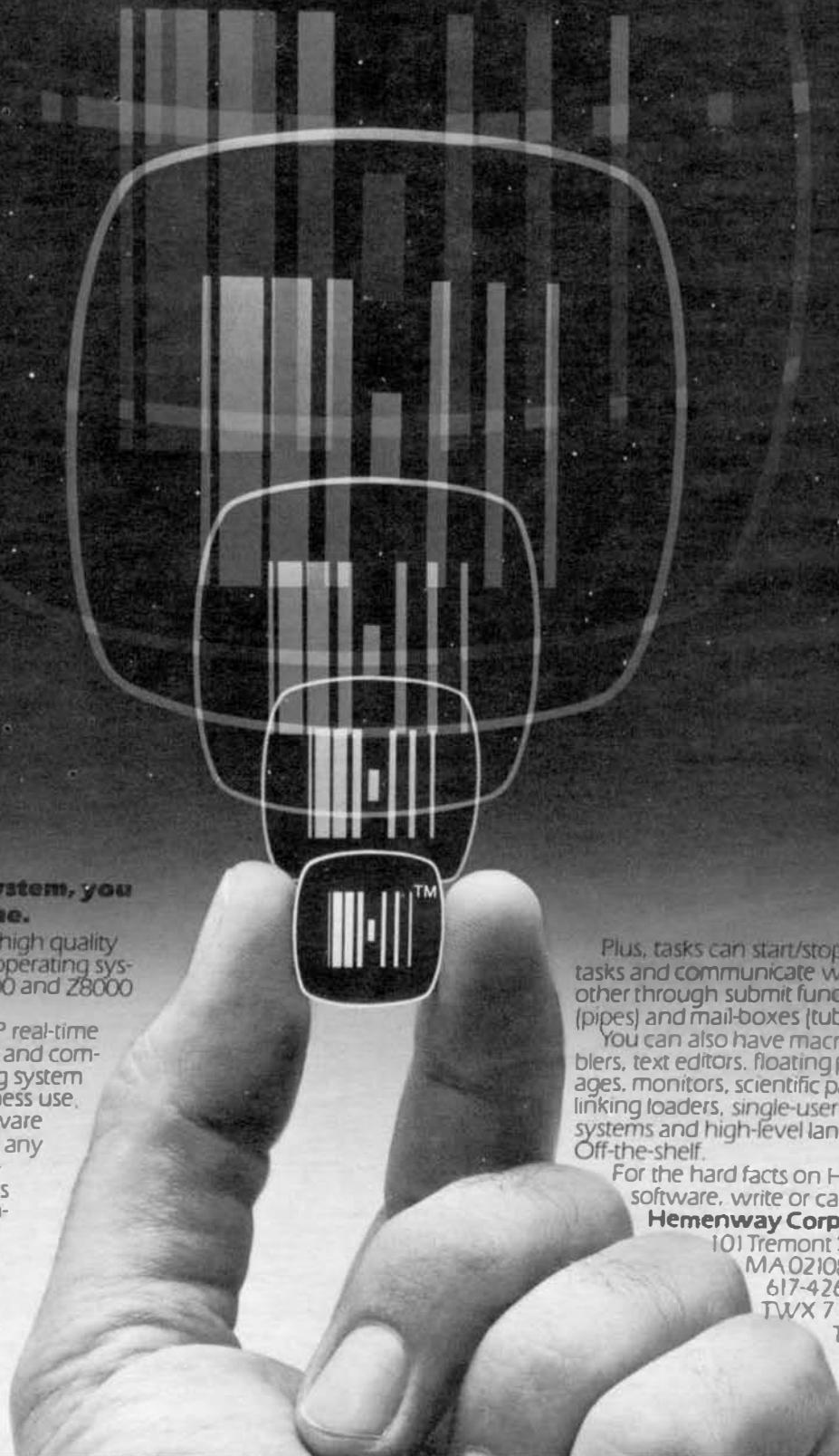
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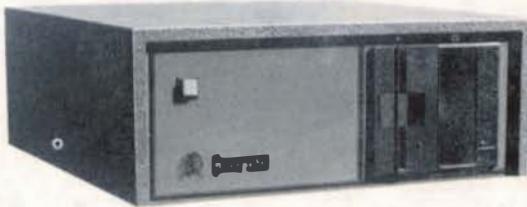
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The Chieftain series includes 5 1/4- and 8-inch Winchesters that range from 4- to 60-megabyte capacity, and higher as technology advances. All hard disk Chieftains include 64-k memory with two serial ports and DOS69D disk operating system.

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Average access time for 5 1/4-inch Winchesters is 70 msec., comparable to far more costly hard disk systems. That means data transfer *ten-times faster* than floppy disk systems.



**Write or call today
for details (including the
remarkably low prices)
on the total Chieftain
Series . . . and on
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• 2-MHZ OPERATION

All Chieftains operate at 2-MHz, regardless of disk storage type or operating system used. Compare this to other hard disk systems, no matter **how** much they cost!

• DMA DATA TRANSFER

DMA data transfer to-and-from tape and disk is provided for optimum speed. A special design technique eliminates the necessity of halting the processor to wait for data which normally transfers at a slower speed, determined by the rotational velocity of the disk.

• RUNS UNDER DOS OR OS-9

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Winchester with tape or floppy back-up . . . they all run under DOS or OS-9 with **no need** to modify hardware or software.

• UNBOUNDED FLEXIBILITY

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• SMOKE SIGNAL'S HERITAGE OF EXCELLENCE

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Here are the Chieftain 6809-based hard disk computers that are destined to change the data processing industry . . .

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15-megabyte, 5 1/4-inch Winchester with a 1-megabyte 8-inch floppy disk drive.

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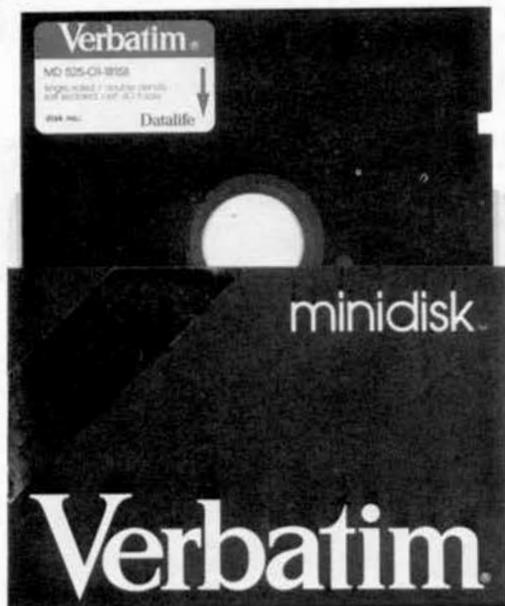
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Based on Food Exchanges

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Sex, Age, Height, Present Weight, Frame Size, Activity Level and Basal Metabolic Rate for normal individuals are taken into account. Ideal weight and sustaining calories for any weight of the above individual are calculated. When a weight goal is given (either gain or loss), and a calorie plan is agreed upon between the computer and the individual, the number of days to reach the weight goal is projected. The starting and ending rate of weight loss is calculated, and a daily calendar with each day's predicted weight for a 30-day period is printed.

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A complete Data Management System which permits files up to 1000K precision BCD arithmetic, Multi-key access, selection and sorting. DMS2/VM employs a virtual memory access method under which programs "think" that entire files are in memory and directly accessible. The system supports alphanumeric, numeric, decimal, Integer, coded and hexadecimal field types. Up to 24 fields and 12 levels per file may be defined by the user. A simple high-level command language allows a variety of data manipulation including reformatting, calculations, inquiry, key-merge, summation, print and display of database data.

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All essential accounting and bookkeeping functions including journal, ledger, income statement and balance sheet. The user defines accounts, products and transactions to the system and thus tailors it to his own retail, wholesale or service environment. The system operates under DMS2/VM which permits custom reports of product movement or account status to be generated. Accounts receivable and payable are integral to the system at a point-of-sale capability.

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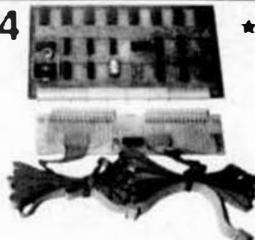
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A FLEX compatible Multiple Fixed Task operating system. Allows user definition of region size for up to eight terminals/tasks and simultaneous execution of commands from all terminals.

All software runs on SWTPC 6809 with 56K or more memory and 8" disk. Written in modular assembler, requires FLEX operating system. Manuals available, DMS2/VM \$10, Accounting \$15, deduct from order. Add P&H \$2.50, Foreign \$5.00, U.S. State add sales tax. No C.O.D., Send Check or Money Order to:

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48K 2MHz STATIC RAM/ROM CARD

- *24 2K blocks memory mapped on any 2K boundary
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- *gold connectors

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NEW ACCESSORIES FOR 68XX USERS:

SS-50/SS-50C EXTENDER CARD	\$35.00
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*Both cards assembled with a built in logic aid & gold edge connectors

SS-30 WIRE-WRAP/PROTOTYPE BOARD (board only)	\$20.00
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*Pad spacing permits most standard sockets from 8 to 64 pins

*Provision has been made for voltage regulators

FEATURED PRODUCT: SP-1 Bare card \$49.00 Asm. + tested \$195.00

*A super prototype board *Card design includes

- (3) 6821 6 parallel ports
 - (4) 6850 4 serial ports
 - (1) 6840 3 16 bit counter/timers
- which are fully buffered and decoded

*Accommodates a mix of 38, 14 & 16 pin wire wrap sockets

*Pad spacing permits most standard sockets from 8 to 64 pins

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special parts kit

A/T without extra features

*SUPER CPU assembled with source listing
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*Monitor in two 2708 EPROMS

*CPU bare card, doc., & src.

*VIDEO RAM asm. 7x9 chars 64x16

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*PARALLEL I/O asm 100 I/O lines
incl. 5 PIA's for 10 ports

*PARALLEL I/O bare card & doc.

*SS-50 WIRE-WRAP/PROTOTYPE bare

*TRANSITION CARD asm.

*TRANSITION CARD bare

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Only GIMIX offers you SOFTWARE SWITCHING between MICROWARE's OS-9 and TSC's FLEX. Plus you get the power of the GMXBUG system monitor with its advanced debugging utility, and memory manipulation routines. A wide variety of languages and other software is available for these two predominant 6809 Disk Operating Systems.

You can order a system to meet your needs, or select from the 6809 Systems featured below.

JUDGE THE FEATURES AND QUALITY OF GIMIX 6809 SYSTEMS

GIMIX' CLASSY CHASSIS™ is a heavyweight aluminum mainframe cabinet with back panel cutouts to conveniently connect your terminals, printers, drives, monitors, etc. A 3 position keyswitch lets you lock out the reset switch. The power supply features a ferro-resonant constant voltage transformer that supplies 8V at 30 amps, +15V at 5 amps, and -15V at 5 amps to insure against problems caused by adverse power input conditions. It supplies power for all the boards in a fully loaded system plus two 5 1/4" drives (yes! even a Winchester) that can be installed in the cabinet. The Mother board has fifteen 50 pin and eight 30 pin slots to give you the most room for expansion of any SS50 system available. 11 standard baud rates from 75 to 38.4K are provided and the I/O section has its own extended addressing to permit the maximum memory address space to be used. The 2 MHz 6809 CPU card has both a time of day clock with battery back-up and a 6840 programmable timer. It also contains 1K RAM, 4 PROM/ROM/RAM sockets, and provides for an optional 9511A or 9512 Arithmetic Processor. The RAM boards use high speed, low power STATIC memory that is fully compatible with any DMA technique. STATIC RAM requires no refresh timing, no wait states or clock stretching, and allows fast, reliable operation. The system includes a 2 port RS232 serial interface and cables. All GIMIX boards use gold plated bus connectors and are fully socketed. GIMIX designs, manufactures, and tests in-house its complete line of products. All boards are twice tested, and burned in electrically to insure reliability and freedom from infant mortality of component parts. All systems are assembled and then retested as a system after being configured to your specific order.

56KB 2MHZ 6809 SYSTEMS WITH GMXBUX/FLEX/OS-9 SOFTWARE SELECTABLE

With #58 single density disk controller	\$2988.59
With #68 DMA double density disk controller	\$3248.49
to substitute Non-volatile CMOS RAM with battery back-up, add	300.00
for 50 Hz export power supply models, add	30.00

Either controller can be used with any combination of 5" and/or 8" drives, up to 4 drives total, have data recovery circuits (data separators), and are designed to fully meet the timing requirements of the controller I.C.s.

5 1/4" DRIVES INSTALLED IN THE ABOVE with all necessary cables

	SINGLE DENSITY	DOUBLE DENSITY	
40 track (48TPI) single sided	Formatted 199,680	Unformatted 250,000	Formatted 341,424
40 track (48TPI) double sided	399,360	500,000	500,000
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80 track (96TPI) double	808,960	1,000,000	726,064
			2 for \$700.00
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			2 for 900.00
			2 for 1300.00

Chart shows total capacity in Bytes for 2 drives.

Contact GIMIX for price and availability of 8" floppy disk drives and cabinets; and 5" and 8" Winchester hard disk system.

128KB 2MHz 6809 DMA Systems for use with TSC's UNIFLEX or MICROWARE's OS-9 Level 2

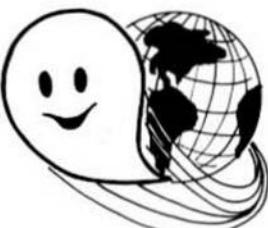
(Software and drives not included)	\$3798.39
to substitute 128KB CMOS RAM with battery back-up, add	600.00
for each additional 64KB NMOS STATIC RAM board, add	639.67
for each additional 64KB CMOS STATIC RAM board, add	988.64
for 50 Hz export power supply, add	30.00

NOTE: UNIFLEX can not be used with 5" minifloppy drives.

GIMIX has a wide variety of RAM, ROM, Serial and Parallel I/O, Video, Graphics, and other SS50 bus cards that can be added now or in the future. Phone or write for more complete information and brochure.

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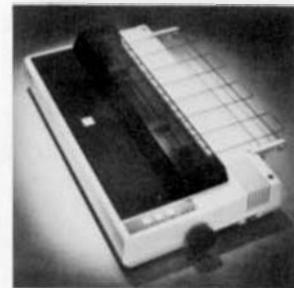
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The Epson MX-80 The Epson MX-100



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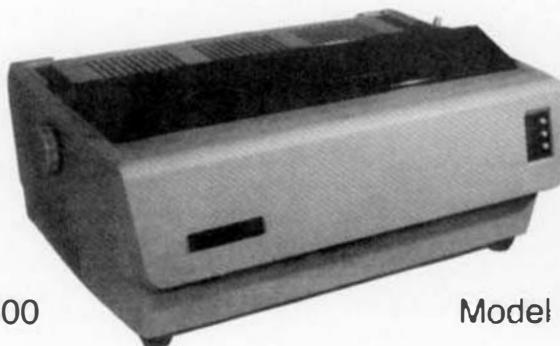
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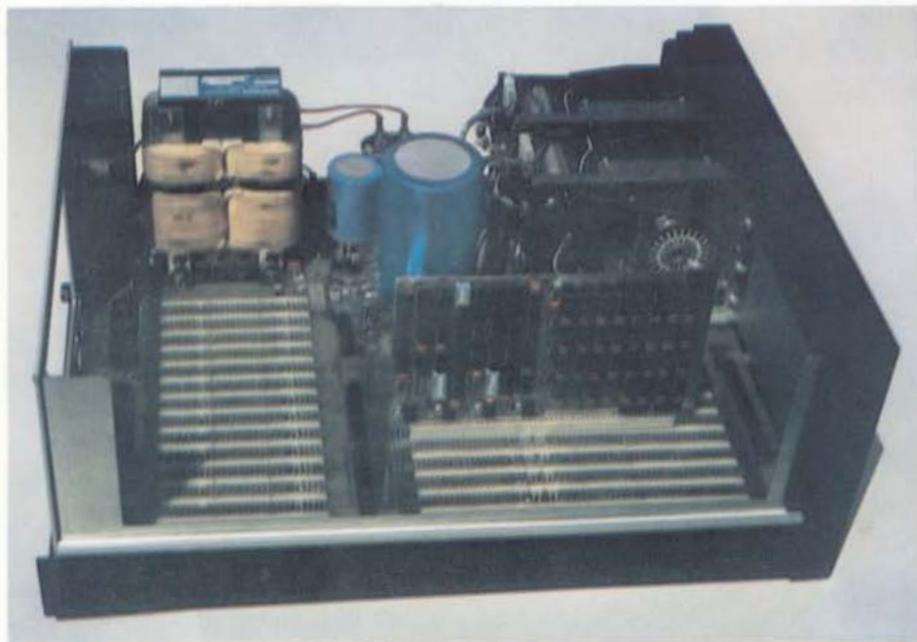
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- Standard 1K Scratchpad RAM
- Standard Clock/Calendar with Battery
- Provision for Programmers Console

68000

- Standard 8 MHz Operation
- Memory Management Hardware
- Provision for Programmers Console
- 16 Bit Power and 8 Bit Compatibility

The HELIX™ computer system represents the latest advance in S-50 bus computer systems. Relying on the physical nature of S-50 bus connectors to guarantee compatibility, the HELIX adds 14 bus lines (becoming S-64) to allow a 68000 processor to operate with full 16 bit data transfer and 24 bit addressing, while at the same time providing full interchangability with existing S-50 components.

Offered with a selection of processors, memories, and peripheral controllers, a HELIX system can be configured for applications ranging from advanced hobbyist to multiterminal time-sharing.

Designed to offer the utmost in speed, reliability, and utility at a reasonable price, it represents a new standard of quality for those who require a professionally designed computer for professional use.

THE POWER SUPPLY

- Ferro-resonant Transformer for Line Noise and Under-Voltage Protection
- Conservative 25 Amps at 8.5 Volts
- Conservative 5 Amps at ±16 Volts
- Conservative Component Rating for Reliability

THE COMPONENTS

- Fully Socketed
- Gold Plated Bus Connectors
- Only "B" Series 68XX Components Used
- Only Top Grade Logic Circuits Used
- Industrial Grade Components Throughout

THE MEMORIES

DM-64

- Field Proven
 - Proprietary Memory Control Logic
 - Fully Transparent Refresh
 - Tested at 2.5 MHz Operation
- DM-512
- 512K Bytes on a Single S-64 Board
 - 16 Bit Power and 8 Bit Compatibility
 - Runs in Existing S-50 Systems where Physical Space Allows
 - Full 24 Bit Addressing
 - Fully Transparent Refresh

THE PRICES

Because of the variety of configurations possible, full pricing cannot be given. Representative prices are:

- | | |
|--------------------------|--------|
| • 64K 6809 HELIX | \$1995 |
| • 64K 68000 HELIX | \$2595 |
| • 512K 6809 HELIX | \$4450 |
| • 512K 68000 HELIX | \$4995 |

HAZELWOOD COMPUTER SYSTEMS

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(314) 837-3466

Dealer and OEM Inquiries Invited. We support our Dealers.

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